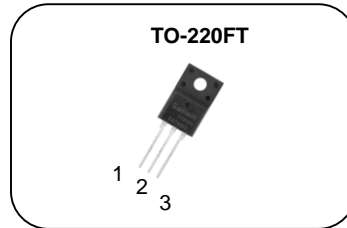


N-channel Enhanced mode TO-220FTN MOSFET

Features

- High ruggedness
- Low $R_{DS(ON)}$ (Typ 1.1Ω)@ $V_{GS}=10V$
- Low Gate Charge (Typ 30nC)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Application:Charger,LED,PC Power



1. Gate 2. Drain 3. Source

BV_{DSS} : 650V
I_D : 7A
$R_{DS(ON)}$: 1.1Ω

General Description

This power MOSFET is produced with advanced technology of SAMWIN.

This technology enable the power MOSFET to have better characteristics, including fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics.



Order Codes

Item	Sales Type	Marking	Package	Packaging
1	SW YN 7N65D	SW7N65D	TO-220FTN	TUBE

Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{DSS}	Drain to source voltage	650	V
I_D	Continuous drain current (@ $T_C=25^{\circ}C$)	7*	A
	Continuous drain current (@ $T_C=100^{\circ}C$)	4.4*	A
I_{DM}	Drain current pulsed (note 1)	28	A
V_{GS}	Gate to source voltage	±30	V
E_{AS}	Single pulsed avalanche energy (note 2)	430	mJ
E_{AR}	Repetitive avalanche energy (note 1)	40	mJ
dv/dt	Peak diode recovery dv/dt (note 3)	5	V/ns
P_D	Total power dissipation (@ $T_C=25^{\circ}C$)	27.8	W
	Derating factor above 25°C	0.22	W/°C
T_{STG}, T_J	Operating junction temperature & storage temperature	-55 ~ + 150	°C
T_L	Maximum lead temperature for soldering purpose, 1/8 from case for 5 seconds.	300	°C

*. Drain current is limited by junction temperature.

Thermal characteristics

Symbol	Parameter	Value	Unit
R_{thjc}	Thermal resistance, Junction to case	4.5	°C/W
R_{thja}	Thermal resistance, Junction to ambient	50	°C/W

Electrical characteristic (T_J= 25°C unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
Off characteristics						
BV _{DSS}	Drain to source breakdown voltage	V _{GS} =0V, I _D =250uA	650			V
ΔBV _{DSS} / ΔT _J	Breakdown voltage temperature coefficient	I _D =250uA, referenced to 25°C		0.51		V/°C
I _{DSS}	Drain to source leakage current	V _{DS} =650V, V _{GS} =0V			1	uA
		V _{DS} =520V, T _J =125°C			50	uA
I _{GSS}	Gate to source leakage current, forward	V _{GS} =30V, V _{DS} =0V			100	nA
	Gate to source leakage current, reverse	V _{GS} =-30V, V _{DS} =0V			-100	nA
On characteristics						
V _{GS(TH)}	Gate threshold voltage	V _{DS} =V _{GS} , I _D =250uA	2.5		4.5	V
R _{DS(ON)}	Drain to source on state resistance	V _{GS} =10V, I _D = 3.5A, T _J =25°C		1.1	1.4	Ω
		V _{GS} =10V, I _D = 3.5A, T _J =125°C		2.4		Ω
G _{fs}	Forward transconductance	V _{DS} =30 V, I _D =3.5A		6.3		S
Dynamic characteristics						
C _{iss}	Input capacitance	V _{GS} =0V, V _{DS} =25V, f=1MHz		1230		pF
C _{oss}	Output capacitance			108		
C _{rss}	Reverse transfer capacitance			16		
t _{d(on)}	Turn on delay time	V _{DS} =350V, I _D =7A, R _G =25Ω (note 4,5)		16		ns
t _r	Rising time			36		
t _{d(off)}	Turn off delay time			83		
t _f	Fall time			40		
Q _g	Total gate charge	V _{DS} =520V, V _{GS} =10V, I _D =7A (note 4,5)		30		nC
Q _{gs}	Gate-source charge			5		
Q _{gd}	Gate-drain charge			15		
R _g	Gate resistance	V _{DS} =0V, Scan F mode		1.7		Ω

Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I _S	Continuous source current	Integral reverse p-n Junction diode in the MOSFET			7	A
I _{SM}	Pulsed source current				28	A
V _{SD}	Diode forward voltage drop.	I _S =7A, V _{GS} =0V			1.4	V
t _{rr}	Reverse recovery time	I _S =7A, V _{GS} =0V, di _f /dt=100A/us		436		ns
Q _{rr}	Reverse recovery charge				3.6	

※. Notes

1. Repeitative rating : pulse width limited by junction temperature.
2. L = 17.5mH, I_{AS} = 7A, V_{DD} = 50V, R_G=25Ω, Starting T_J = 25°C
3. I_{SD} ≤ 7A, di/dt = 100A/us, V_{DD} ≤ BV_{DSS}, Starting T_J =25°C
4. Pulse Test : Pulse Width ≤ 300us, duty cycle ≤ 2%.
5. Essentially independent of operating temperature.

Fig. 1. On-state characteristics

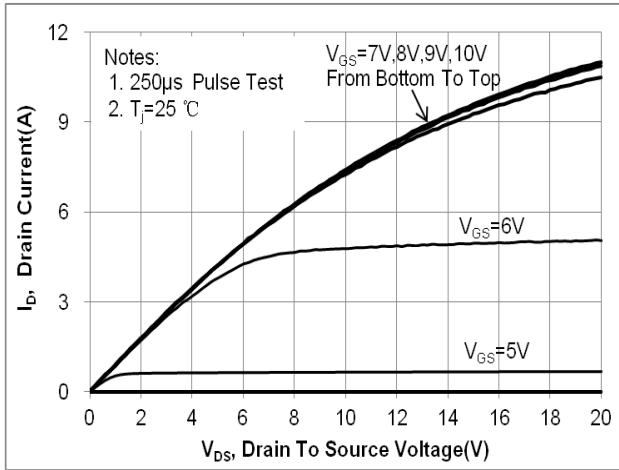


Fig. 2. Transfer Characteristics

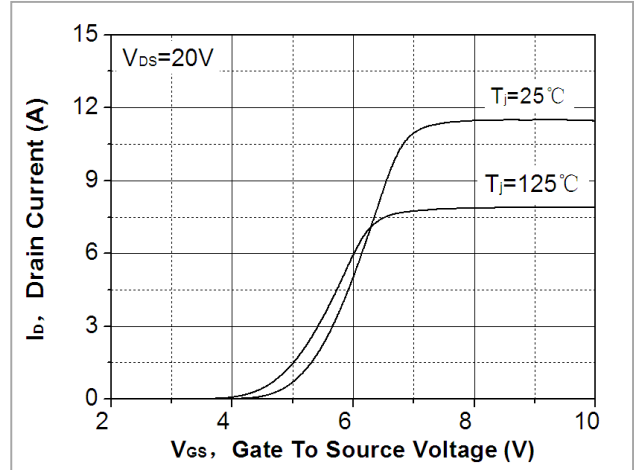


Fig. 3. On-resistance variation vs. drain current and gate voltage

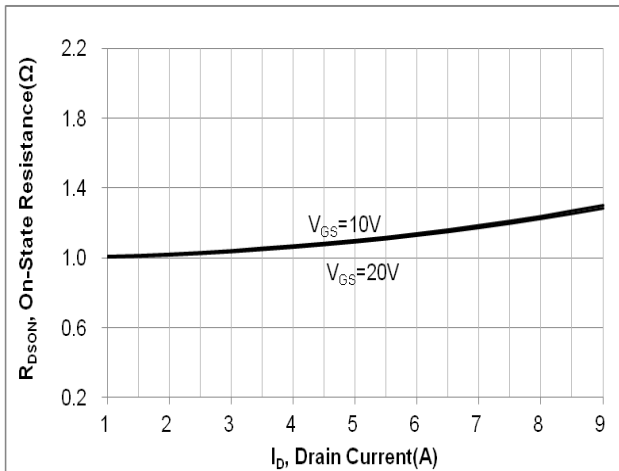


Fig. 4. On-state current vs. diode forward voltage

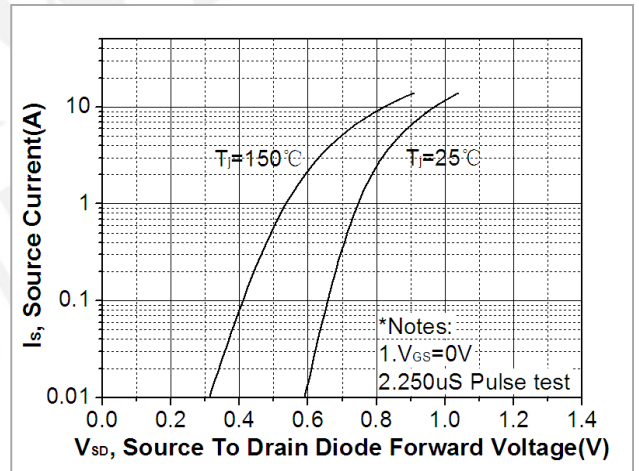


Fig 5. Breakdown Voltage Variation vs. Junction Temperature

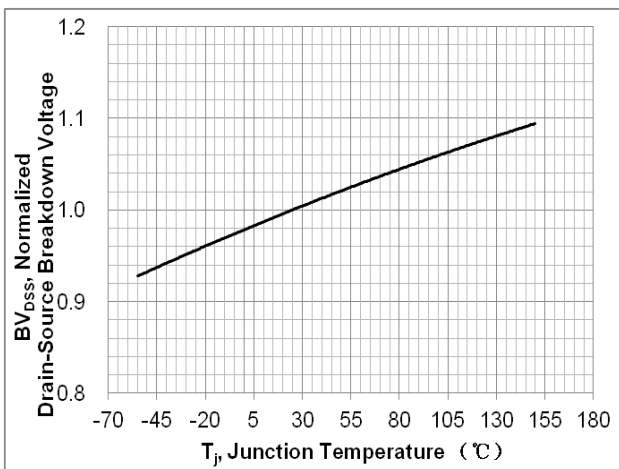


Fig. 6. On resistance variation vs. junction temperature

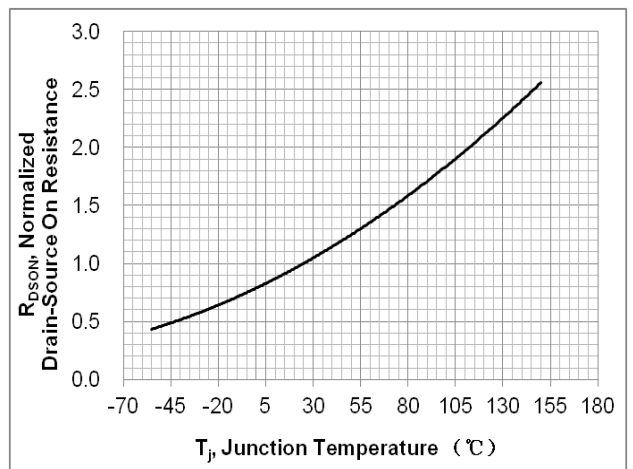


Fig. 7. Gate charge characteristics

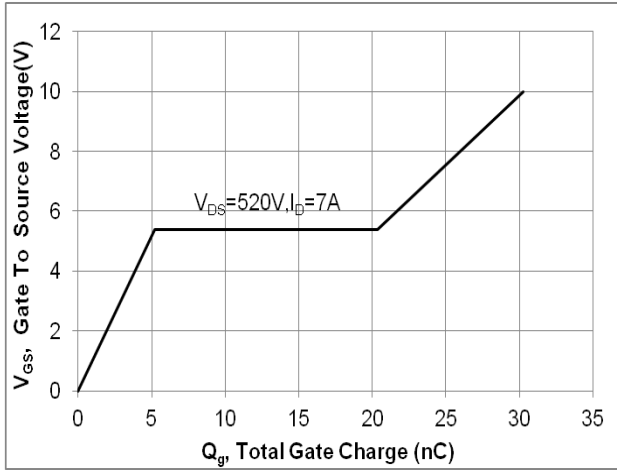


Fig. 8. Capacitance Characteristics

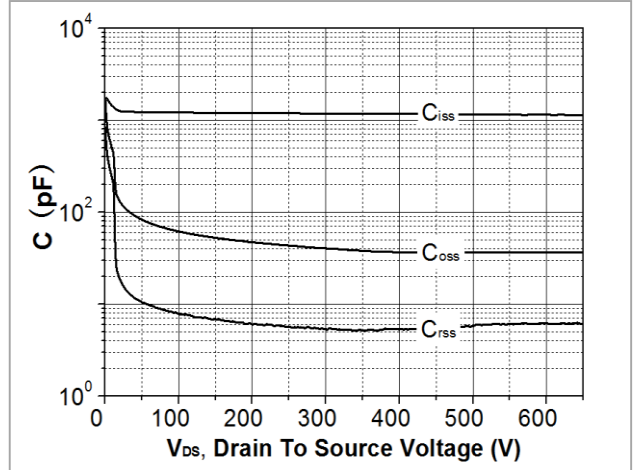


Fig. 9. Maximum safe operating area

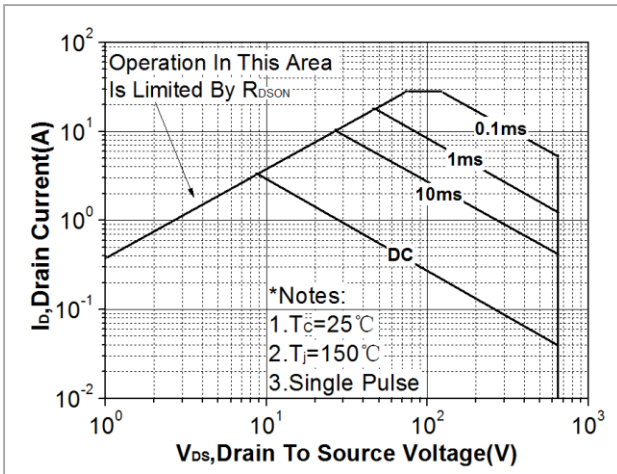


Fig. 10. Transient thermal response curve

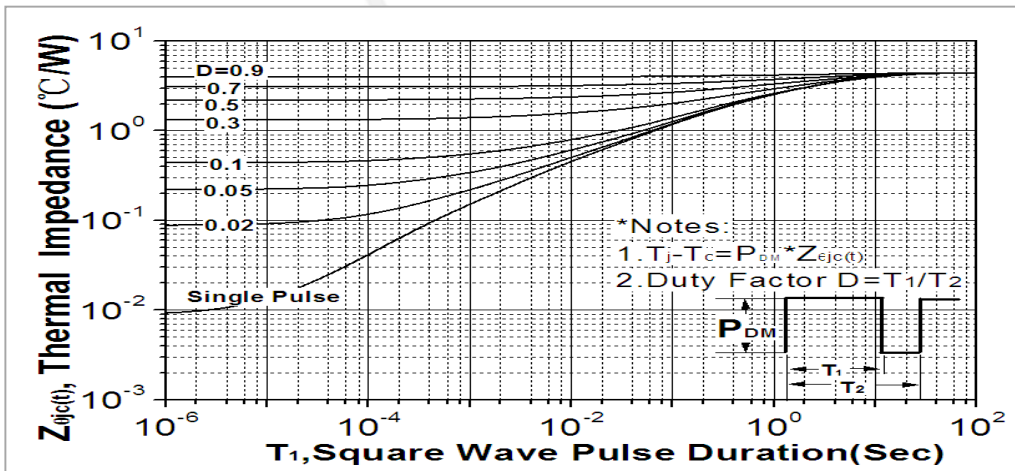


Fig. 11. Gate charge test circuit & waveform

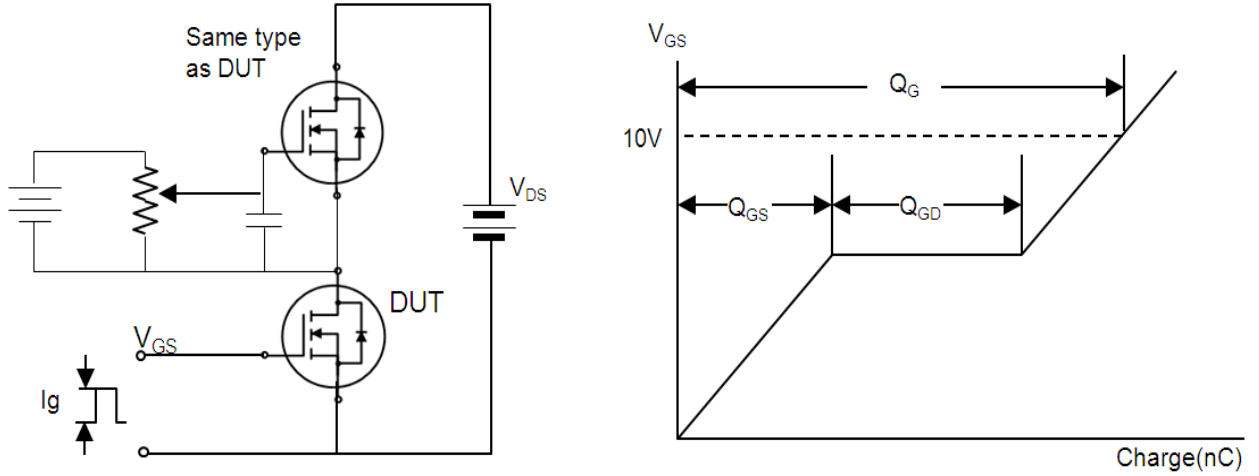


Fig. 12. Switching time test circuit & waveform

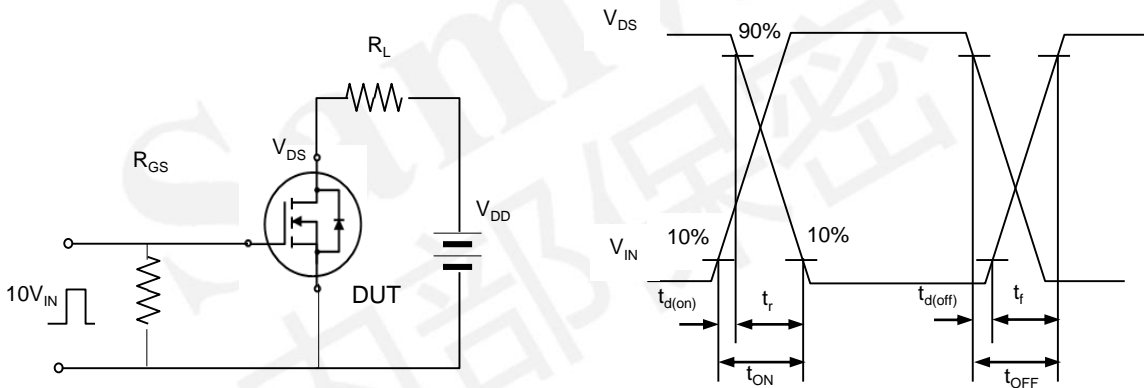


Fig. 13. Unclamped Inductive switching test circuit & waveform

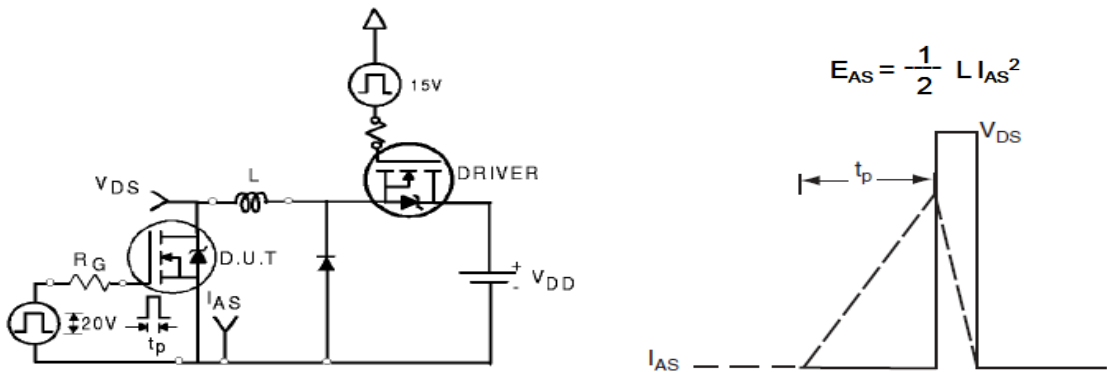
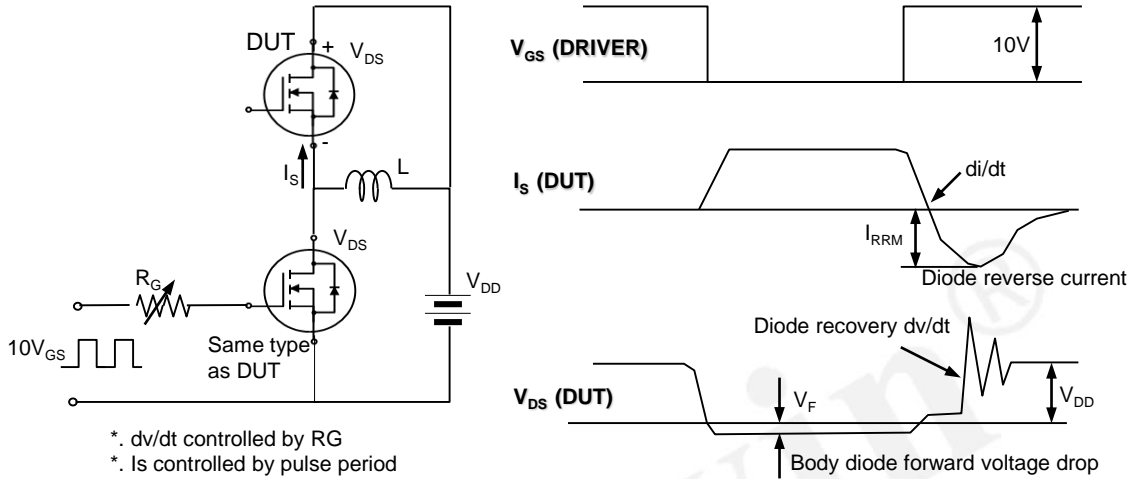


Fig. 14. Peak diode recovery dv/dt test circuit & waveform



DISCLAIMER

* All the data & curve in this document was tested in XI'AN SEMIPOWER TESTING & APPLICATION CENTER.

* This product has passed the PCT,TC,HTRB,HTGB,HAST,PC and Solderdunk reliability testing.

* Qualification standards can also be found on the Web site (<http://www.semipower.com.cn>) 

* Suggestions for improvement are appreciated, Please send your suggestions to samwin@samwinsemi.com