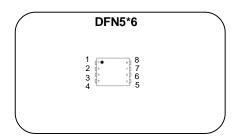


N-channel Enhanced mode DFN5*6 MOSFET

Features

- High ruggedness
- Low $R_{DS(ON)}$ (Typ $9.9m\Omega$)@ V_{GS} =4.5V (Typ $8.3m\Omega$)@ V_{GS} =10V
- Low Gate Charge (Typ 49nC)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Application: Electronic Ballast, Motor Control Synchronous Rectification, Inverter



4.Gate 5,6,7,8.Drain 1,2,3.Source

 $BV_{DSS}:60V$ $I_{D}:50A$

 $R_{DS(ON)}$: 9.9m $\Omega@V_{GS}$ =4.5V

 $8.3m\Omega@V_{GS}=10V$







General Description

This power MOSFET is produced with advanced technology of SAMWIN. This technology enable the power MOSFET to have better characteristics, including fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics.

Order Codes

Iter	m	Sales Type	Marking	Package	Packaging
1		SW HA 088R06VT	SW088R06VT	DFN5*6	REEL

Absolute maximum ratings

Symbol	Parameter		Value	Unit
V _{DSS}	Drain to source voltage		60	V
	Continuous drain current (@T _C =25°C)	15	50*	А
l _D	Continuous drain current (@T _C =100°C)		33*	A
I _{DM}	Drain current pulsed	(note 1)	200	A
	Continuous drain current (@T _a =25°C)	$\supset V$	13	А
I _{DSM}	Continuous drain current (@T _a =70°C)		10	А
V _{GS}	Gate to source voltage		±20	V
E _{AS}	Single pulsed avalanche energy	(note 2)	192	mJ
E _{AR}	Repetitive avalanche energy	(note 1)	11	mJ
dv/dt	Peak diode recovery dv/dt	(note 3)	5	V/ns
_	Total power dissipation (@T _c =25°C)		40.3	W
P _D	Total power dissipation (@T _a =25°C)		2.8	W
T _{STG} , T _J	Operating junction temperature & storage ter	mperature	-55 ~ + 150	°C

^{*.} Drain current is limited by junction temperature.

Thermal characteristics

Symbol	Parameter	Value	Unit
R _{thjc}	Thermal resistance, Junction to case	3.1	°C/W
R _{thja}	Thermal resistance, Junction to ambient	45	°C/W

Note: R_{thja} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is d efined as the solder mounting surface of the drain pins. R_{thjc} is guaranteed by design while R_{thca} is determined by the user's board design. DFN5*6 R_{thja} : 45 °C/W on a 1 in² pad of 2oz copper.



Electrical characteristic ($T_J = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Off ch188a	racteristics					
BV _{DSS}	Drain to source breakdown voltage	V _{GS} =0V, I _D =250uA	60			V
ΔBV_{DSS} / ΔT_{J}	Breakdown voltage temperature coefficient	I _D =250uA, referenced to 25°C		0.05		V/°C
	Drain to source leakage current	V _{DS} =60V, V _{GS} =0V			1	uA
I _{DSS}		V _{DS} =48V, T _J =125°C			50	uA
I _{GSS}	Gate to source leakage current, forward	V _{GS} =20V, V _{DS} =0V	(3)	100	nA
	Gate to source leakage current, reverse	V _{GS} =-20V, V _{DS} =0V			-100	nA
On charac	teristics				1	
V _{GS(TH)}	Gate threshold voltage	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1.4		2.4	V
	Drain to source on state resistance	V _{GS} =4.5V, I _D =5A,T _J =25°C		9.9	12.4	mΩ
$R_{DS(ON)}$		V _{GS} =10V, I _D =5A,T _J =25°C		8.3	10.4	mΩ
		V _{GS} =10V, I _D =5A,T _J =125°C	4	13.3		mΩ
G_fs	Forward transconductance	V _{DS} =10V, I _D =5A	1	25		S
Dynamic c	haracteristics).			
C_{iss}	Input capacitance			2010		pF
C_{oss}	Output capacitance	V_{GS} =0V, V_{DS} =30V, f=1MHz		207		
C_{rss}	Reverse transfer capacitance			165		
$t_{d(on)}$	Turn on delay time			12		ns
t _r	Rising time	V_{DS} =30V, I_{D} =11A, R_{G} =4.7 Ω , V_{GS} =10V (note 4,5)		42		
t _{d(off)}	Turn off delay time			45		
t _f	Fall time	(11010-1,0)		20		
Qg	Total gate charge	V_{DS} =48V, V_{GS} =10V, I_{D} =11A , I_{G} =3mA		49		nC
Q_gs	Gate-source charge			6		
Q_{gd}	Gate-drain charge	(note 4,5)		15		
R_g	Gate resistance	V _{DS} =0V, Scan F mode		1.47		Ω

Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Is	Continuous source current	Integral reverse p-n Junction			50	Α
I _{SM}	Pulsed source current	diode in the MOSFET			200	Α
V_{SD}	Diode forward voltage drop.	I _S =11A, V _{GS} =0V			1.4	٧
t _{rr}	Reverse recovery time	I _S =11A, V _{GS} =0V,		27		ns
Q _{rr}	Reverse recovery charge	dI _F /dt=100A/us		12		nC

- Repeatitive rating : pulse width limited by junction temperature.
- L =2.67mH, I_{AS} =12A, V_{DD} =50V, R_{G} =25 Ω , Starting T_{J} = 25°C I_{SD} ≤ 11A, di/dt = 100A/us, V_{DD} ≤ BV_{DSS}, Staring T_{J} =25°C Pulse Test : Pulse Width ≤ 300us, duty cycle ≤ 2%.
- 3.
- 4.
- Essentially independent of operating temperature.

Fig. 1. On-state characteristics

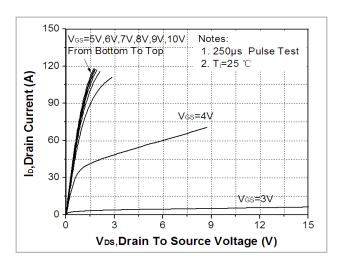


Fig. 3. On-resistance variation vs. drain current and gate voltage

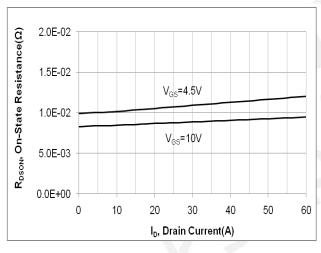


Fig. 5. Breakdown voltage variation vs. junction temperature

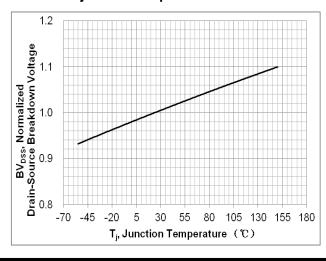


Fig. 2. Transfer characteristics

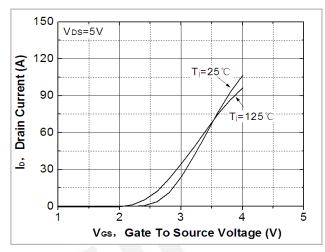


Fig. 4. On-state current vs. diode forward voltage

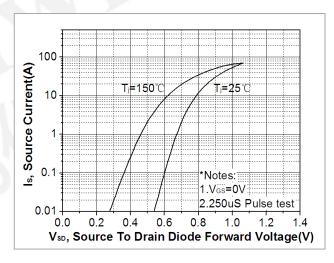


Fig. 6. On-resistance variation vs. junction temperature

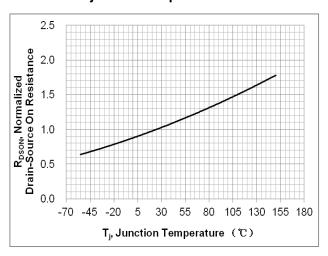


Fig. 7. Gate charge characteristics

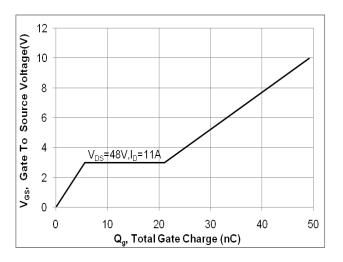


Fig. 9. Maximum safe operating area

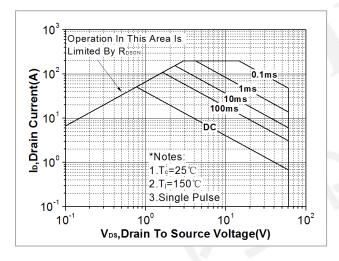


Fig. 8. Capacitance Characteristics

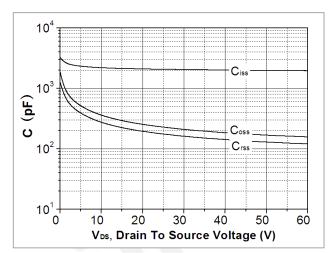


Fig. 10. Maximum drain current vs. case temperature

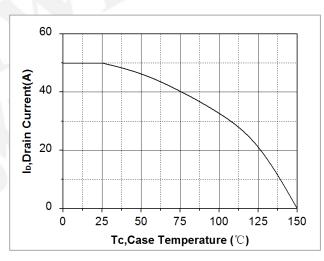


Fig. 11. Transient thermal response curve

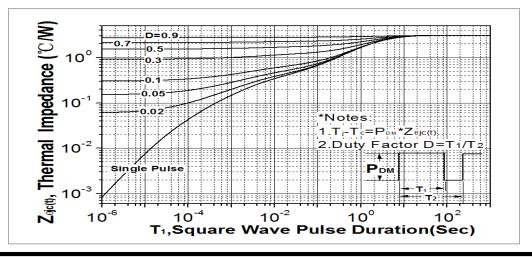




Fig. 12. Gate charge test circuit & waveform

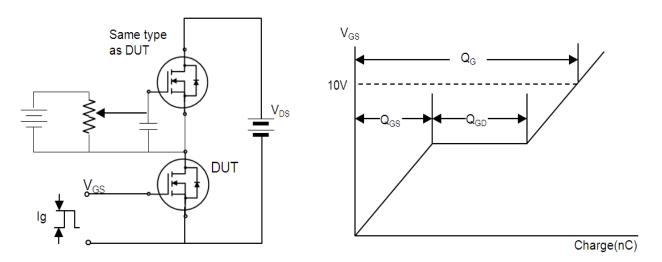


Fig. 13. Switching time test circuit & waveform

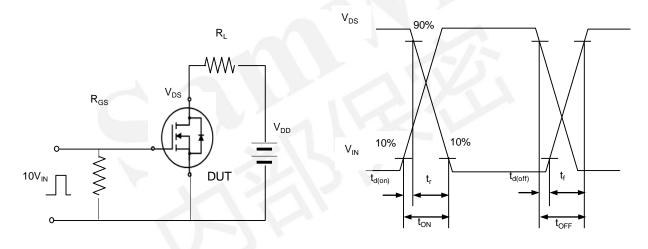


Fig. 14. Unclamped Inductive switching test circuit & waveform

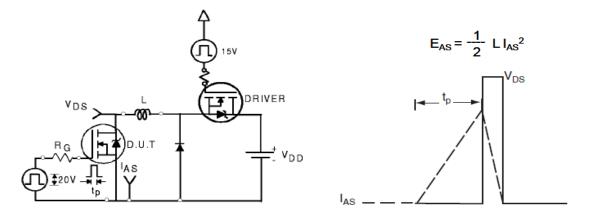
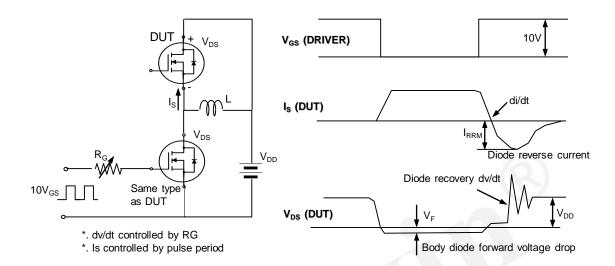




Fig. 15. Peak diode recovery dv/dt test circuit & waveform



DISCLAIMER

- * All the data & curve in this document was tested in XI'AN SEMIPOWER TESTING & APPLICATION CENTER.
- * This product has passed the PCT,TC,HTRB,HTGB,HAST,PC and Solderdunk reliability testing.
- * Qualification standards can also be found on the Web site (http://www.semipower.com.cn)



* Suggestions for improvement are appreciated, Please send your suggestions to samwin@samwinsemi.com