

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SU04F, TC7SU04FU

Inverter

The TC7SU04 is a high speed C²MOS Inverter fabricated with silicon gate C²MOS technology.

It achieves high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation.

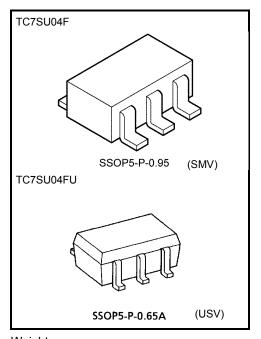
The internal circuit is composed of single stages inverter, it can be applied for crystal oscillation.

The input is equipped with protection circuits against static discharge or transient excess voltage.

Output currents are 1/2 compared to TC74HC series models.

Features

- High speed: tpd = 7 ns (typ.) at Vcc = 5 V
- Low power dissipation: ICC = 1.0 μA (max) at Ta = 25°C
- High noise immunity: VNIH = VNIL = 10% VCC (min)
- · Output drive capability: 5 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 2 mA (min)
- Balanced propagation delay time: t_{pLH} ≃ t_{pHL}
- Wide operating voltage range: Vcc (opr) = 2 to 6 V



Weight SSOP5-P-0.95: 0.016 g (typ.) SSOP5-P-0.65A: 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	٧
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	٧
Input diode current	lıK	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±12.5	mA
DC V _{CC} /ground current	Icc	±25	mA
Power dissipation	PD	200	mW
Storage temperature range	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

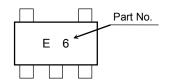
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

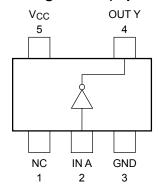
Start of commercial production 1987-09



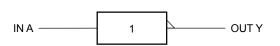
Marking



Pin Configuration (top view)



Logic Diagram



Truth Table

А	Υ
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	Vout	0 to Vcc	٧
Operating temperature range	T _{opr}	-40 to 85	°C



Electrical Characteristics DC Electrical Characteristics

Characteristics Symbol Test Condition			Ta = 25°C			Ta = -40 to 85°C					
		Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit		
			_		2.0	1.7	_	_	1.7	_	_
Input voltage High level	VIH	4.5			3.6	_	_	3.6	_		
		6.0			4.8	_	_	4.8	_	V	
			_		2.0	_	_	0.3	_	0.3	- -
	Low level	VIL			4.5	_	_	0.9	_	0.9	
					6.0	_	_	1.2	_	1.2	
				I _{OH} = -20 μA	2.0	1.8	2.0	_	1.8	_	V
					4.5	4.0	4.5	_	4.0	_	
High level Output	High level	level V _{OH}	V _{IN} = V _{IL}		6.0	5.5	5.9	_	5.5	_	
				I _{OH} = -2 mA	4.5	4.18	4.31	_	4.13	_	
				I _{OH} = -2.6 mA	6.0	5.68	5.80	_	5.63	_	
voltage		Low level V _{OL} V		I _{OL} = 20 μA	2.0	_	0	0.2	_	0.2	
Low level			V _{IN} = V _{IH}		4.5	_	0	0.5	_	0.5	
	Low level				6.0	_	0.1	0.5	_	0.5	
				I _{OL} = 2 mA	4.5	_	0.17	0.26	_	0.33	
				I _{OL} = 2.6 mA	6.0	_	0.18	0.26	_	0.33	
Input leakage	nput leakage current I _{IN} V _{IN} = V _{CC} or GND		6.0	_	_	±0.1	_	±1.0	μΑ		
Quiescent supply current I _{CC} V _{IN} = V _{CC} or GND		6.0	_	_	1.0	_	10.0	μΑ			

AC Electrical Characteristics (C_L = 15 pF, input t_r = t_f = 6 ns, V_{CC} = 5 V)

Characteristics	Symbol	Test Condition		l lmi4		
		rest Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH} t _{THL}	_	_	5	10	ns
Propagation delay time	t _{pLH} t _{pHL}	_	_	7	15	ns



AC Electrical Characteristics ($C_L = 50 \text{ pF}$, input $t_r = t_f = 6 \text{ ns}$)

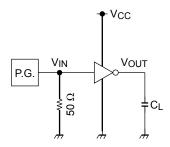
Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Lloit
			Vcc (V)	Min	Тур.	Max	Min	Max	Unit
Output transition time	tTLH tTHL	-	2.0	_	50	125	_	155	ns
			4.5	_	14	25	_	31	
			6.0	_	12	21	_	26	
Propagation delay time	t _{PLH}	_	2.0	_	48	100	_	125	ns
			4.5	_	12	20	_	25	
			6.0	_	9	17	_	21	
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note 1)	_	10	_	_	_	pF

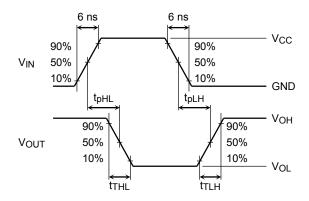
Note 1: CPD defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to test circuit).

Average operating current can be obtained by the equation hereunder.

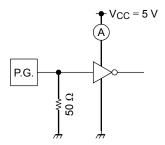
ICC (opr) = CPD • VCC • fIN + ICC

Switching Characteristics Test Circuit





Icc (opr) Test Circuit

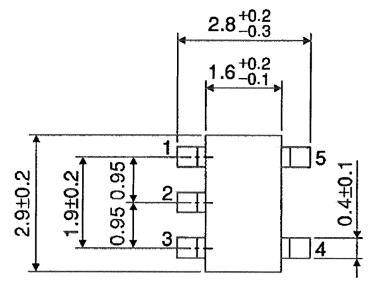


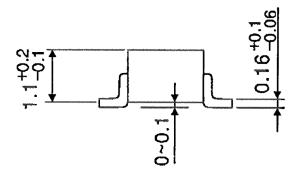
Input waveform is the same as that in case of switching characteristics test.



Package Dimensions

SSOP5-P-0.95 Unit: mm





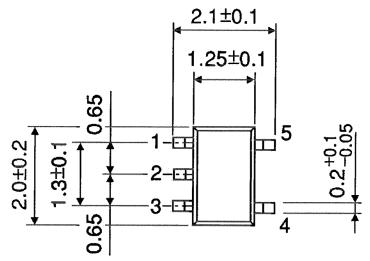
Weight: 0.016 g (typ.)

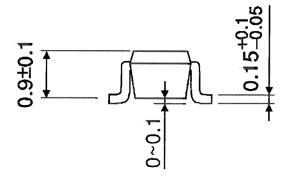
Unit: mm



Package Dimensions

SSOP5-P-0.65A





Weight: 0.006 g (typ.)



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