CMOS Digital Integrated Circuits Silicon Monolithic

# TC7SET02FU

#### 1. Functional Description

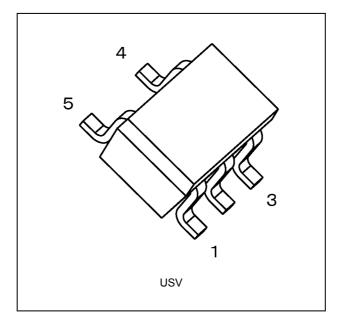
• 2-Input NOR Gate

#### 2. Features

- (1) AEC-Q100 (Rev. H) (Note 1)
- (2) Wide operating temperature range:  $T_{opr} = -40$  to 125 °C (Note 2)
- (3) High speed operation:  $t_{pd}$  = 4.2 ns (typ.) (V<sub>CC</sub> = 5.0 V, C<sub>L</sub> = 15 pF)
- (4) Low power dissipation:  $I_{CC} = 2.0 \ \mu A \ (max) \ (T_a = 25 \ ^\circ C)$
- (5) Compatible with TTL outputs:  $V_{IL} = 0.8 V$  (max)
  - $V_{IH} = 2.0 V (min)$
- (6) 5.5 V tolerant inputs
- (7) Balanced Propagation Delay:  $t_{PLH} \approx t_{PHL}$
- Note 1: This device is compliant with the reliability requirements of AEC-Q100. For details, contact your Toshiba sales representative.

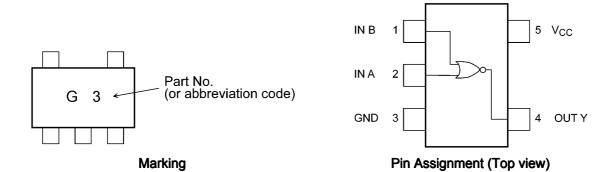
Note 2: For devices with the ordering part number ending in J(CT.  $T_{opr}$  = -40 to 85 °C for the other devices.

#### 3. Packaging



# TOSHIBA

# 4. Marking and Pin Assignment



#### 5. IEC Logic Symbol



#### 6. Truth Table

А	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

# 7. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 7.0	V
Input voltage	V <sub>IN</sub>		-0.5 to 7.0	
DC output voltage	V <sub>OUT</sub>		-0.5 to V <sub>CC</sub> + 0.5	
Input diode current	I <sub>IK</sub>		-20	mA
Output diode current	I <sub>OK</sub>	(Note 1)	±20	
DC output current	I <sub>OUT</sub>		±25	
V <sub>CC</sub> /ground current	I <sub>CC</sub>		±50	
Power dissipation	PD		200	mW
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$ 

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# 8. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		4.5 to 5.5	V
Input voltage	V <sub>IN</sub>		0 to 5.5	
Output voltage	V <sub>OUT</sub>		0 to V <sub>CC</sub>	
Operating temperature	T <sub>opr</sub>	(Note 1)	-40 to 125	°C
		(Note 2)	-40 to 85	
Input rise and fall time	dt/dv		0 to 20	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 1: For devices with the ordering part number ending in J(CT.

Note 2: For devices except those with the ordering part number ending in J(CT.

#### 9. Electrical Characteristics

#### 9.1. DC Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
High-level input voltage	VIH	—		4.5 to 5.5	2.0	_	_	V
Low-level input voltage	VIL	—		4.5 to 5.5	_		0.8	V
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -50 μA	4.5	4.4	4.5	—	V
			I <sub>OH</sub> = -8 mA	4.5	3.94	—	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 50 μA	4.5	_	0.0	0.1	V
			I <sub>OL</sub> = 8 mA	4.5	_	—	0.36	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±0.1	μA
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		5.5	_		2.0	μA
	I <sub>CCT</sub>	Per input: V <sub>IN</sub> = 3.4 V Other input: V <sub>CC</sub> or GND		5.5	—	_	1.35	mA

# 9.2. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C)

Characteristics	Symbol	Test Conditio	V <sub>CC</sub> (V)	Min	Max	Unit	
High-level input voltage	V <sub>IH</sub>	—		4.5 to 5.5	2.0	—	V
Low-level input voltage	VIL	_		4.5 to 5.5	_	0.8	V
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -50 μA	4.5	4.4	_	V
			I <sub>OH</sub> = -8 mA	4.5	3.80	—	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 50 μA	4.5	_	0.1	V
			I <sub>OL</sub> = 8 mA	4.5	_	0.44	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	±1.0	μA
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		5.5	_	20.0	μA
	І <sub>ССТ</sub>	Per input: V <sub>IN</sub> = 3.4 V Other input: V <sub>CC</sub> or GND		5.5	_	1.50	mA

## 9.3. DC Characteristics (Note) (Unless otherwise specified, T<sub>a</sub> = -40 to 125 °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit	
High-level input voltage	VIH	—		4.5 to 5.5	2.0	—	V
Low-level input voltage	VIL	_		4.5 to 5.5	_	0.8	V
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -50 μA	4.5	4.4	_	V
			I <sub>OH</sub> = -8 mA	4.5	3.70	—	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OL</sub> = 50 μA	4.5	_	0.1	V
			I <sub>OL</sub> = 8 mA	4.5	_	0.55	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	±2.0	μA
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		5.5	_	40.0	μA
	І <sub>ССТ</sub>	Per input: V <sub>IN</sub> = 3.4 V Other input: V <sub>CC</sub> or GND		5.5		1.50	mA

Note: For devices with the ordering part number ending in J(CT.

# 9.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		—	$5.0\pm0.5$	15	_	4.2	6.2	ns
					50	_	6.5	9.0	
Input capacitance	C <sub>IN</sub>		_			_	4	10	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)	—			_	17	_	pF

Note 1:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.  $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

#### 9.5. AC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C, Input: t<sub>r</sub> = t<sub>f</sub> = 3 ns)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	—	$5.0\pm0.5$	15	1.0	7.1	ns
				50	1.0	10.3	
Input capacitance	C <sub>IN</sub>	—			_	10	pF

### 9.6. AC Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to 125 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	—	$5.0\pm0.5$	15	1.0	8.0	ns
				50	1.0	11.5	
Input capacitance	C <sub>IN</sub>	_			_	10	pF

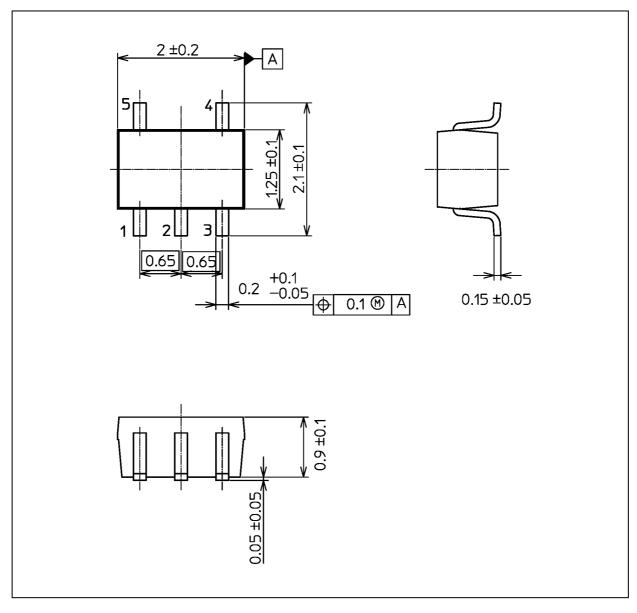
Note: For devices with the ordering part number ending in J(CT.



### **Package Dimensions**

TC7SET02FU

Unit: mm



Weight: 0.006 g (typ.)

	Package Name(s)
JEDEC: SOT-353	
Nickname: USV	

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