

## HG741 Operational Amplifier

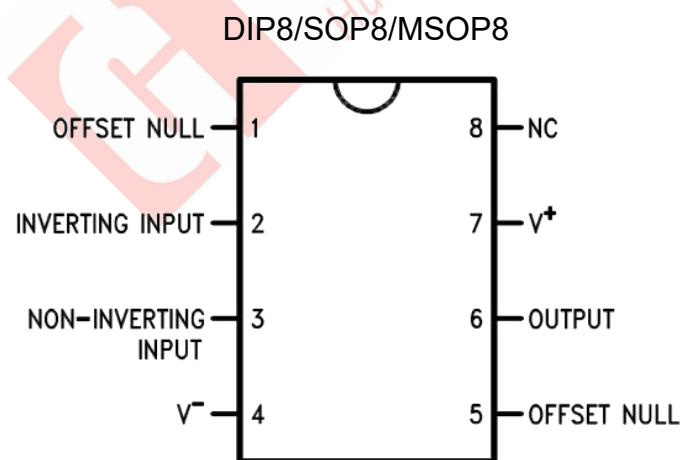
### General Description

The HG741 series are general purpose operational amplifiers which feature improved performance over industry standards like the HG709. They are direct, plug-in replacements for the 709C, HG201, MC1439 and 748 in most applications. The amplifiers offer many features which make their application nearly foolproof: overload protection on the input and output, no latch-up when the common mode range is exceeded, as well as freedom from oscillations.

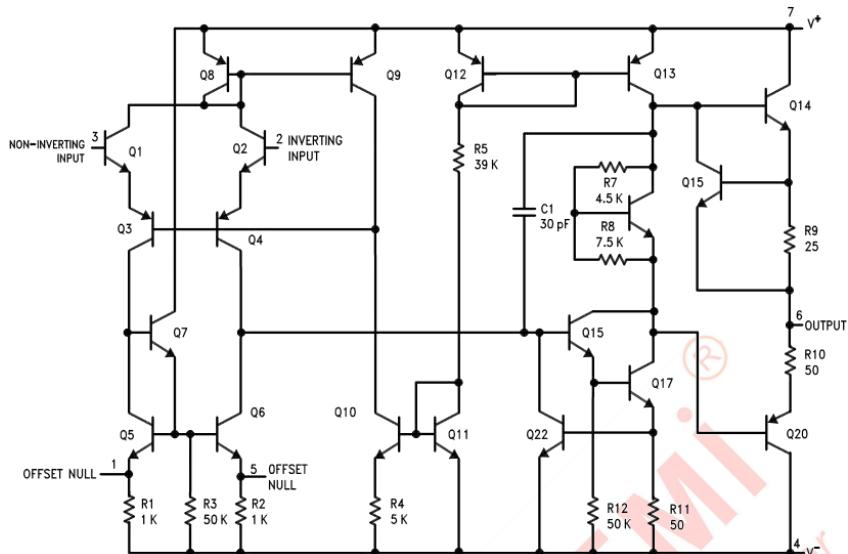
### Ordering Information

DEVICE	PACKAGE TYPE	MARKING	PACKING	PACKING QTY
HG741N	DIP8	HG741	TUBE	2000pcs/Box
HG741AN	DIP8	HG741A	TUBE	2000pcs/Box
HG741M/TR	SOP8	HG741	REEL	2500pcs/Reel
HG741AM/TR	SOP8	HG741A	REEL	2500pcs/Reel
HG741MM/TR	MSOP8	HG741	REEL	3000pcs/Reel
HG741AMM/TR	MSOP8	HG741A	REEL	3000pcs/Reel

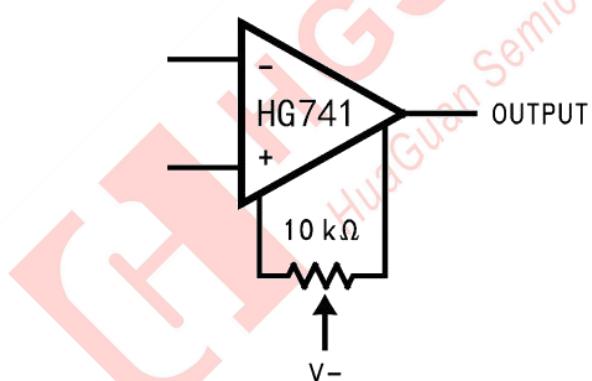
### Connection Diagram



## Schematic Diagram



## Offset Nulling Circuit



## Absolute Maximum Ratings

CONDITION		LIMITS	UNITS
Supply Voltage		±22	V
Power Dissipation(Note2)		500	mW
Differential Input Voltage		±30	V
Input Voltage(Note3)		±15	V
Output Short Circuit Duration		Continuous	
Operating Temperature Range	HG741A	-40 ~ +85	°C
	HG741	0 ~ +70	°C
Junction Temperature	HG741A	150	°C
	HG741	100	°C
Soldering Information	N-Package(10 seconds)	260	°C
	J-or H-Package(10 seconds)	300	°C
M-Package	Vapor Phase(60 seconds)	215	°C
	Infrared(15 seconds)	215	°C
Storage Temperature Range		-65 ~ +150	°C
ESD Tolerance(Note7)		400	V

## Electrical Characteristics

Parameter	Conditions	HG741A			HG741			Units
		Min	Typ	Max	Min	Typ	Max	
Input Offset Voltage	$T_A=25^\circ C$ $R_s \leq 10K\Omega$ $R_s \leq 50\Omega$		0.8	3.0		2.0	6.0	mW mW
	$T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}$ $R_s \leq 50\Omega$ $R_s \leq 10K\Omega$			4.0			7.5	mW mW
Average Input Offset Voltage Drift				15				$\mu V/^\circ C$
Input Offset Voltage Adjustment Range	$T_A=25^\circ C, V_s = \pm 20V$	$\pm 10$				$\pm 15$		mW
Input Offset Current	$T_A=25^\circ C$ $T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}$		3.0	30 70		20	200 300	nA nA
				0.5				$nA/^\circ C$
Input Bias Current	$T_A=25^\circ C$ $T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}$		30	80 0.210		80	500 0.8	nA $\mu A$
	$T_A=25^\circ C, V_s = \pm 20V$	1.0	6.0		0.3	2.0		$M\Omega$
Input Resistance	$T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}, V_s = \pm 20V$	0.5						$M\Omega$
								V
Input Voltage Range	$T_A=25^\circ C$				$\pm 12$	$\pm 13$		V
	$T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}$							V
Large Signal Voltage Gain	$T_A=25^\circ C, R_L \geq 2K\Omega$ $V_s = \pm 20V, V_o = \pm 15V$ $V_s = \pm 15V, V_o = \pm 10V$	50			20	200		$V/mW$ $V/mW$
	$T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}$ $R_L \geq 2K\Omega$ $V_s = \pm 20V, V_o = \pm 15V$ $V_s = \pm 15V, V_o = \pm 10V$ $V_s = \pm 5V, V_o = \pm 2V$	32			15			$V/mW$ $V/mW$ $V/mW$
		10						
	$V_s = \pm 20V$ $R_L \geq 10K\Omega$ $R_L \geq 2K\Omega$	$\pm 16$ $\pm 15$						V V
	$V_s = \pm 15V$ $R_L \geq 10K\Omega$ $R_L \geq 2K\Omega$				$\pm 12$ $\pm 10$	$\pm 14$ $\pm 13$		V V
Output Short Circuit Current	$T_A=25^\circ C$ $T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}$	10 10	25	35 40		25		mA mA
Common-Mode Rejection Ratio	$T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}$ $R_s \leq 10K\Omega, V_{CM} = \pm 12V$ $R_s \leq 50\Omega, V_{CM} = \pm 12V$			0.5	70	90		dB dB
		80	95					
Supply Voltage Rejection Ratio	$T_{A\text{MIN}} \leq T_A \leq T_{A\text{MAX}}$ $V_s = \pm 20V \text{ to } V_s = \pm 5V$ $R_s \leq 50\Omega$ $R_s \leq 10K\Omega$				77	96		dB dB
		86	96					

Transient Response Rise Time Overshoot	TA=25°C,Unity Gain		0.25 6.0	0.8 20		0.3 5		μs
Bandwidth(Note5)	TA=25°C	0.43 7	1.5					MHz
Slew Rate	TA=25°C,Unity Gain	0.3	0.7			0.5		V/μs
Supply Current	TA=25°C					1.7	2.8	mA
Power Consumption	TA=25°C Vs=±20V Vs=±15V		80	150		50	85	mw mw

**Note1:**“Absolute Maximum Ratings”indicate limits beyond which damage to the device may occur.Operating Ratings indicate conditions for which the device is functional,but do not guarantee specific performance limits.

**Note 2:** For operation at elevated temperatures, these devices must be derated based on thermal resistance, and T<sub>j</sub> max. (listed under “Absolute Maximum Ratings”). T<sub>j</sub> = T<sub>A</sub> +(θ<sub>JA</sub> P<sub>D</sub>).

Thermal Resistance	DIP(B)	SOP-8(M)
θ <sub>JA</sub> (Junction to Ambient)	100°C/W	195°C/W
θ <sub>JC</sub> (Junction to Case)	N/A	N/A

Note 3: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

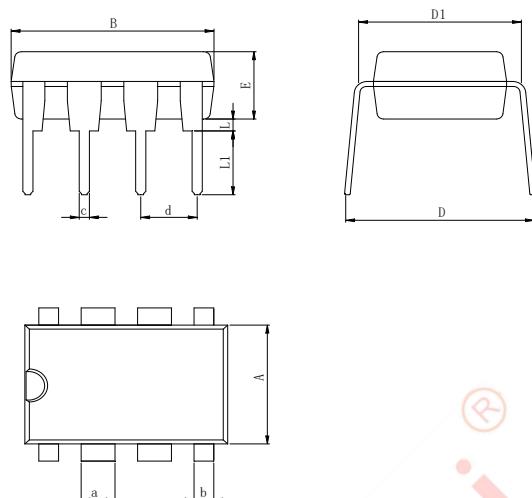
Note 4: Unless otherwise specified, these specifications apply for VS = ±15V, -40°C ≤ TA ≤ +85°C (HG741A). For the HG741, these specifications are limited to 0°C ≤ TA ≤ +70°C.

Note 5: Calculated value from: BW (MHz) = 0.35/Rise Time(μs).

Note 6: Human body model, 1.5 kΩ in series with 100 pF.

## Physical Dimensions

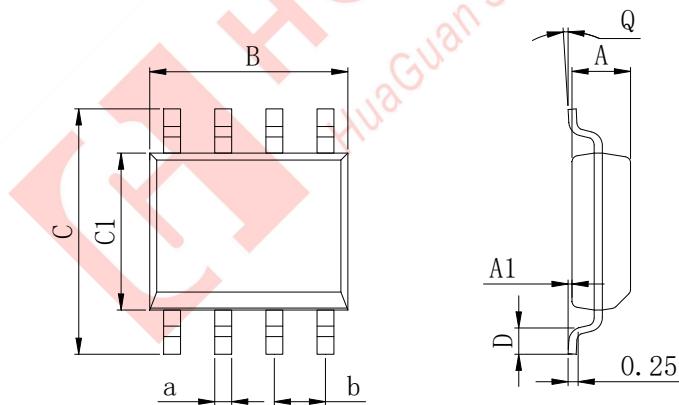
DIP-8L



**Dimensions In Millimeters(DIP8L)**

Symbol:	A	B	D	D1	E	L	L1	a	b	c	d
<b>Min:</b>	6.10	9.00	8.40	7.42	3.10	0.50	3.00	1.50	0.85	0.40	2.54 BSC
<b>Max:</b>	6.68	9.50	9.00	7.82	3.55	0.70	3.60	1.55	0.90	0.50	

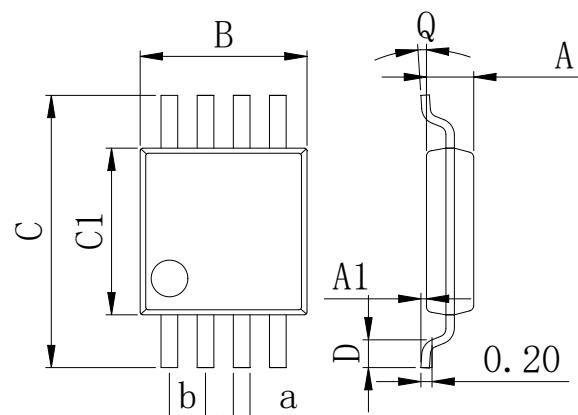
SOP-8L 150mil



**Dimensions In Millimeters(SOP8)**

Symbol:	A	A1	B	C	C1	D	Q	a	b
<b>Min:</b>	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
<b>Max:</b>	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

**MSOP8**



**Dimensions In Millimeters(MSOP8L)**

Symbol:	A	A1	B	C	C1	D	Q	a	b
<b>Min:</b>	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
<b>Max:</b>	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

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