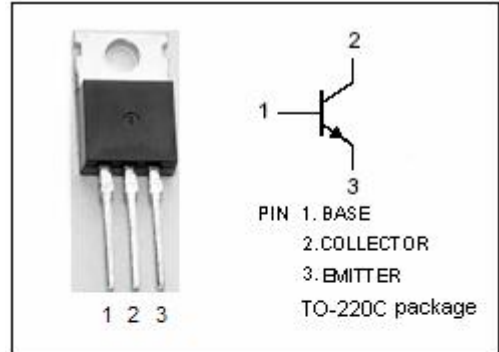


**isc Silicon NPN Power Transistor**
**2SD1163**
**DESCRIPTION**

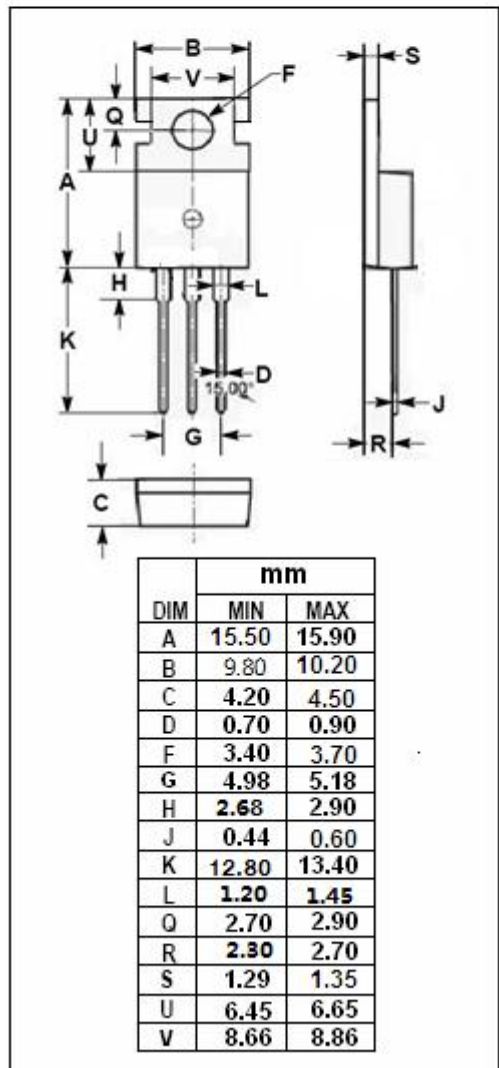
- Collector Current:  $I_C = 7A$
- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = 120V(\text{Min.})$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- Designed for TV horizontal deflection applications.


**ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	300	V
$V_{CEO}$	Collector-Emitter Voltage	120	V
$V_{EBO}$	Emitter-Base Voltage	6	V
$I_C$	Collector Current-Continuous	7	A
$I_{CM}$	Collector Current-Peak	10	A
$I_{C(surge)}$	Collector Current-Surge	20	A
$P_C$	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	40	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ\text{C}$



## isc Silicon NPN Power Transistor

## 2SD1163

## ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10mA ; R <sub>BE</sub> = ∞	120			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 1mA ; I <sub>C</sub> = 0	6			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 0.5A			2.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 0.5A			1.2	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 300V ; I <sub>E</sub> = 0			5	mA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 5A ; V <sub>CE</sub> = 5V	25			
t <sub>f</sub>	Fall Time	I <sub>CP</sub> = 3.5A; I <sub>B1</sub> = 0.45A			0.5	μs

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