

# **SK3522LD**

Multilayer Chip DIPLEXER

Dual-band/dual-mode at 1.5GHz and 2.4GHz

Revision 4 : Sept. 2019





# 1. Scope

This specification applies to SK3522LD of Multilayer Chip Diplexer.

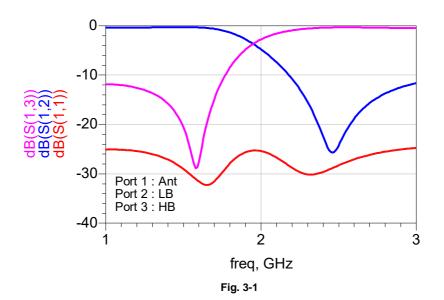
# 2. Product Description and Identification (Part Number)

Multi-layer Chip Diplexer

#### 3. Electrical Characteristics

Part Number	SK3522LD			
Application	Low Band	High Band		
Bandwidth (BW)	1570~1610 MHz	2400~2500 MHz		
I.L. in BW	0.45 dB max at 25 $^{\circ}\!$	0.5 dB max at 25 $℃$		
	0.6 dB max. at -40~85 $^\circ\!\!\!\subset$	0.7 dB max. at -40~85°ℂ		
Attenuation(Absolute value)	20 dB min. at 2400~2500MHz	22 dB min. at 1570~1610MHz		
la alatian	20 dB min. at 1570~1610MHz			
Isolation	20 dB min. at 2400~2500MHz			
R.L. in BW	15 dB min	15 dB min		
Characteristic Impedance (Nom.)	50 ohm			
Power Capacity	3.0 W max.			

- a) Operating and storage temperature range (individual chip without packing): -40  $^{\circ}$ C ~ +85  $^{\circ}$ C.
- b) Storage temperature range (packaging conditions): -10  $^{\circ}$ C ~ +40  $^{\circ}$ C and RH 70% (Max.).
- c) Test equipment: Network Analyzer:E5071C
- d) Electrical Performance: See Fig. 3-1.





Land

Unit: mm

#### 4. **Shape and Dimensions**

1) Dimensions and terminal configuration: See Fig. 4-1

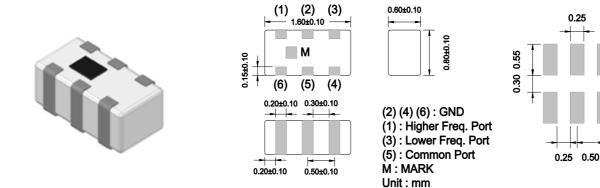
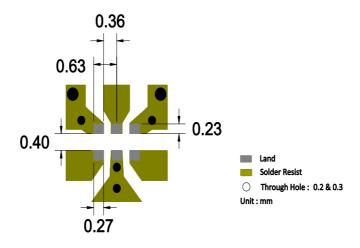


Fig. 4-1



2) Recommended Land Pattern: See Fig.4-2



Line width should be designed to match 50  $\Omega$  characteristic impedance, depending on PCB material and thickness.

Fig. 4-2

#### **Test and Measurement Procedures**

### **5.1 Test Conditions**

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

- Ambient Temperature: 20±15℃
- Relative Humidity: 65±20%
- Air Pressure: 86 KPa to 106 KPa

If any doubt on the results, measurements/tests should be made within the following limits:

- Ambient Temperature: 20±2℃
- b. Relative Humidity: 65±5%
- Air Pressure: 86KPa to 106 KPa

### 5.2 Visual Examination

Inspection Equipment: 20 X magnifier



# 5.3 Reliability Test

Items	Requirements	Test Methods and Remarks		
5.3.1 Terminal Strength	No visible mechanical damage.	<ul> <li>Solder the inductor to the testing jig (glass epoxy board shown as the following figure) using leadfree solder. Then apply a force in the direction of the arrow.</li> <li>5N force for1608 series.</li> <li>Keep time: 10±1sec.</li> </ul> Chip 5N/10±1s Speed: 1.0mm/s Glass Epoxy Board Mounting Pad		
5.3.2 Resistance to Flexure	No visible mechanical damage.	<ol> <li>Solder the chip to the test jig (glass epoxy board) using a leadfree solder. Then apply a force in the direction shown as the following figure. Solder the chip to the test jig (glass epoxy board) using leadfree solder. Then apply a force in the direction.</li> <li>Flexure: 2mm</li> <li>Pressurizing Speed: 0.5mm/sec</li> <li>Keep time: ≥30 sec</li> </ol>		
	Unit: mm R1	20 10 10 Flexure: 2		
5.3.3 Vibration	No visible mechanical damage.	<ol> <li>Solder the chip to the testing jig (glass epoxy board shown as the following figure) using leadfree solder.</li> <li>The chip shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz.</li> <li>The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours).</li> </ol> Solder Mask Cu Pad		
5.3.4 Dropping	No visible mechanical damage.	Drop the chip 10 times on a concrete floor from a height of 100 cm.		
5.3.5 Solderability	<ol> <li>No visible mechanical damage.</li> <li>Wetting shall be exceeded 75% coverage.</li> </ol>	<ol> <li>Solder temperature: 240±2°C</li> <li>Duration: 3sec</li> <li>Solder: Sn/3.0Ag/0.5Cu</li> <li>Flux: 25% Resin and 75% ethanol in weight</li> </ol>		
5.3.6 Resistance to Soldering Heat	No visible mechanical damage.	<ol> <li>Solder temperature: 260±5°C</li> <li>Duration: 5 sec</li> <li>Solder: Sn/3.0Ag/0.5Cu</li> <li>Flux: 25% Resin and 75% ethanol in weight</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>		



**Specifications for Multilayer Chip Diplexer** 

		opecifications for Multila	,			
5.3.7	1	No visible mechanical	1	① Temperature and time: -40°C for 30±3 min→85°C for		
Thermal Shock		damage.	30±3min			
	2	Satisfy electrical	② Transforming interval: Max. 20 sec.			
		Characteristic.	3	Tested cycle: 100 cycles		
			4 The chip shall be stabilized at normal condition for 1~2			
			hours before measuring.			
				30 min. 30 min.		
			85	°C		
			Ambient			
			Temp	oerature -40°C 30 min. 20sec. (max.) 20sec. (max.)		
5.3.8	1	No visible mechanical	1	Temperature: 60±2℃		
Damp Heat		damage.	2	Humidity: 90% to 95% RH		
(Steady States)	2	Satisfy electrical	3	③ Duration: 500 <sup>+24</sup> hours		
		Characteristic.	4 The chip shall be stabilized at normal condition for 1~2			
			hours before measuring.			
5.3.9	1	No visible mechanical	1	Temperature: 85±2°C		
Resistance to High temperature		damage.	2	Duration: 500 <sup>+24</sup> hours		
	2	Satisfy electrical	3	The chip shall be stabilized at normal condition for 1~2		
		Characteristic.	hours before measuring.			

# 6. Packaging and Storage

# 6.1 Packaging

There is one type of packaging for the Diplexer. Please specify the packing code when ordering.

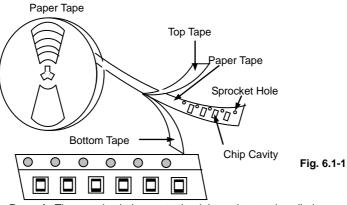
6.1.1 Tape Carrier Packaging:

Packaging code: T

- a. Tape carrier packaging are specified in attached figure Fig. 6.1-1~3
- b. Tape carrier packaging quantity please see the following table:

Type	1608[0603]		
Tape	Paper Tape		
Quantity	4K		

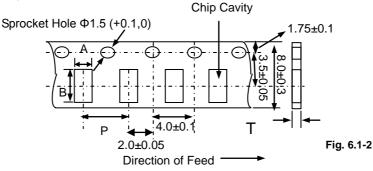
# (1) Taping Drawings (Unit: mm)



**Remark:** The sprocket holes are to the right as the tape is pulled toward the user.

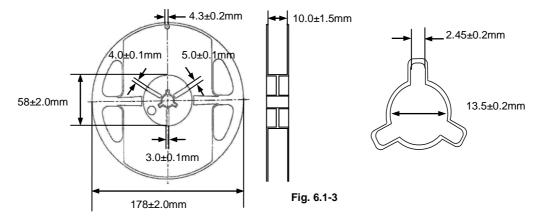


# (2) Taping Dimensions (Unit: mm)



Туре	Chip Thickness	Α	В	Р	T max
SK3522LD	0.60±0.10	1.00±0.10	1.80±0.10	4.0±0.10	0.75

#### (3) Reel Dimensions (Unit: mm)



# 6.2 Storage

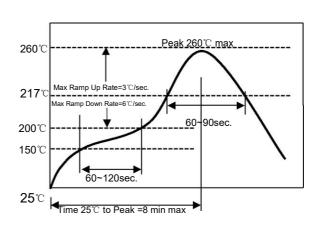
- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.
- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H<sub>2</sub>S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability specified in **Clause 5.3.6** shall be guaranteed for 6 months from the date of delivery on condition that they are stored at the environment specified in **Clause 3**. For those parts, which passed more than 6 months shall be checked solder-ability before use.

# 7. Recommended Soldering Technologies

# 7.1 Re-flowing Profile

- △ Preheat condition: 150 ~200°C/60~120sec.
- $\triangle$  Allowed time above 217°C: 60~90sec.
- △ Max temp: 260°C
- △ Max time at max temp: 10sec.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- △ Allowed Reflow time: 2x max

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]





# 7.2 Iron Soldering Profile

△ Iron soldering power: Max.30W

 $\triangle$  Soldering Tip temperature: 350 °C Max.

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]

