

# 零件承认书

SPECIFICATION FOR APPROVAL

客户名称: \_\_\_\_\_

客户料号: \_\_\_\_\_

增益料号: 144-L2N70020R00

规格描述: MOS管/SMD/L2N7002M3T5G/SOT-723D/ROHS/MD

日期: 2022/6/19

版本: A

## 增益签核:

制订	审核	核准
张翔	刘业明	柯文学

## 客户签核:

工程	审核	核准



东莞市增益实业有限公司

地址: 东莞市塘厦镇林村塘厦大道北552号

电话: 0769-87321000

传真: 0769-87891229

物料类型:

MOS管

日期:

2022/6/19

版本:

A





# 1N4001-1N4007

## PLASTIC SILICON RECTIFIERS

**VOLTAGE** 50 to 1000 Volts **CURRENT** 1.0 Amperes

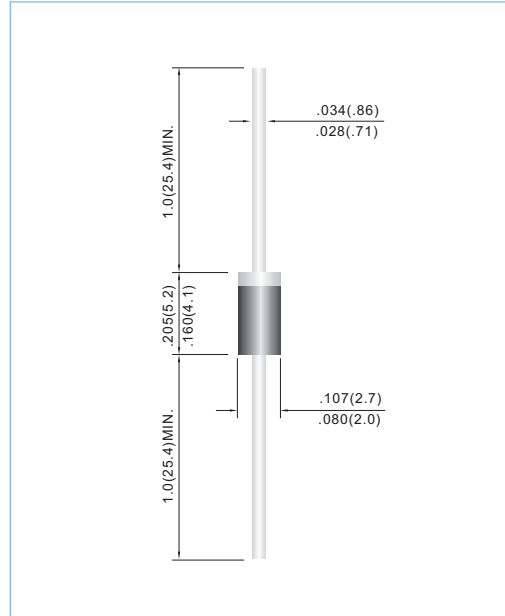
DO-41 Unit: inch(mm)

### FEATURES

- Low forward voltage drop
- High current capability
- High reliability
- High surge current capability
- Exceeds environmental standards of MIL-S-19500/228
- In compliance with EU RoHS 2002/95/EC directives

### MECHANICAL DATA

- Case: DO-41 Molded plastic
- Epoxy: UL 94V-O rate flame retardant.
- Lead: Axial leads, solderable per MIL-STD-750, Method 2026
- Polarity: Color band denotes cathode end
- Mounting Position: Any
- Weight: 0.012 ounces, 0.30 gram



### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.

PARAMETER	SYMBOL	1N4001	1N4002	1N4003	1N4004	1N4005	1N4006	1N4007	UNITS
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	$V_{RMS}$	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	$V_{DC}$	50	100	200	400	600	800	1000	V
Maximum Average Forward Current .375" (9.5mm) lead length at $T_A=75^\circ\text{C}$	$I_{F(AV)}$	1.0							A
Peak Forward Surge Current : 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	$I_{FSM}$	30							A
Maximum Forward Voltage at 1.0A	$V_F$	1.1							V
Maximum DC Reverse Current at Rated DC Blocking Voltage $T_J=25^\circ\text{C}$ $T_J=100^\circ\text{C}$	$I_R$	5.0 500							$\mu\text{A}$
Typical Junction Capacitance (Note 1)	$C_J$	15							pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$ $R_{\theta JC}$ $R_{\theta JL}$	110 40 35							$^\circ\text{C} / \text{W}$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150							$^\circ\text{C}$

#### NOTES:

1. Measured at 1 MHz and applied reverse voltage of 4.0 VDC.
2. Thermal Resistance from Junction to Ambient and from junction to lead at 0.375" (9.5mm) lead length P.C.B. mounted.



# 1N4001-1N4007

## RATING AND CHARACTERISTIC CURVES

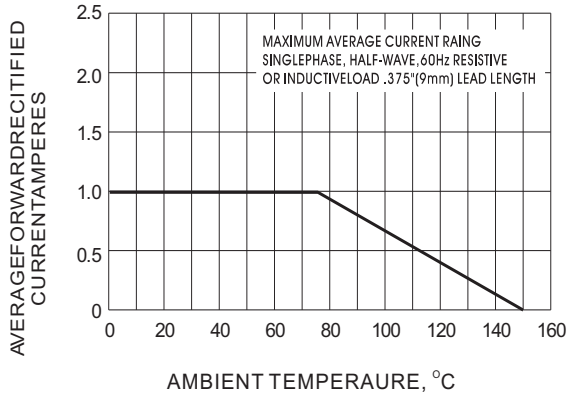


Fig.1-FORWARD CURRENT DERATING CURVE

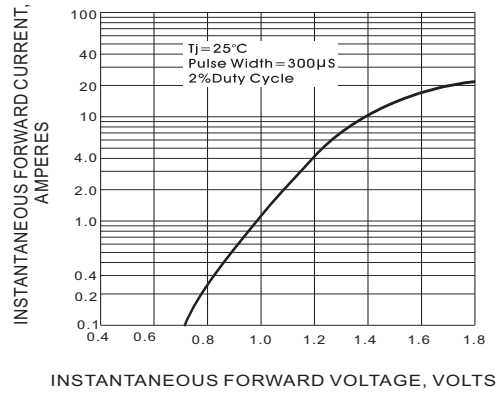


Fig.2-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

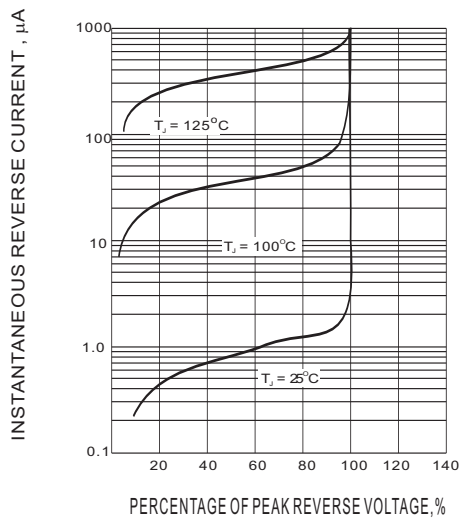


Fig.3-TYPICAL REVERSE CHARACTERISTIC

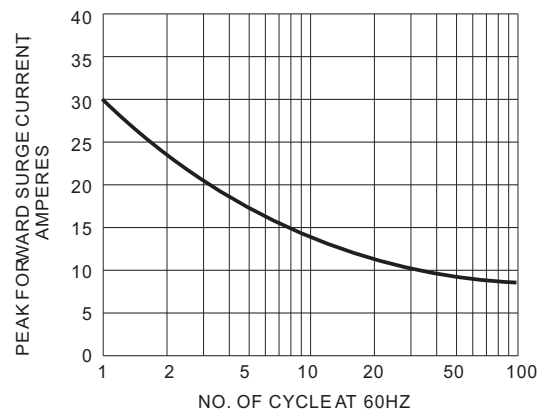


Fig.4-MAXIMUM NON-REPETITIVE SURGE CURRENT

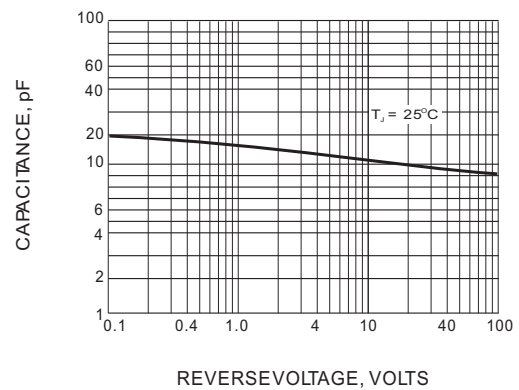


Fig.5-TYPICAL JUNCTION CAPACITANCE



## 2N7002KM

### 60V N-Channel Enhancement Mode MOSFET

<b>VOLTAGE</b>	60 Volt	<b>CURRENT</b>	0.34 A
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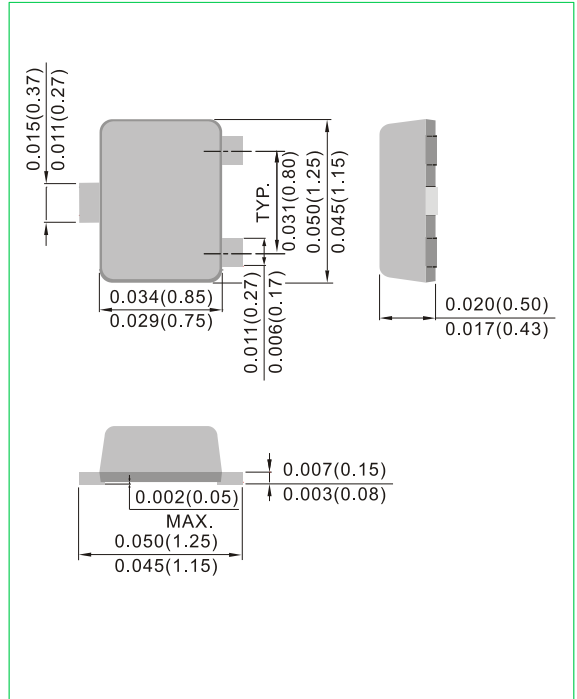
<b>SOT-723</b>	Unit : inch(mm)
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### FEATURES

- Epoxy meets UL 94 V-0 flammability rating
- High density cell design for low  $R_{DS(ON)}$
- Voltage controlled small signal switch
- High Saturation Current Capability
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC61249 Std. (Halogen Free)

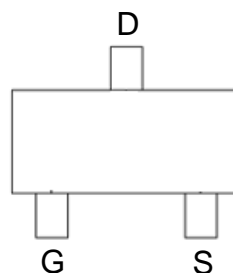
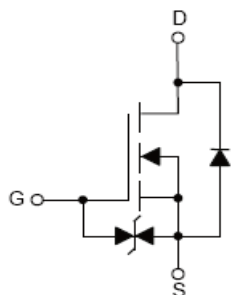
### MECHANICAL DATA

- Case: SOT-723 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Marking Code: RK



### MAXIMUM RATINGS (Ta = 25 °C)

Parameter	Symbol	Value	Units
Drain-source Voltage	$V_{DS}$	60	V
Gate-source-Voltage	$V_{GS}$	±20	V
Drain Current	$I_D$	0.34	A
Total Power Dissipation	$P_D$	0.15	W
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55 to 150	°C
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	833	°C/W



G:GATE  
S:SOURCE  
D:DRAIN



## 2N7002KM

### ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

Parameter	Test Conditions	Symbol	Min	TYP.	Max	Units
Drain-source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	$V_{(BR)DSS}$	60			V
Gate-Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	$V_{GS(th)}$	1.0	1.4	2.5	V
Gate -body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	$I_{GSS}$			$\pm 10$	$\mu A$
	$V_{DS}=0V, V_{GS}=\pm 10V$				$\pm 200$	nA
	$V_{DS}=0V, V_{GS}=\pm 5V$				$\pm 100$	nA
Zero Gate Voltage Drain Current	$V_{DS}=48V, V_{GS}=0V$	$I_{DSS}$			1	$\mu A$
Drain-source On-resistance	$V_{GS}=10V, I_D=500mA$	$R_{DS(ON)}$		1.3	4	$\Omega$
	$V_{GS}=10V, I_D=500mA$			1.4	4.5	
Recovered Charge	$V_{GS}=0V, I_S=300mA, V_R=25V, dl/dt=-100A/\mu s$	$Q_r$		30		Nc
<b>Dynamic Characteristics</b>						
Input Capacitance	$V_{DS}=10V, V_{GS}=0V$ $f=1MHz$	$C_{iss}$			40	pF
Output Capacitance		$C_{oss}$			30	
Reverse Transfer Capacitance		$C_{rss}$			10	
<b>Switching Characteristics</b>						
Turn-on delay time	$V_{DD}=25V, V_{GS}=10V, R_L=250\Omega, R_{GS}=50K, R_{GEN}=25\Omega$	$t_{d(on)}$			10	ns
Turn-off delay time		$t_{d(off)}$			15	
Reverse Recovery Time	$V_{GS}=0V, I_S=300mA, V_R=25V, dl/dt=-100A/\mu s$	$t_{rr}$		30		
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage	$V_{GS}=0V, I_S=200mA$	$V_{SD}$		0.97	1.5	V



# 2N7002KM

## RATING AND CHARACTERISTIC CURVES

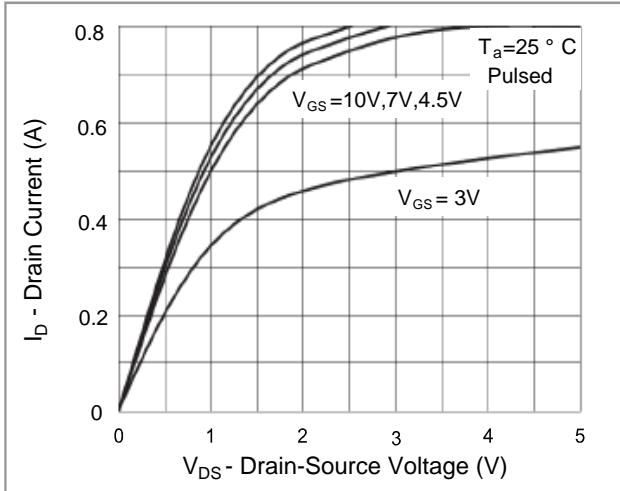


Fig.1 Output Characteristics

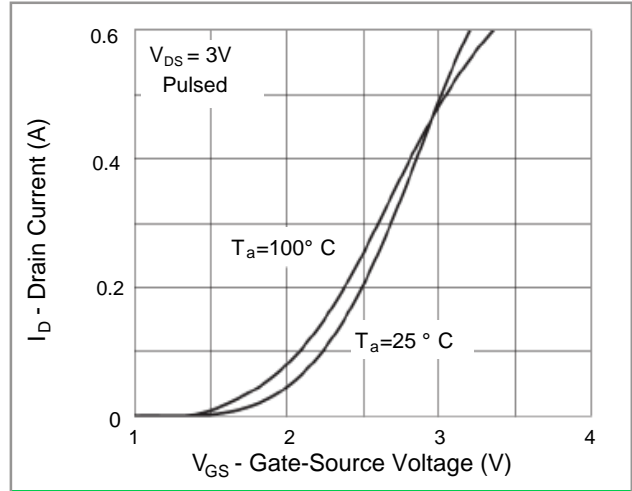


Fig.2 Transfer Characteristics

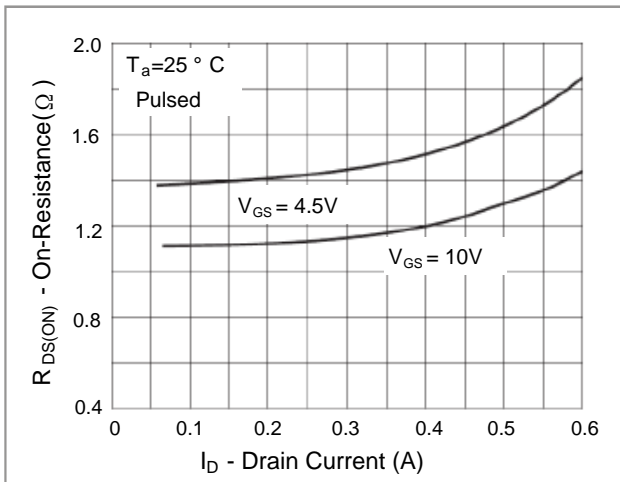


Fig.3 Drain-Source On-Resistance

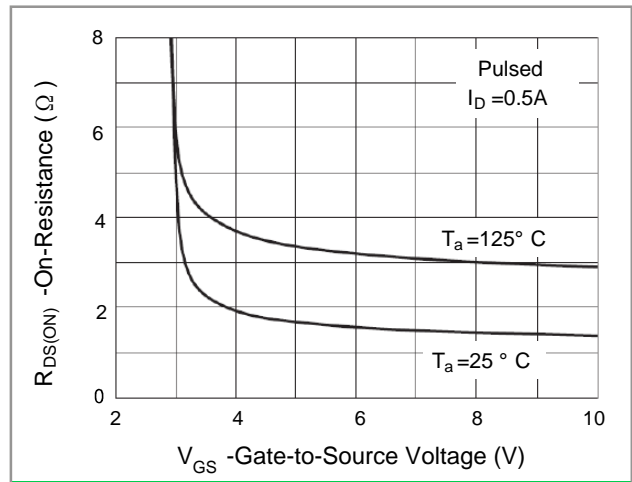


Fig.4 On Resistance VS Gate to Source Voltage

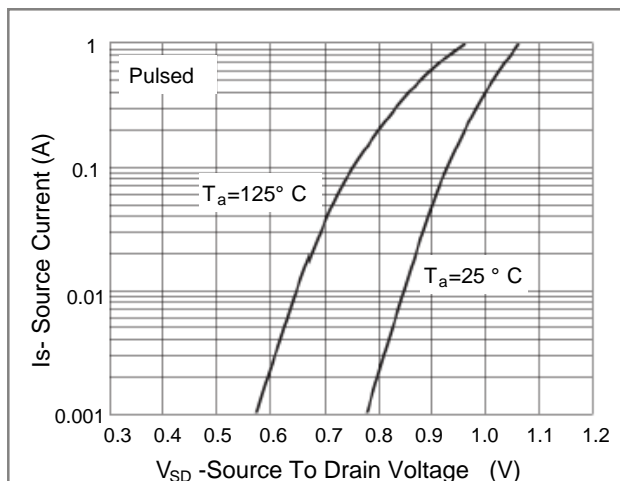


Fig.5 Source- Drain Diode Forward

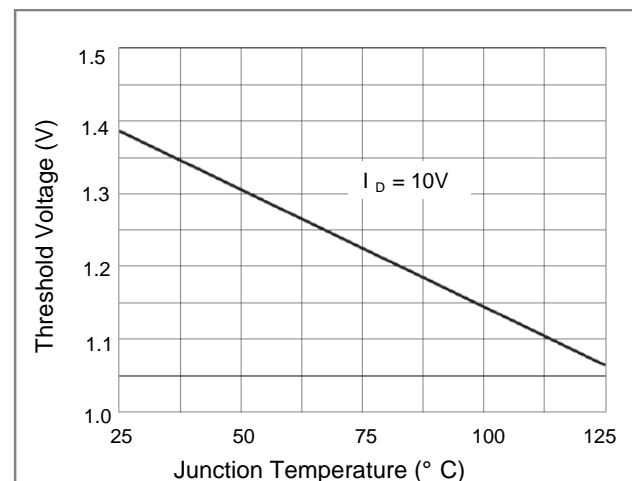


Fig.6 Threshold Voltage