

#### **Description**

The SI2300 uses advanced trench technology

to provide excellent R<sub>DS(ON)</sub>, low gate charge and

operation with gate voltages as low as 2.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

# D. S. G.

**SOT-23** 

#### **General Features**

 $V_{DS} = 20V I_{D} = 6.0A$ 

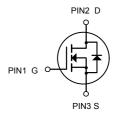
 $R_{DS(ON)}$  < 27m $\Omega$ @  $V_{GS}$ =4.5V

#### **Application**

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

## **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
SI2300	SOT-23	2300	3000

#### Absolute Maximum Ratings (T<sub>A</sub>=25 ℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V <sub>DS</sub>	Drain-Source Voltage	20	V
V <sub>G</sub> s	Gate-Source Voltage	±12	V
I <sub>D</sub>	Drain Current-Continuous	6	А
Ідм	Drain Current-Pulsed (Note 1)	25	А
P <sub>D</sub>	Maximum Power Dissipation	0.35	W
Тл,Тэтс	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$
Rеja	Thermal Resistance,Junction-to-Ambient (Note 2)	100	°C/W



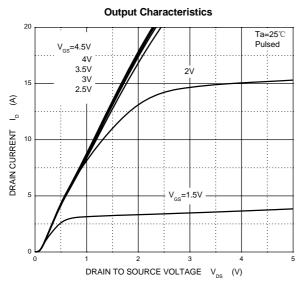
# $T_a$ =25 $^{\circ}$ C unless otherwise specified

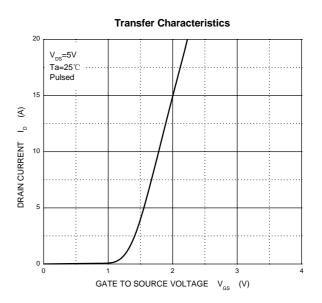
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
STATIC PARAMETERS			•			
Drain-source breakdown voltage	V (BR) DSS	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	20			V
Gate-source leakage current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =16V, V <sub>GS</sub> =0V			1.0	μΑ
Gate threshold voltage	V <sub>G</sub> S(th)	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5	0.7	1.0	V
Drain-source on-state resistance	D	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5.0A		22	27	mΩ
	RDS(on)	V <sub>GS</sub> =2.5V, I <sub>D</sub> =4.0A		35	42	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =2.0A			73	
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =1A		0.75	1	V
Forward transconductance	<b>9</b> fS	V <sub>DS</sub> =5V, I <sub>D</sub> =3.8A	4			S
DYNAMIC PARAMETERS*						
Input capacitance	C <sub>iss</sub>			630		
Output capacitance	Coss	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V,f =1MHz		164		pF
Reverse transfer capacitance	C <sub>rss</sub>	]		137		
Gate resistance	Rg	V <sub>DS</sub> =0V,V <sub>GS</sub> =0V,f =1MHz		1.5		Ω
SWITCHING PARAMETERS*			•			
Turn-on delay time	td(on)			5.5		
Rise time	tr	V <sub>GS</sub> =5V,V <sub>DS</sub> =10V,		14		no
Turn-off delay time	me t <sub>d(off)</sub> R <sub>L</sub> =1.70		=1.7Ω,R <sub>GEN</sub> =6Ω			ns
Fall time	tf			10.2		

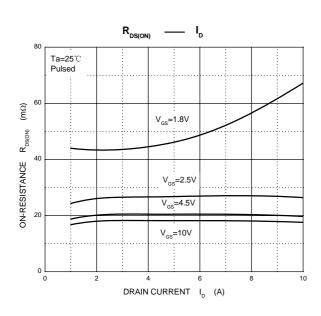
<sup>\*</sup>These parameters have no way to verify.

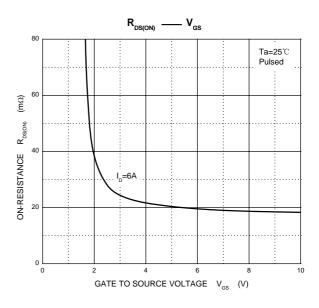


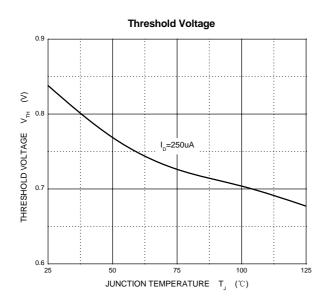
## **Typical Characteristics**

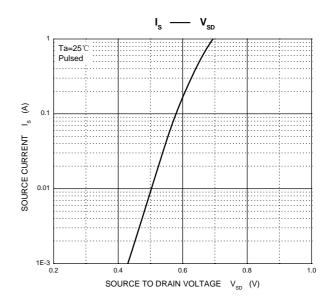






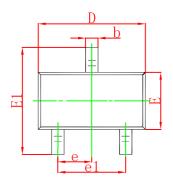


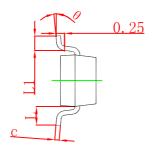


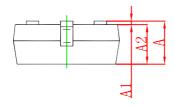




# **SOT-23 Package Outline Dimensions**

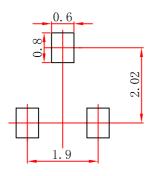






Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950	) TYP	0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

# **SOT-23 Suggested Pad Layout**



- Note:
  1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
  3.The pad layout is for reference purposes only.



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