

Description

The AP8P06S uses advanced trench technology to provide excellent $R_{\rm DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

 $V_{DS} = -60V I_{D} = -8A$

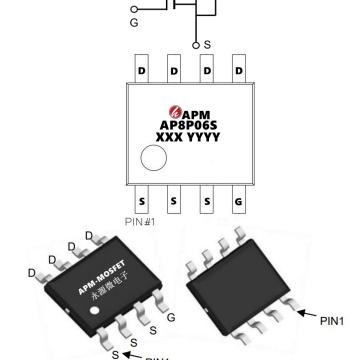
 $R_{DS(ON)} < 70 \text{m}\Omega$ @ $V_{GS}=10 \text{V}$ (Type: $55 \text{m}\Omega$)

Application

Brushless motor

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

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Product ID	Pack	Marking	Qty(PCS)	
AP8P06S	SOP-8	AP8P06S XXXX YYYY	3000	

Absolute Maximum Ratings (T_c=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units	
V _D s	Drain-Source Voltage	-60	V	
Vgs	Gate-Source Voltage	±20	V	
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-8.0	А	
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -10V ¹	-3	А	
Ірм	Pulsed Drain Current ²	-7.5	А	
EAS	Single Pulse Avalanche Energy ³	35.3	mJ	
las	Avalanche Current -26.6		А	
P _D @T _A =25°C	Total Power Dissipation ⁴ 1.5		W	
Тѕтс	Storage Temperature Range -55 to 150		°C	
TJ	Operating Junction Temperature Range -55 to 150		°C	
ReJA	Thermal Resistance Junction-Ambient ¹ 85		°C/W	
R _θ JC	Thermal Resistance Junction-Case ¹	36 °C/W		



Electrical Characteristics (T_A=25°Cunless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-60			V	
△BVDSS /△Tj	BV _{DSS} Temperature Coefficient	Reference to 25℃, I _D =-1mA		-0.03		V/°C	
DDO(ON)	0.1. 5 . 0 . 0 5	V _{GS} =-10V , I _D =-12A		55	70	m0	
KD2(ON)	RDS(ON) Static Drain-Source On-Resistance V _{GS} =-4.5V , I _D =-8A			64	105	mΩ	
VGS(th)	Gate Threshold Voltage)/)/ L 0504	-1.2	-1.5	-2.5	V	
△V _{GS(th)}	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=-250uA$		4.56		mV/℃	
IDCC	Dunin Course Lookens Courset	V _{DS} =-48V , V _{GS} =0V , T _J =25℃			1		
IDSS	Drain-Source Leakage Current	V _{DS} =-48V , V _{GS} =0V , T _J =55℃			5	uA	
IGSS	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA	
gfs	Forward Transconductance	V _{DS} =-5V , I _D =-12A		15.4		S	
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		13.5		Ω	
Qg	Total Gate Charge (-4.5V)			9.86			
Q _{gs}	Gate-Source Charge	V _{DS} =-48V , V _{GS} =-4.5V , I _D =-10A		3.08		nC	
Q_{gd}	Gate-Drain Charge			2.95			
Td(on)	Turn-On Delay Time			28.8			
Tr	Rise Time	V _{DD} =-15V , V _{GS} =-10V ,		19.8			
Td(off)	Turn-Off Delay Time	R _G =3.3□, I _D =-1A		60.8		ns	
T _f	Fall Time			7.2			
Ciss	Input Capacitance			1447			
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz		97.3		pF	
Crss	Reverse Transfer Capacitance			70			
Is	Continuous Source Current ^{1,5}	\/ -\/ -0\/ Faras O:			-18	Α	
ISM	Pulsed Source Current ^{2,5}	V _G =V _D =0V , Force Current			-36	Α	
VSD	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25℃			-1.2	V	

Note:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width $\leq 300 \text{us}$, duty cycle $\leq 2\%$
- 3. The EAS data shows Max. rating . The test condition is VDD=-25V,VGS=-10V,L=0.1mH,IAS=-26.6A
- 5. The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.

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Typical Characteristics

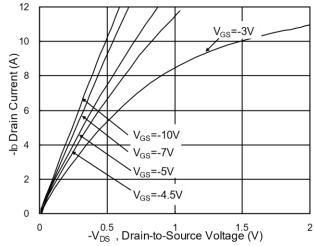


Fig.1 Typical Output Characteristics

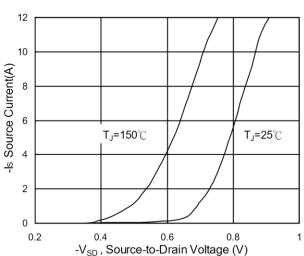


Fig.3 Forward Characteristics of Reverse

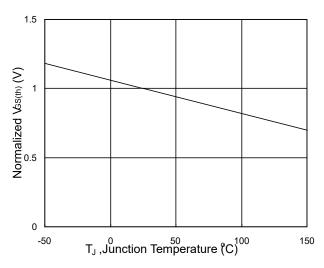


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

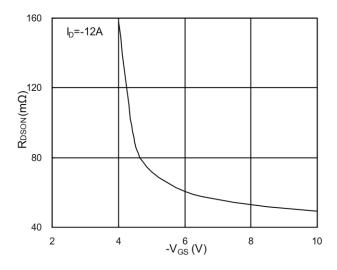


Fig.2 On-Resistance v.s Gate-Source

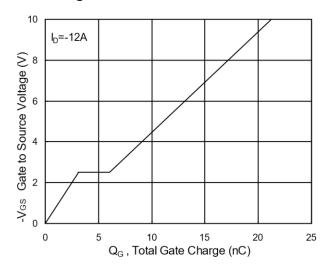


Fig.4 Gate-Charge Characteristics

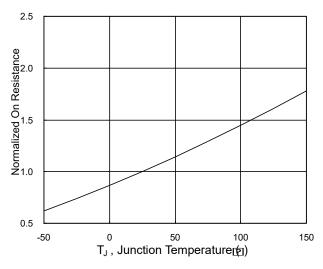
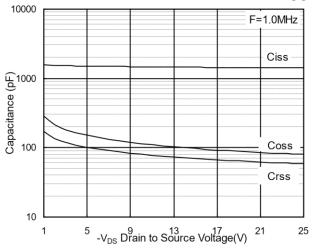


Fig.6 Normalized R_{DSON} v.s T_J







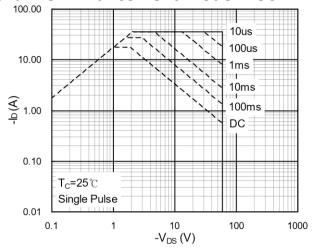


Fig.7 Capacitance

Fig.8 Safe Operating Area

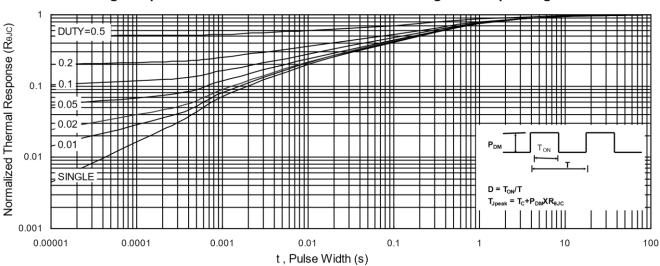


Fig.9 Normalized Maximum Transient Thermal Impedance

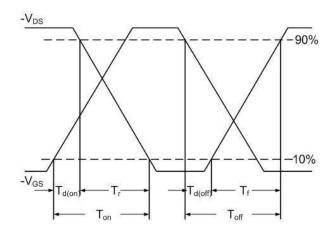


Fig.10 Switching Time Waveform

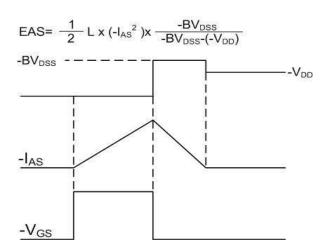
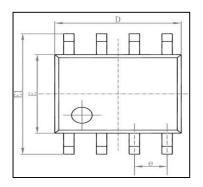


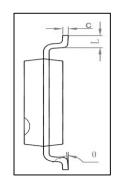
Fig.11 Unclamped Inductive Waveform

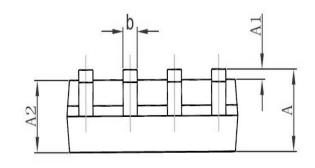
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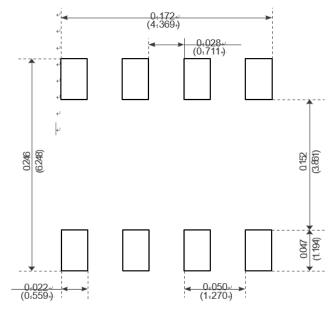
Package Mechanical Data-SOP-8







Ch - I	Dimensions Ir	n Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
Α	1. 350	1. 750	0. 053	0.069
A1	0. 100	0. 250	0. 004	0. 010
A2	1. 350	1. 550	0. 053	0. 061
b	0. 330	0. 510	0. 013	0. 020
С	0. 170	0. 250	0.006	0. 010
D	4. 700	5. 100	0. 185	0. 200
E	3. 800	4. 000	0. 150	0. 157
E1	5. 800	6. 200	0. 228	0. 244
е	1. 270	(BSC)	0.050	(BSC)
L	0. 400	1. 270	0. 016	0.050
θ	0°	8°	0°	8°



Recommended Minimum Pads-



-60V P-Channel Enhancement Mode MOSFET Attention

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Edition	Date	Change
Rve1.0	2018/1/31	Initial release

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