

#### **Description**

The AP80P04D uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

 $V_{DS} = -40V I_{D} = -80 A$ 

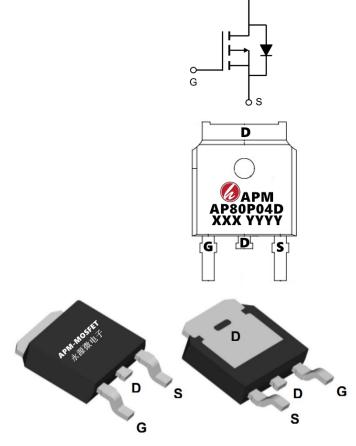
 $R_{DS(ON)} < -10m\Omega$  @  $V_{GS}=-10V$  (Type: 7.0m $\Omega$ )

#### **Application**

Battery protection

Load switch

Uninterruptible power supply



### **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
AP80P04D	TO-252-3L	AP80P04D XXX YYYY	2500

Absolute Maximum Ratings (TC=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units	
V <sub>D</sub> s	Drain-Source Voltage	-40	V	
Vgs	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-80	А	
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-56	А	
Ірм	Pulsed Drain Current <sup>2</sup> -280		А	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	500	mJ	
las	Avalanche Current	-50	А	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	52.1	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	62	°C/W	
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	2.4	°C/W	



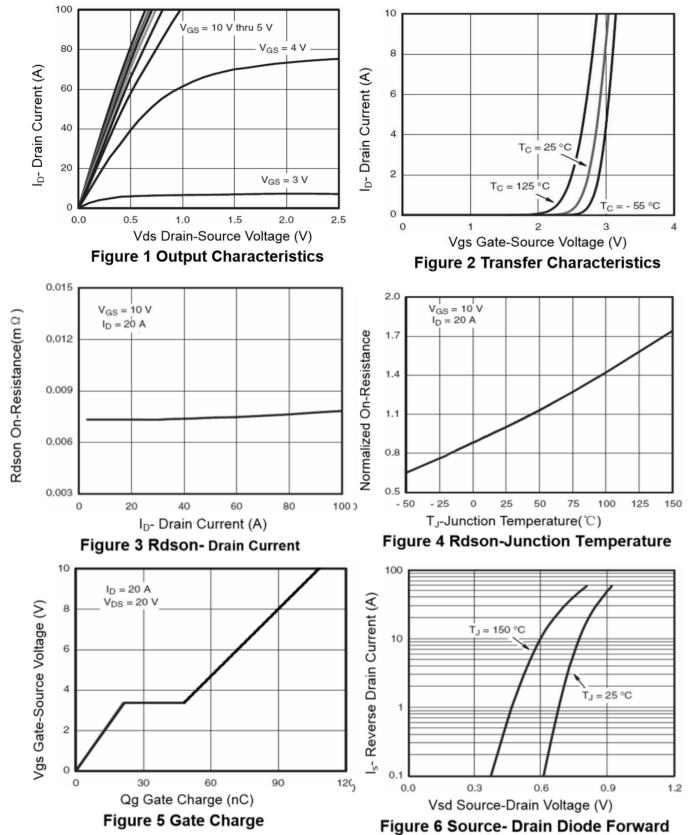
### Electrical Characteristics (T<sub>J</sub>=25℃, unless otherwise noted)

Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA		-40	-44		V	
∆BVdss/∆TJ	BV <sub>DSS</sub> Temperature Coefficient Reference to 25°C , I <sub>D</sub> =-1mA			-0.023		V/°C	
_		V <sub>GS</sub> =-10V , I <sub>D</sub> =-12A		7.0	10	mΩ	
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-12A		9.0	15		
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1.2	-1.8	-2.5	V	
Ipss	Drain-Source Leakage Current	V <sub>DS</sub> =-40V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA	
IDSS	Diain-Source Leakage Current	V <sub>DS</sub> =-40V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5		
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =-15V , I <sub>D</sub> =-12A		20		S	
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		7	14	Ω	
$Q_g$	Total Gate Charge (-4.5V)			27.9			
Qgs	Gate-Source Charge	V <sub>DS</sub> =-20V , V <sub>GS</sub> =-10V , I <sub>D</sub> =-12A		7.7		nC	
$Q_{gd}$	Gate-Drain Charge	.5 .=		7.5			
Td(on)	Turn-On Delay Time			40			
Tr	Rise Time	$V_{DD}$ =-20V , $V_{GS}$ =-10V , $R_{G}$ =3.0 $\Omega$ .		35.2		no	
Td(off)	Turn-Off Delay Time	I <sub>D</sub> =-12A		100		ns	
T <sub>f</sub>	Fall Time			9.6			
Ciss	Input Capacitance			6500			
Coss	Output Capacitance	V <sub>DS</sub> =-20V , V <sub>GS</sub> =0V , f=1MHz		790		pF	
Crss	Reverse Transfer Capacitance			605			
ls	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-70	Α	
Vsp	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1.2	٧	

#### Note:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width  $\leq 300 \text{us}$  , duty cycle  $\leq 2\%$
- $3\$  The EAS data shows Max. rating . The test condition is VDD=-32V,VGS=-10V,L=0.1mH,IAS=-50A
- 5. The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.

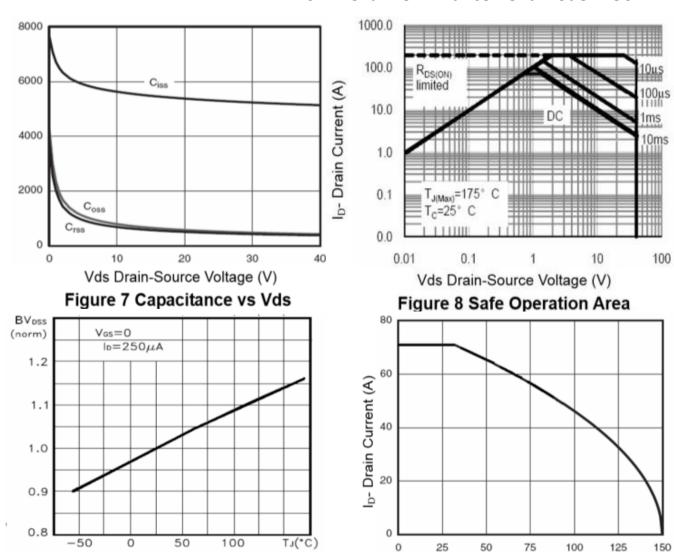






C Capacitance (pF)

### -40V P-Channel Enhancement Mode MOSFET



T<sub>J</sub>-Junction Temperature(°C)

Figure 9 BV<sub>DSS</sub> vs Junction Temperature

Figure 10 ID Current Derating vs Junction Temperature

T<sub>J</sub>-Junction Temperature(°C)

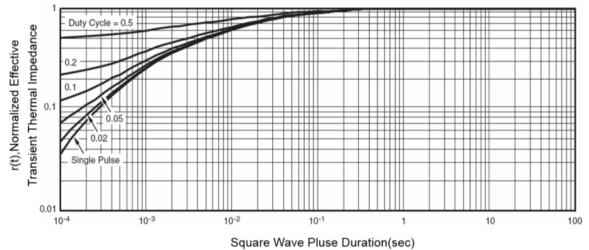
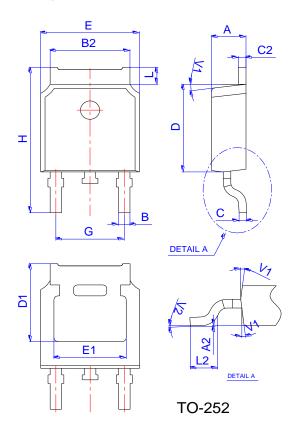


Figure 11 Normalized Maximum Transient Thermal Impedance

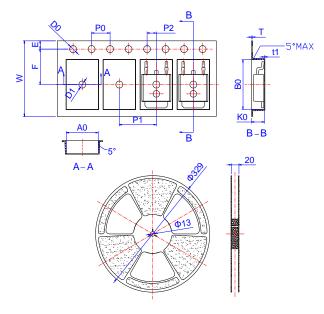


# Package Mechanical Data: TO-252-3L



	Dimensions					
Ref.	. Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
В	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
С	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
Н	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

# **Reel Spectification-TO-252**



	Dimensions					
Ref.		Millimeters		Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
Е	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
В0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
Т	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583



#### **Attention**

- 1,Any and all APM Microelectronics products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your APM Microelectronics representative nearest you before using any APM Microelectronics products described or contained herein in such applications.
- 2,APM Microelectronics assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all APM Microelectronics products described or contained herein.
- 3, Specifications of any and all APM Microelectronics products described or contained here instipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- 4, APM Microelectronics Semiconductor CO., LTD. strives to supply high quality high reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. Whendesigning equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- 5,In the event that any or all APM Microelectronics products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- 6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of APM Microelectronics Semiconductor CO., LTD.
- 7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. APM Microelectronics believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- 8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "DeliverySpecification" for the APM Microelectronics product that you Intend to use.







Edition	Date	Change
Rve1.0	2020/10/8	Initial release

Copyright Attribution"APM-Microelectronice"