

#### **Description**

The AP80P01NF uses advanced trench technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a

Battery protection or in other Switching application.

#### **General Features**

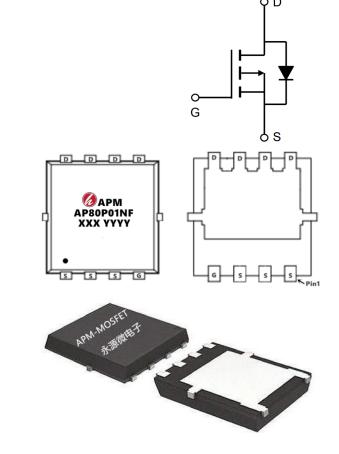
 $V_{DS} = -12V \quad I_{D} = -80A$ 

 $R_{DS(ON)} < 4.5 m\Omega$  @  $V_{GS}=10V$ 

#### **Application**

Load switch

electronic cigarette



#### **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
AP80P01NF	PDFN5*6-8L	AP80P01NF XXX YYYY	2500

## **Absolute Maximum Ratings** (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-12	V
VGS	Gate-Source Voltage	±12	V
ID.	Drain Current – Continuous (T <sub>C</sub> =25°C)	-80	Α
ID	Drain Current – Continuous (Tc=100℃)	-54	Α
IDM	Drain Current – Pulsed <sup>1</sup>	-240	Α
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> =25℃)	41.67	W
	Power Dissipation – Derate above 25℃	0.33	W/℃
TSTG	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$
R₀JA	Thermal Resistance Junction to ambient	62	°CM
R₀JC	Thermal Resistance Junction to Case	3	°CM



#### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

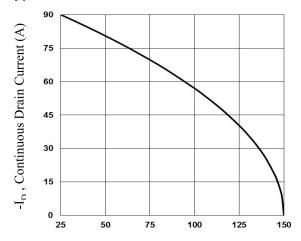
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	VGS=0V , ID=-250uA	-15	-17		V	
△BVDSS/△TJ	BVDSS Temperature Coefficient	Reference to 25℃, ID=-1mA		-0.008		V/°C	
	Static Drain-Source On-Resistance	VGS=-4.5V , ID=-20A		3.5	4.5	mΩ	
RDS(ON)		VGS=-2.5V , ID=-20A		4.8	6.0		
VGS(th)	Gate Threshold Voltage		-0.4	-0.6	-1.0	V	
△VGS	VGS(th) Temperature Coefficient	VGS=VDS , ID =-250uA		-3.44		mV/℃	
1500		VDS=-20V , VGS=0V , TJ=25℃			-1	uA	
IDSS	Drain-Source Leakage Current	VDS=-16V ,VGS=0V ,TJ=125℃			-30	uA	
IGSS	Gate-Source Leakage Current	VGS=±12V , VDS=0V			±500	nA	
gfs	Forward Transconductance	VDS=-10V , IS=-3A		30		S	
Qg	Total Gate Charge2 , 3			149	225		
Qgs	Gate-Source Charge2, 3	VDS=-16V , VGS=-4.5V , ID=-		14.4	22		
Qgd	Gate-Drain Charge2, 3	5A		42.8	65	nC	
Td(on)	Turn-On Delay Time2, 3			21.2	42		
Tr	Rise Time2 , 3	VDD=-15V , VGS=-4.5V ,		20.6	40		
Td(off)	Turn-Off Delay Time2, 3	RG=25 ID=-1A		26	52	nS	
Tf	Fall Time2, 3	15- 17		400	600		
Ciss	Input Capacitance			6800			
Coss	Output Capacitance	VDS=-15V , VGS=0V , F=1MHz		769		_	
Crss	Reverse Transfer Capacitance			726		pF	
Rg	Gate resistance	VGS=0V, VDS=0V, F=1MHz		2.6		Ω	
IS	Contineous Source Current	V VI 0V 5			-90		
ISM	Pulsed Source Current	Vg=Vd=0V, Force Current			-180	Α	
VSD	Diode Forward Voltage	Vgs=0V Is=1A Tj=25℃			-1	V	

#### Note:

- Repetitive Rating : Pulsed width limited by maximum junction temperature. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- Essentially independent of operating temperature.



#### **Typical Characteristics**



 $T_C$  , Case Temperature (°C)

Fig.1 Continuous Drain Current

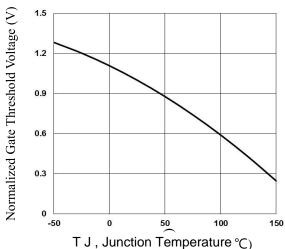
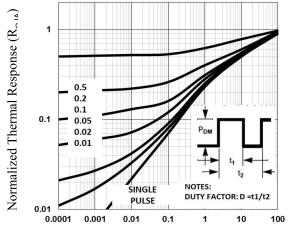


Fig.3 Normalized V<sub>th</sub> vs.



Square Wave Pulse Duration (s)

Fig.5 Normalized Transient Response

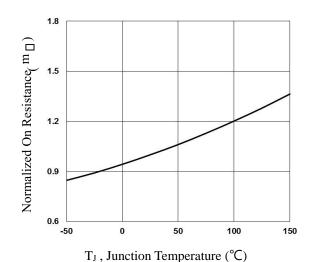
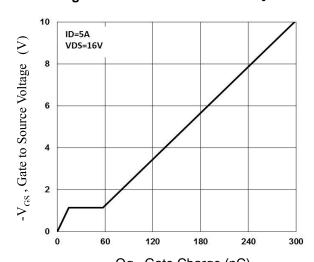
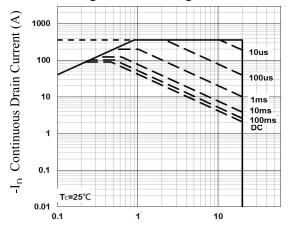


Fig.2 Normalized RDSON vs. TJ



Qg , Gate Charge (nC) Fig.4 Gate Charge Waveform



-V<sub>DS</sub> , Drain to Source Voltage (V)

Fig.6 Maximum Safe Operation Area





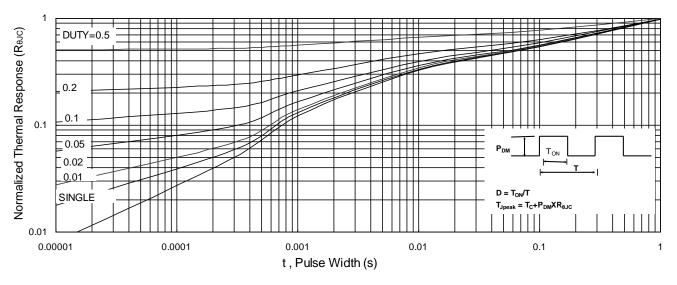
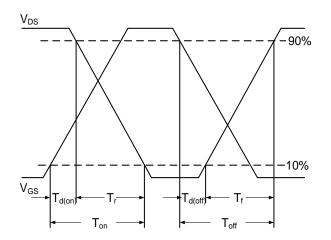


Fig.9 Normalized Maximum Transient Thermal Impedance



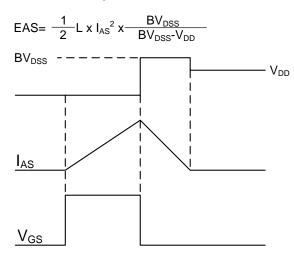
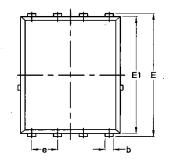
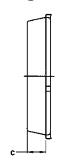


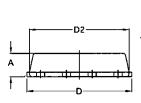
Fig.11 Unclamped Inductive Switching Waveform

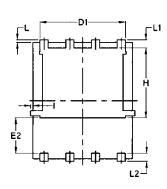


# Package Mechanical Data-DFN5\*6-8L-JQ Single









		Com	mon		
Symbol	mm		Inch		
	Mim	Max	Min	Max	
Α	1.03	1.17	0.0406	0.0461	
b	0.34	0.48	0.0134	0.0189	
С	0.824	0.0970	0.0324	0.082	
D	4.80	5.40	0.1890	0.2126	
D1	4.11	4.31	0.1618	0.1697	
D2	4.80	5.00	0.1890	0.1969	
E	5.95	6.15	0.2343	0.2421	
E1	5.65	5.85	0.2224	0.2303	
E2	1.60	/	0.0630	/	
е	1.27	BSC	0.05	BSC	
L	0.05	0.25	0.0020	0.0098	
L1	0.38	0.50	0.0150	0.0197	
L2	0.38	0.50	0.0150	0.0197	
Н	3.30	3.50	0.1299	0.1378	
I	/	0.18	/	0.0070	



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# AP80P01NF

## -12V P-Channel Enhancement Mode MOSFET

Edition	Date	Change
Rve1.0	2020/10/22	Initial release

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