

68V N-Channel Enhancement Mode MOSFET

Description

The AP80N07F uses advanced trench technology

to provide excellent $R_{\text{DS}(\text{ON})},$ low gate charge and

operation with Hight EAS. This device is suitable

for use as a Battery protection

or in other Switching application.

General Features

V_{DS} = 68V I_D =80A

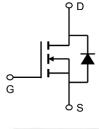
 $R_{DS(ON)} < 9.0m\Omega @ V_{GS}=10V (Type: 7.2m\Omega)$

Application

Battery protection

Load switch

Uninterruptible power supply







Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP80N07F	TO-220-3L	AP80N07F XXX YYYY	1000

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	68	V	
VGS	Gate-Source Voltage	±20	V	
I₀@Tc=25℃	Continuous Drain Current, V _{GS} @ 10V ¹	80	А	
I _D @T _C =100℃	Continuous Drain Current, V _{GS} @ 10V ¹	52	А	
IDM	Pulsed Drain Current ²	320	А	
EAS	Single Pulse Avalanche Energy ³	110	mJ	
IAS	Avalanche Current	22	А	
P₀@Tc=25℃	Total Power Dissipation ⁴	103	W	
TSTG	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
R ₀ JA	Thermal Resistance Junction-ambient ¹	63	°C/W	
R ₀ JC	Thermal Resistance Junction-Case ¹	1.46	°C/W	

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Electrical Characteristics (TJ=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	68	72		V
∆BVDSS/∆TJ	BVDSS Temperature Coefficient	Reference to 25 $^\circ\!\!\mathbb{C}$, I_D=1mA		0.023		V/℃
RDS(ON)	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =10A		7.5	9.0	mΩ
VGS(th)	Gate Threshold Voltage V _{GS} =V _{DS} , I _D =250uA		2.0	3.0	4.0	V
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	VGS-VDS, ID -2300A		-4.2		mV/℃
IDSS	Drain-Source Leakage Current	$V_{\text{DS}}\text{=}68V$, $V_{\text{GS}}\text{=}0V$, $T_{\text{J}}\text{=}25^\circ\!\mathbb{C}$			1	uA
		$V_{\text{DS}}\text{=}68V$, $V_{\text{GS}}\text{=}0V$, $T_{\text{J}}\text{=}55^\circ\!\mathbb{C}$			5	
IGSS	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA
Qg	Total Gate Charge (4.5V)			35		nC
Qgs	Gate-Source Charge	VDS =30V, ID =30A, VGS =10V		11		
Qgd	Gate-Drain Charge			9		
Td(on)	Turn-On Delay Time			15		ns
Tr	Rise Time	VDS =30V,ID =30A,		90		
Td(off)	Turn-Off Delay Time	RGEN =3Ω, V GS =10V		45		
T _f	Fall Time			30		
Ciss	Input Capacitance			400		
Coss	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		267		pF
Crss	Reverse Transfer Capacitance			250		
IS	Continuous Source Current ^{1,5}				80	Α
ISM	Pulsed Source Current ^{2,5}	$V_G=V_D=0V$, Force Current			320	Α
VSD	Diode Forward Voltage ²	V GS =0V, I S =80A			1.2	V
trr	Reverse Recovery Time	T J =25℃		78		nS
Qrr	Reverse Recovery Charge	I F =20A,dI/dt=100A/μs		51		nC

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

 $2 \ensuremath{\scriptstyle \sim}$ The data tested by pulsed , pulse width .The EAS data shows Max. rating .

3、The test cond≦ 300us duty cycle ≦ 2%, duty cycle ition is TJ =25℃, VDD =35V, VG =10V, R G =25Ω, L=0.5mH, IAS =21A

4 The power dissipation is limited by 175° junction temperature

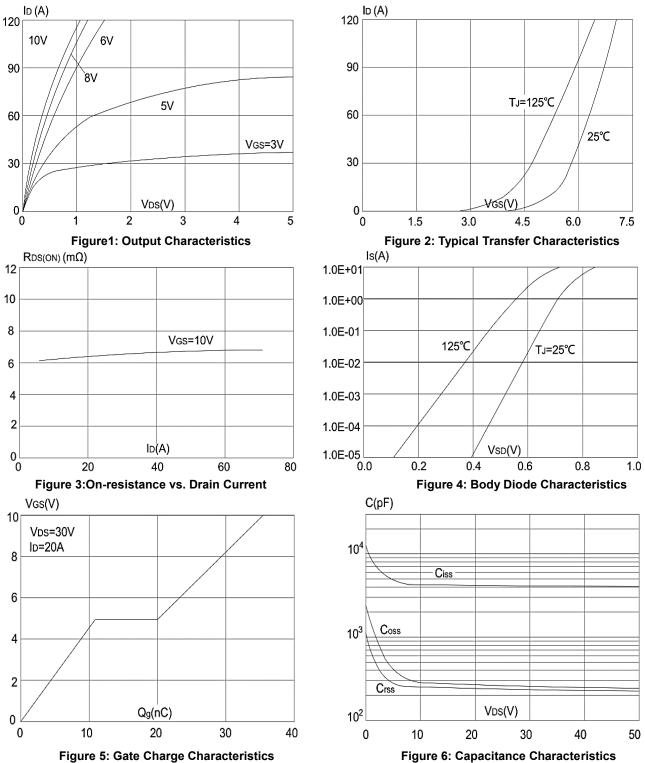
5. The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

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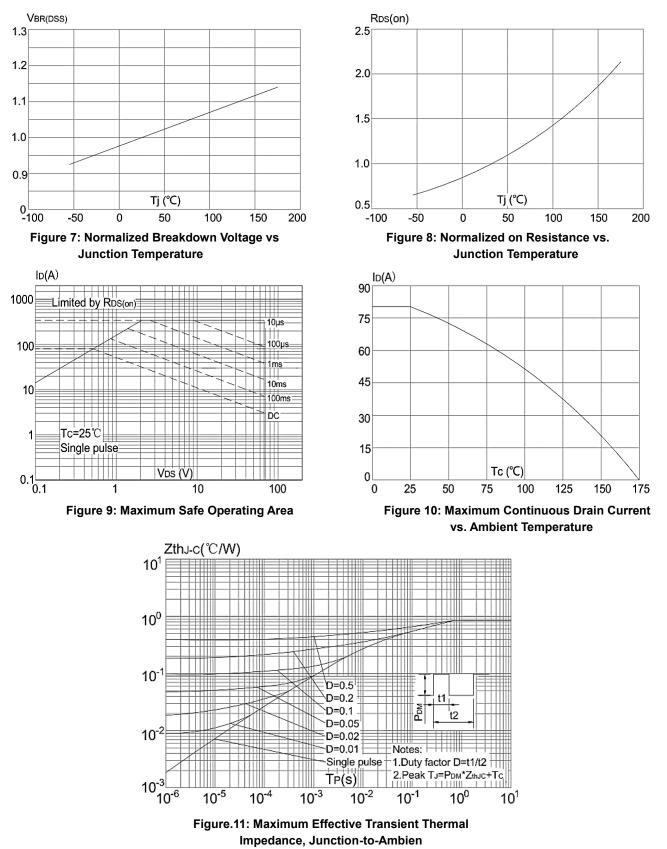
Typical Characteristics





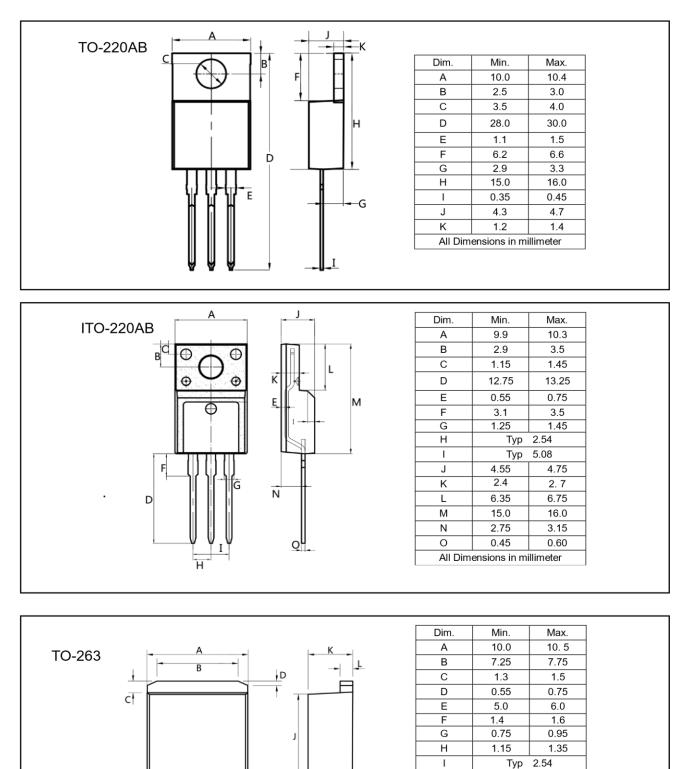
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0.02

2.4

0.35

All Dimensions in millimeter

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Edition	Date	Change
RVE1.0	2018/12/21	Initial release

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