

# AP2313MI

### -12V P-Channel Enhancement Mode MOSFET

#### Description

The AP2313MI uses advanced trench technology

to provide excellent R<sub>DS(ON)</sub>, low gate charge and

operation with gate voltages as low as 2.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = -12V I<sub>D</sub> =-8A

 $R_{DS(ON)} < 20m\Omega @ V_{GS}=4.5V$ 

#### Application

Lithium battery protection

Wireless impact

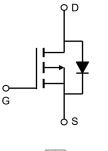
Mobile phone fast charging

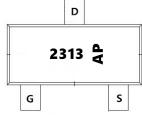
#### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP2313MI	SOT-23-3L	2313-AP	3000

#### Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-12	V
VGS	Gate-Source Voltage	±12	V
ID@TA=25°C	Continuous Drain Current	8.0	A
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current	5.3	A
IDM	Pulsed Drain Current <sup>2</sup>	40	A
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>3</sup>	1	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-ambient <sup>1</sup>	125	°C/W
R₀JA	Thermal Resistance Junction-Ambient <sup>1</sup> (t ≤10s)	85	°C/W









# AP2313MI

### -12V P-Channel Enhancement Mode MOSFET

#### Electrical Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> = -250µA	-12	-16	-	V
∆BVDSS/∆TJ	BVDSS Temperature Coefficient	Reference to 25℃, I <sub>D</sub> =1mA		0.029		V/°C
VGS(th)	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = -250µA	-0.4	-0.7	-1.0	V
	Static Drain-Source on-Resistance note2	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -8A	-	16	20	mΩ
RDS(on)		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -5A	-	20	25	
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> = -12V, V <sub>GS</sub> =0V,	-	-	-1	μA
IGSS	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±12V	-	-	±100	nA
Ciss	Input Capacitance		-	2700	-	pF
Coss	Output Capacitance	V <sub>DS</sub> = -6V, V <sub>GS</sub> =0V, f=1.0MHz	-	680	-	pF
Crss	Reverse Transfer Capacitance		-	590	-	pF
Qg	Total Gate Charge	V <sub>DS</sub> = -6V, I <sub>D</sub> = -8A,	-	35	-	nC
Qgs	Gate-Source Charge	VBS= -0 V, ID= -0A, VGS= -4.5V	-	5	-	nC
$Q_gd$	Gate-Drain("Miller") Charge		-	10	-	nC
td(on)	Turn-on Delay Time		-	11	-	ns
tr	Turn-on Rise Time	V <sub>DD</sub> = -6V, I <sub>D</sub> = -8A, V <sub>GS</sub> = -4.5V,	-	35	-	ns
td(off)	Turn-off Delay Time	R <sub>GEN</sub> =2.5Ω	-	30	-	ns
t <sub>f</sub>	Turn-off Fall Time			10	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	-16	А
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-64	А
VSD	Drain to Source Diode Forward Voltage V <sub>GS</sub> =0V, I <sub>S</sub> = -16A		-	-0.8	-1.2	V

Notes:

1 、Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2.、Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%

N



### -12V P-Channel Enhancement Mode MOSFET

#### **Typical Characteristics**

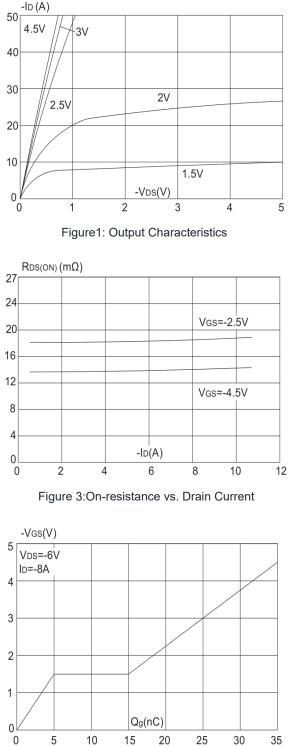


Figure 5: Gate Charge Characteristics

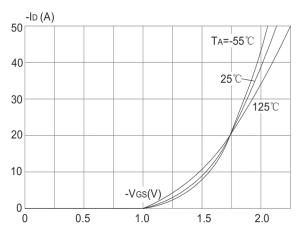


Figure 2: Typical Transfer Characteristics

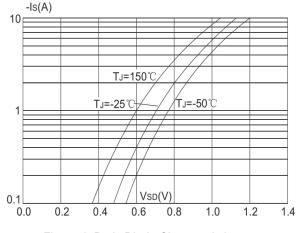
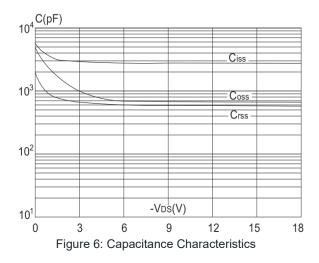


Figure 4: Body Diode Characteristics



ω



### -12V P-Channel Enhancement Mode MOSFET

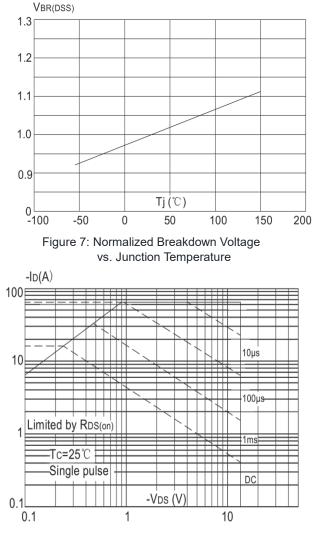


Figure 9: Maximum Safe Operating Area Case Temperature

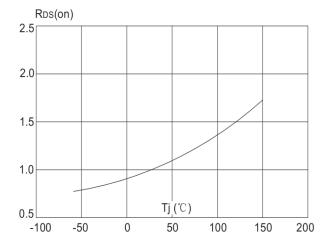


Figure 8: Normalized on Resistance vs. Junction Temperature

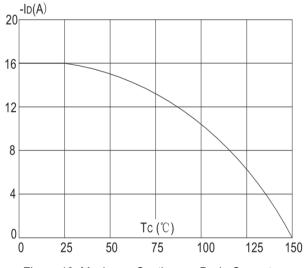


Figure 10: Maximum Continuous Drain Current vs.

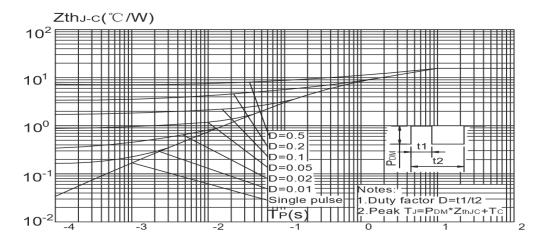
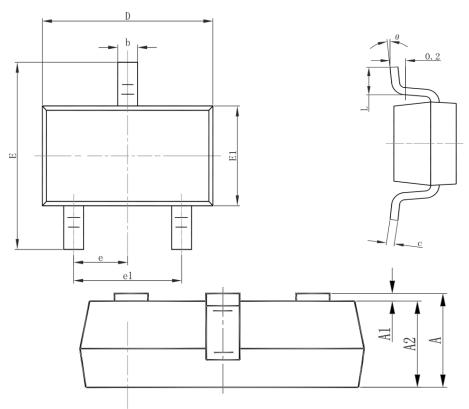


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



-12V P-Channel Enhancement Mode MOSFET

# Package Mechanical Data-SOT23-3



Gumbal	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
А	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

### -12V P-Channel Enhancement Mode MOSFET

### Attention

1,Any and all APM Microelectronics products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your APM Microelectronics representative nearest you before using any APM Microelectronics products described or contained herein in such applications.

2,APM Microelectronics assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all APM Microelectronics products described or contained herein.

3, Specifications of any and all APM Microelectronics products described or contained here instipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

4, APM Microelectronics Semiconductor CO., LTD. strives to supply high quality high reliabilityproducts. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. Whendesigning equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

5, In the event that any or all APM Microelectronics products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of APM Microelectronics Semiconductor CO., LTD.

7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. APM Microelectronics believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "DeliverySpecification" for the APM Microelectronics product that you Intend to use.

σ



# AP2313MI

### -12V P-Channel Enhancement Mode MOSFET

Edition	Date	Change
Rve1.0	2020/6/19	Initial release

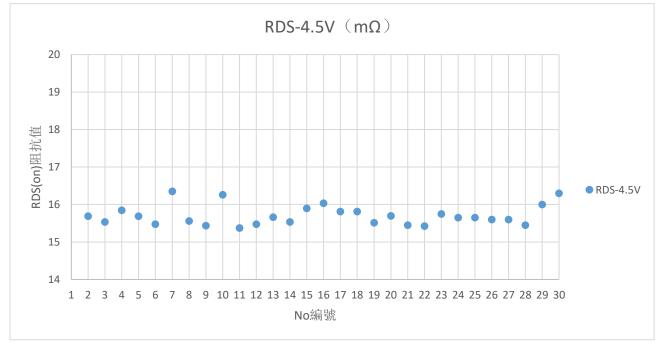
Copyright Attribution"APM-Microelectronice"

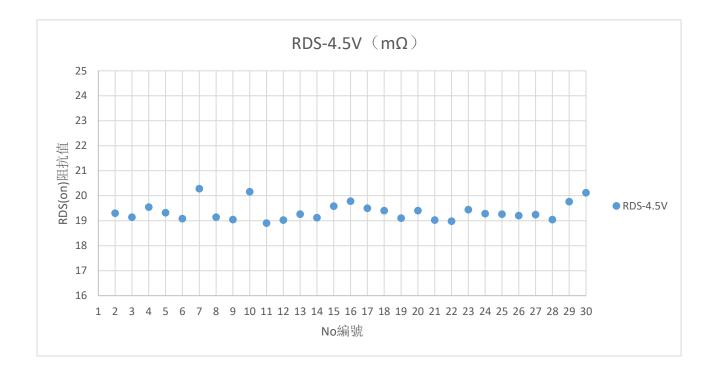
 $\neg$ 



### -12V P-Channel Enhancement Mode MOSFET

## Test Report For 30PCS (30pcs 典型測試報告)





 $\mathbf{0}$ 



#### -12V P-Channel Enhancement Mode MOSFET

