

## 40V N+N-Channel Enhancement Mode MOSFET

### Description

The AP20H04NF uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### General Features

$V_{DS} = 40V$   $I_D = 20A$

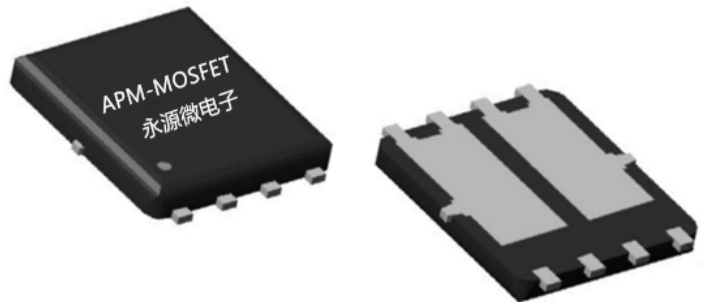
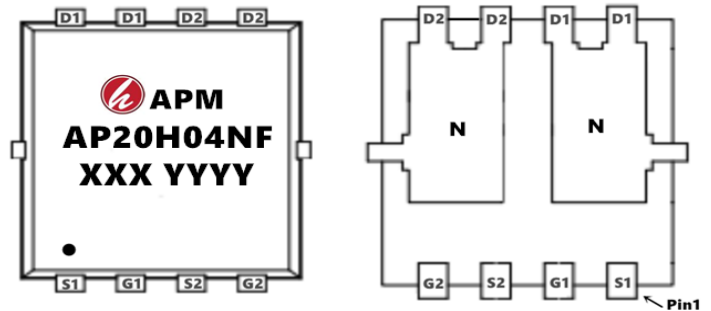
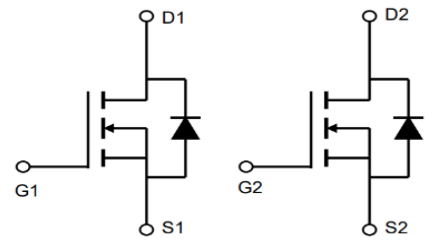
$R_{DS(ON)} < 16m\Omega @ V_{GS}=10V$

### Application

Battery protection

Load switch

Uninterruptible power supply



### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP20H04NF	PDFN5*6-8L	AP20H04NF	5000

### Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

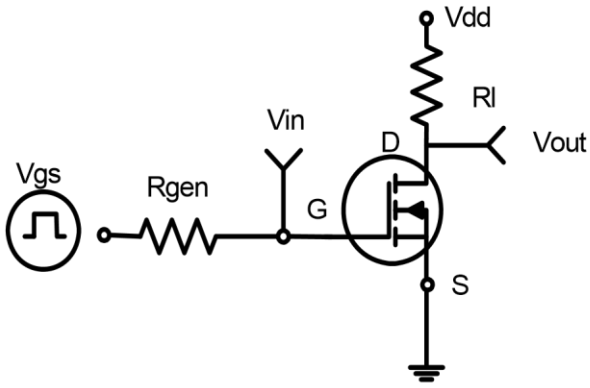
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	20	A
Drain Current-Continuous( $T_C=100^\circ C$ )	$I_D(100^\circ C)$	8.4	A
Pulsed Drain Current	$I_{DM}$	100	A
Maximum Power Dissipation	$P_D$	2	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$
Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	62.5	$^\circ C/W$

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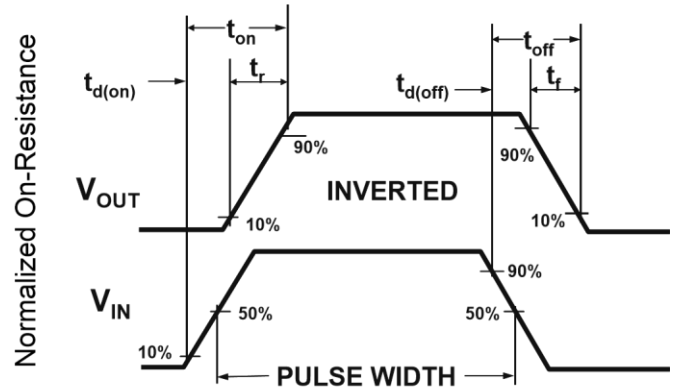
### N-CH Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1	1.5	2.0	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =8A	-	10.5	16	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	-	18.9	24	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =8A	33	-	-	S
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, F=1.0MHz	-	964	-	PF
Output Capacitance	C <sub>oss</sub>		-	109	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	96	-	PF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =20V, R <sub>L</sub> =2.5Ω V <sub>GS</sub> =10V, R <sub>GEN</sub> =3Ω	-	5.5	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	24	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =20V, I <sub>D</sub> =8A, V <sub>GS</sub> =10V	-	22.9	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	5.3	-	nC
Diode Forward Voltage <sup>(Note 3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =9A	-	0.8	1.2	V

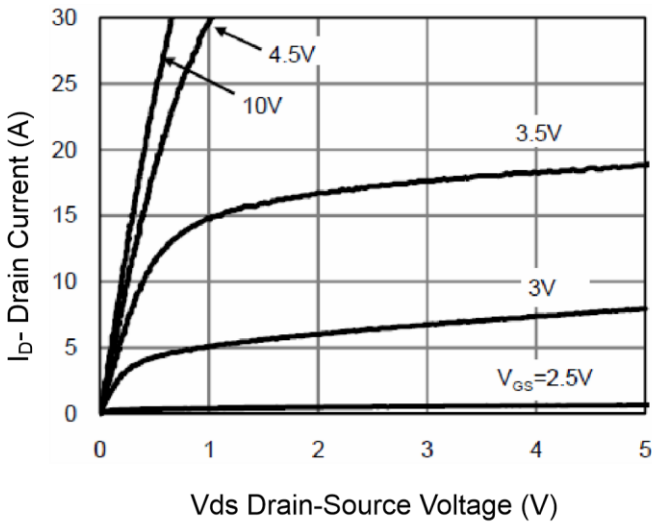
**40V N+N-Channel Enhancement Mode MOSFET**



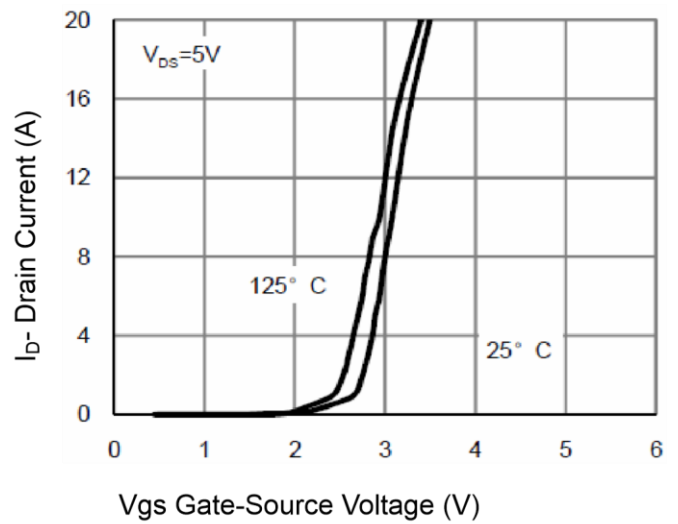
**Figure 1: Switching Test Circuit**



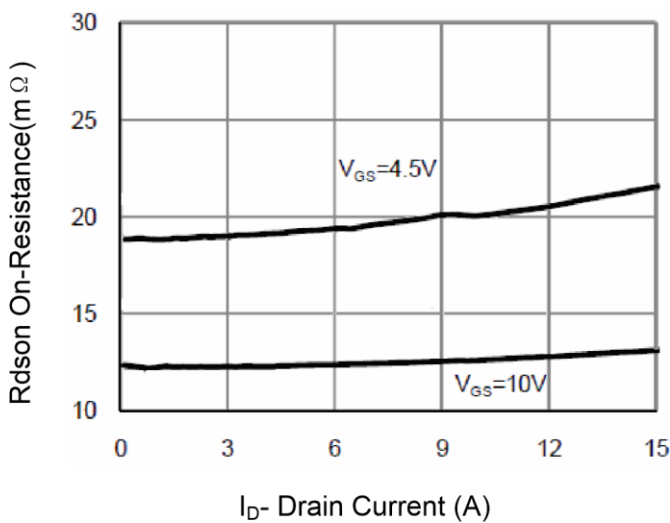
**Figure 2: Switching Waveforms**



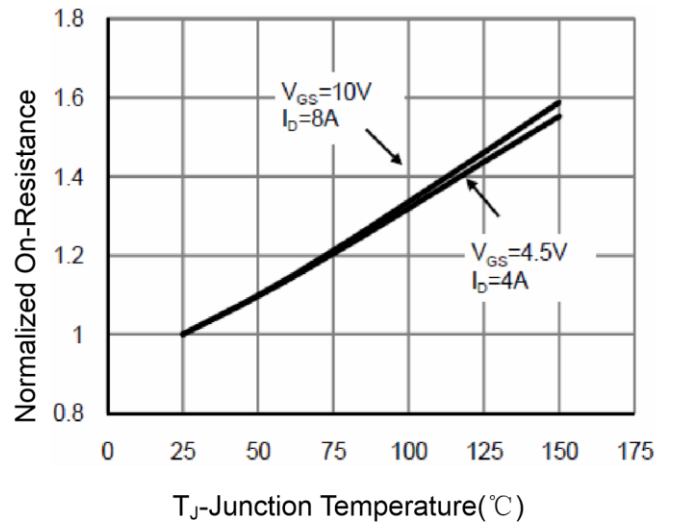
**Figure 3 Output Characteristics**



**Figure 4 Transfer Characteristics**

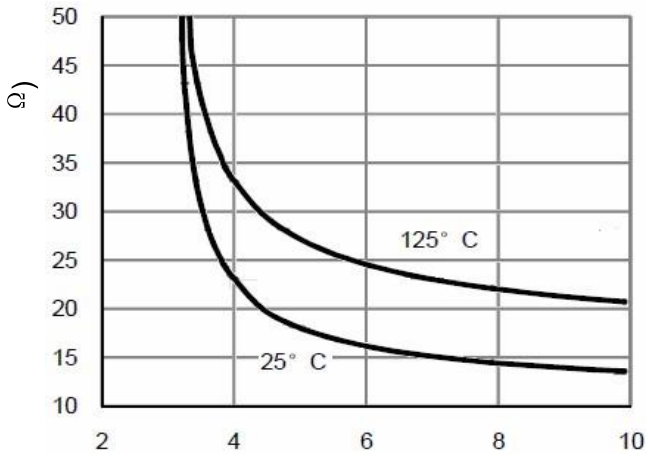


**Figure 5 Drain-Source On-Resistance**

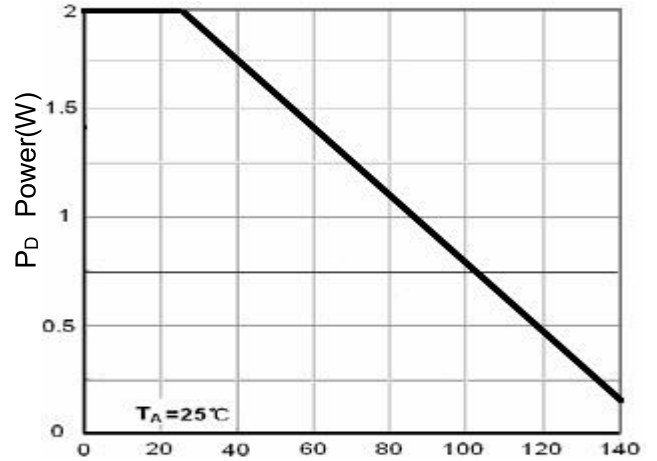


**Figure 6 Drain-Source On-Resistance**

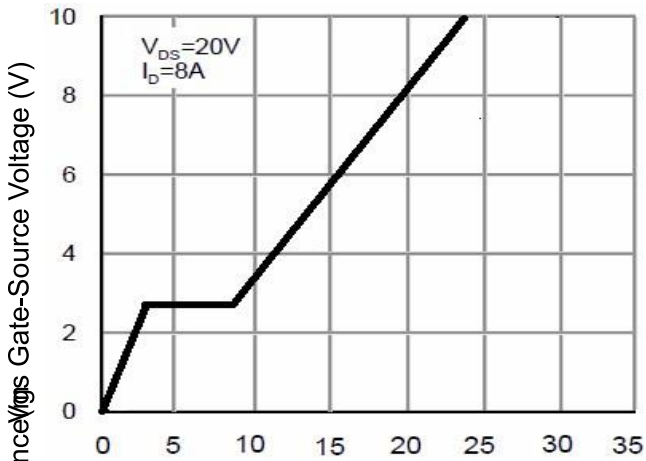
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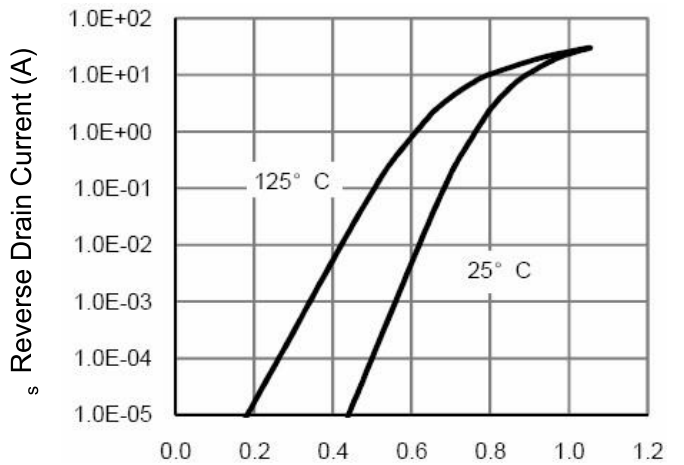
Vgs Gate-Source Voltage (V)  
**Figure 7 Rdson vs Vgs**



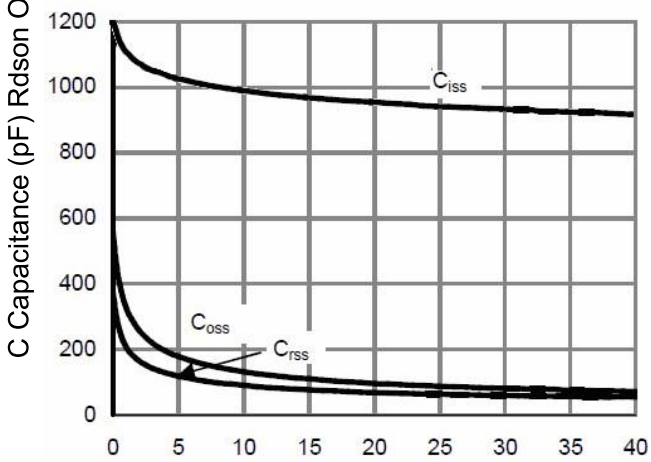
T<sub>J</sub>-Junction Temperature(°C)  
**Figure 8 Power Dissipation**



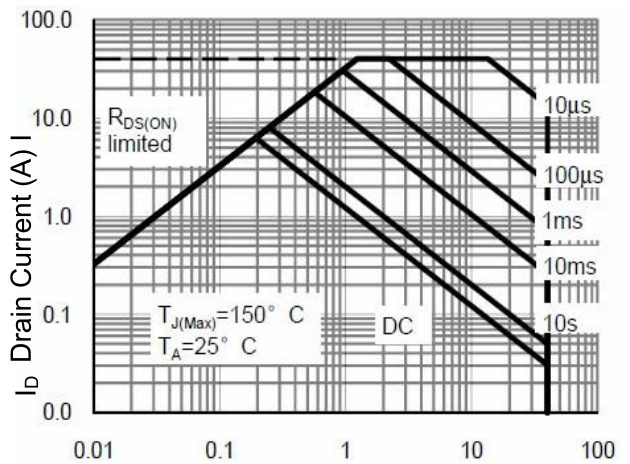
Qg Gate Charge (nC)  
**Figure 9 Gate Charge**



Vds Drain-Source Voltage (V)  
**Figure 10 Source- Drain Diode Forward**

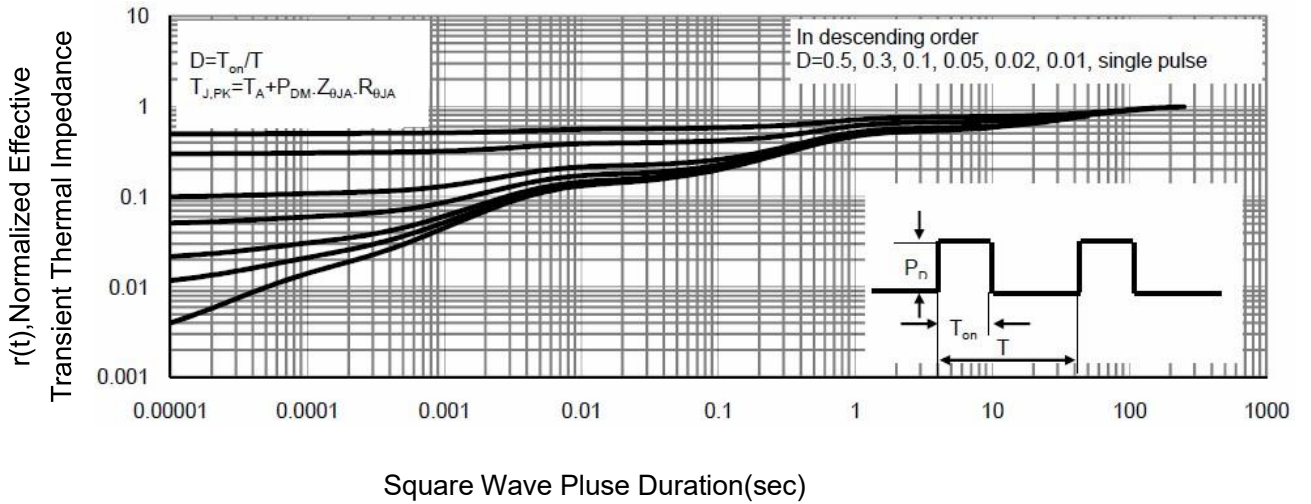


Vds Drain-Source Voltage (V)  
**Figure 11 Capacitance vs Vds**



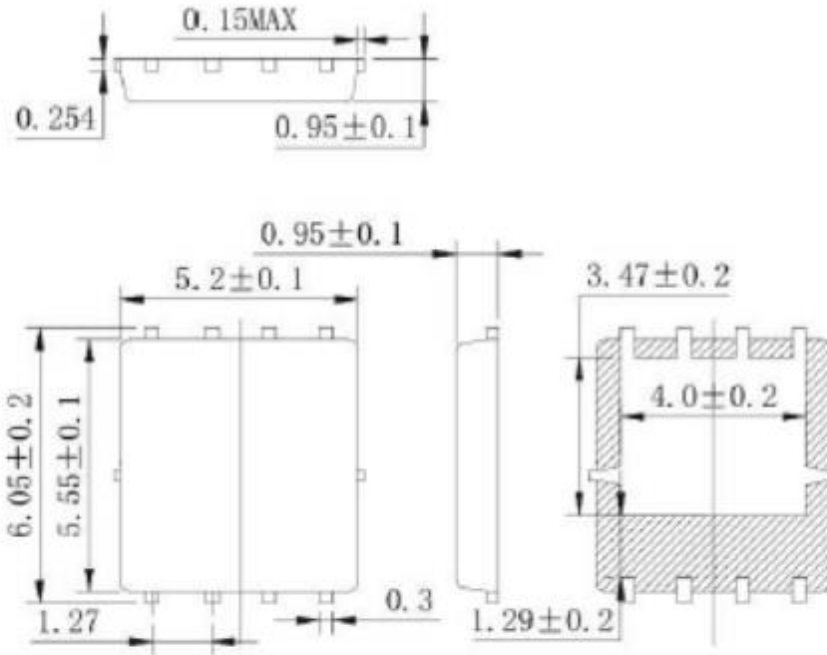
Vds Drain-Source Voltage (V)  
**Figure 12 Safe Operation Area**

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**Figure 13 Normalized Maximum Transient Thermal Impedance**

**DFN5\*6-XW-01**



**40V N+N-Channel Enhancement Mode MOSFET****Attention**

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