

Description

The AP150P03NF uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



 $V_{DS} = -30V I_{D} = -150A$

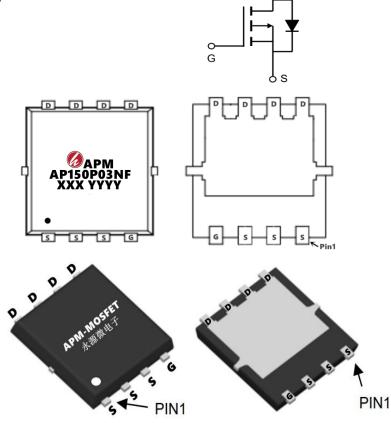
 $R_{DS(ON)} < 3.2 \text{m}\Omega$ @ $V_{GS} = -10 \text{V}$ (Type: 2.5 m Ω)

Application

Lithium battery protection

Wireless impact

Mobile phone fast charging



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP150P03NF	PDFN5*6-8L	AP150P03NF XXX YYYY	5000

Absolute Maximum Ratings (TC=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	-30	V	
VGS	Gate-Source Voltage	±20	V	
ID@TC=25℃	Continuous Drain Current, VGS @ -10V1 -150		А	
ID@TC=100°C	Continuous Drain Current, VGS @ -10V1 -75		А	
IDM	Pulsed Drain Current2	-450	А	
EAS	Single Pulse Avalanche Energy3	576	mJ	
IAS	Avalanche Current -70		А	
PD@TC=25℃	Total Power Dissipation4	150	W	
TSTG	Storage Temperature Range	-55 to 150	$^{\circ}$	
TJ	Operating Junction Temperature Range -55 to 150		$^{\circ}$ C	
RθJA	Thermal Resistance Junction-Ambient 1	25	25 °C/W	
RθJC	Thermal Resistance Junction-Case1	1.06	1.06 °C/W	





Electrical Characteristics (T_J=25 °C, unless otherwise noted)

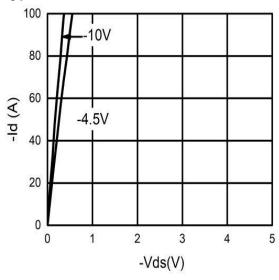
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250µA	-30	-35		V
IDSS	Zero Gate Voltage Drain Current	V _{DS} =-30V, V _{GS} =0V			-1	μA
IGSS	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=-250μA	-1	-1.7	-2.5	>
DDC(ON)	Drain-Source On-State Resistance	V _{GS} =-10V, I _D =-20A		2.5	3.2	mΩ
RDS(ON)		V _{GS} =-4.5V, I _D =-20A		4.0	5.2	mΩ
gFS	Forward Transconductance	V _{DS} =-5V, I _D =-20A		65		S
Ciss	Input Capacitance			7000		pF
Coss	Output Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1.0MHz		820		pF
Crss	Reverse Transfer Capacitance			540		pF
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		2.2		Ω
td(on)	Turn-on Delay Time			14		nS
t _r	Turn-on Rise Time	V _{GS} =-10V, V _{DS} =-15V,		13		nS
td(off)	Turn-Off Delay Time	R _L =0.75Ω, R _{GEN} =3Ω		65		nS
t _f	Turn-Off Fall Time			37		nS
Q_g	Total Gate Charge			130		nC
Qgs	Gate-Source Charge	V _{GS} =-10V, V _{DS} =-15V, I _D =-20A		12		nC
Qgd	Gate-Drain Charge			31		nC
ISD	Source-Drain Current (Body Diode)				-150	Α
VSD	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =-20A			-1.3	V
trr	Reverse Recovery Time	I _F =-20A, di/dt=100A/μs		30		ns
Q _{rr}	Reverse Recovery Charge	I _F =-20A, di/dt=100A/μs		40		nC

Note:

- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width $\leqq 300 \text{us}$, duty cycle $\leqq 2\%$
- 3. The EAS data shows Max. rating . The test condition is TJ =25°C, VDD=-15V, VG=-10V, RG=25 Ω , L=0.5mH, IAS=-30A
- 4. The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I D and I DM, in real applications, should be limited by total power dissipation.



Typical Characteristics



100 Vds=-5V 80 60 -Id (A) 125℃ 40 25℃ 20 0 0 1 2 4 5 -Vgs(V)

Figure 1. Output Characteristics

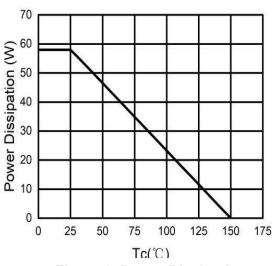


Figure 2. Transfer Characteristics

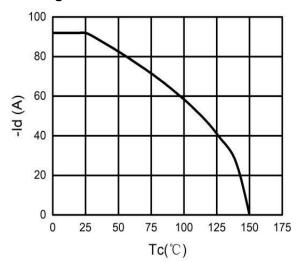


Figure 3. Power Dissipation

1.3

Vgs=0V

Id=250µA

1.1

0.9

0.8

-75 -50 -25 0 25 50 75 100 125 150 175

Tj(°C)

Figure 4. Drain Current

1.8

Vgs=-10V

Id=-20A

Parine 1.2

Vgs=-10V

Id=-20A

Tj(°C)

Figure 5. BV_{DSS} vs Junction Temperature

Figure 6. R_{DS(ON)} vs Junction Temperature





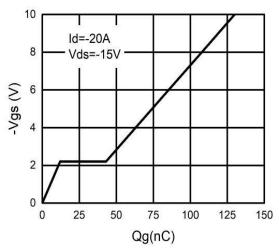
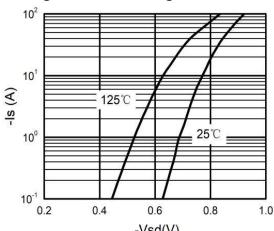


Figure 7. Gate Charge Waveforms



 $\begin{tabular}{ll} -Vsd(V)\\ \hline \textbf{Figure 9. Body-Diode Characteristics} \\ \end{tabular}$

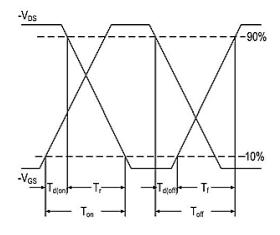
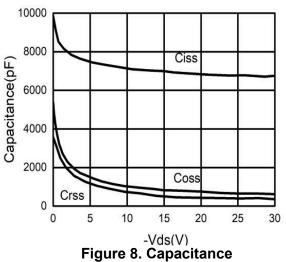


Figure.11 Switching Time Waveform



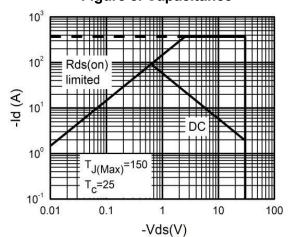


Figure 10. Maximum Safe Operating Area

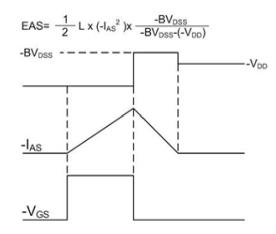
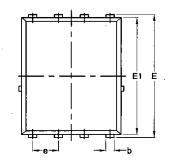


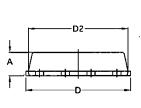
Figure.12 Unclamped Inductive Switching Waveform

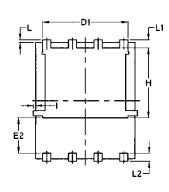


Package Mechanical Data-DFN5*6-8L-JQ Single









		Com	imon		
Symbol	mm		Inch		
	Mim	Max	Min	Max	
Α	1.03	1.17	0.0406	0.0461	
b	0.34	0.48	0.0134	0.0189	
С	0.824	0.0970	0.0324	0.082	
D	4.80	5.40	0.1890	0.2126	
D1	4.11	4.31	0.1618	0.1697	
D2	4.80	5.00	0.1890	0.1969	
E	5.95	6.15	0.2343	0.2421	
E1	5.65	5.85	0.2224	0.2303	
E2	1.60	/	0.0630	/	
е	1.27	BSC	0.05	BSC	
L	0.05	0.25	0.0020	0.0098	
L1	0.38	0.50	0.0150	0.0197	
L2	0.38	0.50	0.0150	0.0197	
Н	3.30	3.50	0.1299	0.1378	
1	/	0.18	/	0.0070	





Attention

- 1,Any and all APM Microelectronics products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your APM Microelectronics representative nearest you before using any APM Microelectronics products described or contained herein in such applications.
- 2,APM Microelectronics assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all APM Microelectronics products described or contained herein.
- 3, Specifications of any and all APM Microelectronics products described or contained here instipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- 4, APM Microelectronics Semiconductor CO., LTD. strives to supply high quality high reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. Whendesigning equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- 5,In the event that any or all APM Microelectronics products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- 6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of APM Microelectronics Semiconductor CO., LTD.
- 7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. APM Microelectronics believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- 8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "DeliverySpecification" for the APM Microelectronics product that you Intend to use.





AP150P03NF

-30V P-Channel Enhancement Mode MOSFET

Edition	Date	Change
Rve1.0	2021/3/10	Initial release

Copyright Attribution"APM-Microelectronice"