

**N-Ch MOSFET** 

#### **General Description**

**Features** 

Low RDS(on) & FOM

Extremely low switching loss

The WSD40120DN use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness and suitable to use in

Excellent stability and uniformity or Invertors

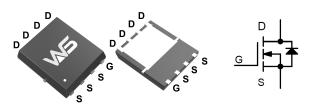
#### **Product Summery**

BVDSS	RDSON	ID
40V	2.5mΩ	110A

#### Applications

- Consumer electronic power supply
- Synchronous-rectification
- Synchronous-rectification applications

### **DFN5X6-8** Pin Configuration



### Absolute Maximum Ratings at Tj=25 °C unless otherwise noted

Symbol	Parameter	Rating	Units	
V <sub>DS</sub>	Drain-Source Voltage	40	V	
V <sub>GS</sub>	Gate-Source Voltage	±20	V	
I₀@T₀=25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	110	A	
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	71	A	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	240	A	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	145	mJ	
I <sub>AS</sub>	Avalanche Current	54	A	
P₀@T₀=25℃	Total Power Dissipation <sup>4</sup>	36	W	
T <sub>STG</sub>	Storage Temperature Range -55 to		°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>		55	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>		3.4	°C/W



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## Electrical Characteristics (T<sub>J</sub>=25<sup>-1</sup>C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	40			V
$\triangle BV_{DSS} / \triangle T_J$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25 $^\circ\!\mathrm{C}$ , I_D=1mA		0.043		V/℃
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		2.5	3.5	mΩ
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		3.5	4.5	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage		1.1	1.5	2.3	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> -V <sub>DS</sub> , I <sub>D</sub> -2500A		-6.94		mV/℃
la se	I <sub>DSS</sub> Drain-Source Leakage Current	$V_{\text{DS}}\text{=}32\text{V}$ , $V_{\text{GS}}\text{=}0\text{V}$ , $T_{\text{J}}\text{=}25^\circ\!\mathrm{C}$			1	
DSS		$V_{\text{DS}}\text{=}32\text{V}$ , $V_{\text{GS}}\text{=}0\text{V}$ , $T_{\text{J}}\text{=}55^\circ\!\text{C}$			5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm20V$ , $V_{DS}$ = $0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A		75		S
R <sub>g</sub>	Gate Resistance	$V_{DS}$ =0V , $V_{GS}$ =0V , f=1MHz		1.5		Ω
Qg	Total Gate Charge (10V)	V <sub>DS</sub> =20V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A		23		
Q <sub>gs</sub>	Gate-Source Charge			7.5		nC
Q <sub>gd</sub>	Gate-Drain Charge			5.5		
T <sub>d(on)</sub>	Turn-On Delay Time			10		
Tr	Rise Time	V <sub>DD</sub> =20V , V <sub>GEN</sub> =10V , R <sub>G</sub> =3.0		5		
T <sub>d(off)</sub>	Turn-Off Delay Time	Ω, I <sub>D</sub> =20A .		33		- ns -
T <sub>f</sub>	Fall Time			6.5		
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =20V , V <sub>GS</sub> =0V , f=1MHz		2810		
C <sub>oss</sub>	Output Capacitance			850		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			85		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	$V_G = V_D = 0V$ , Force Current			30	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1.2	V

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

2 .The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%

3. The EAS data shows Max. rating . The test condition is VDD=20V,VGS=10V,L=0.5mH,IAS=54A 4. The power dissipation is limited by 150°C junction temperature

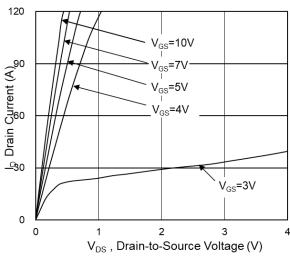
5 .The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.



# WSD40110DN56G

#### N-Ch MOSFET

## **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

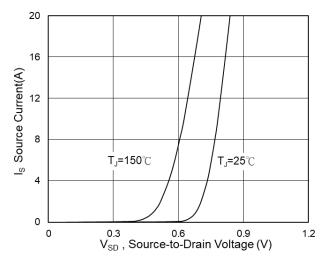


Fig.3 Source Drain Forward Characteristics

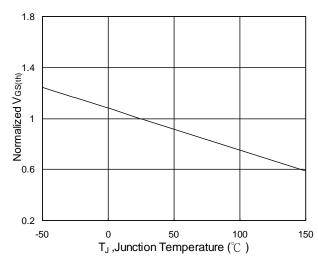


Fig.5 Normalized V<sub>GS(th)</sub> vs T<sub>J</sub>

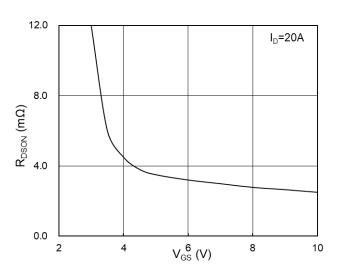


Fig.2 On-Resistance vs G-S Voltage

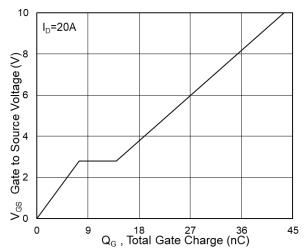


Fig.4 Gate-Charge Characteristics

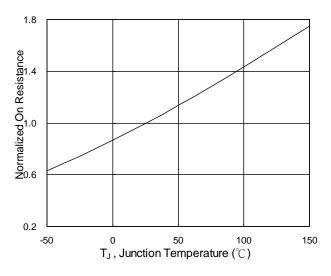


Fig.6 Normalized RDSON vs TJ

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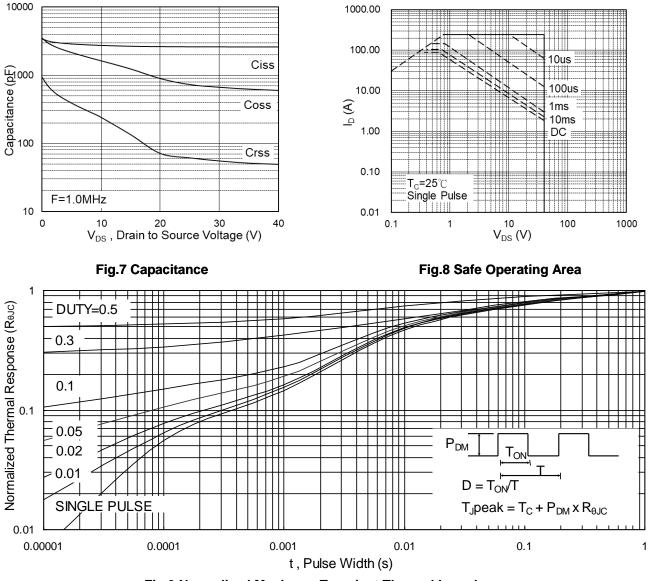


Fig.9 Normalized Maximum Transient Thermal Impedance

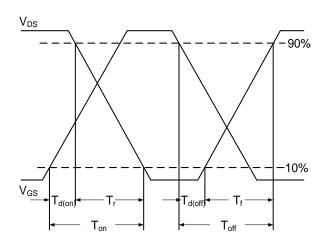


Fig.10 Switching Time Waveform

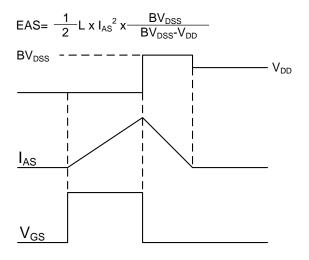


Fig.11 Unclamped Inductive Switching Wave



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