

MSKSEMI

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



PLED

Product data sheet



SOD-123FL

FEATURES

- * Ideal for surface mount applications
- * Easy pick and place
- * Built-in strain relief
- * High surge current capability

MECHANICAL DATA

- * Case: Molded plastic
- * Epoxy: UL 94V-0 rate flame retardant
- * Terminals: Solder plated, solderable per MIL-STD-202F, method 208 guaranteed
- * Polarity: Color band denotes cathode end
- * Mounting position: Any

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25°C ambient temperature unless otherwise specified.
Single phase half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

	P/N	DSR1A S1A	DSR1B S1B	DSR1D S1D	DSR1G S1G	DSR1J S1J	DSR1K S1K	DSR1M S1M	UNITS
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current at T _L = 100°C (NOTE 1)	I _(AV)	1.0							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	25.0							A
Maximum instantaneous forward voltage at 1.0A	V _F	1.1							V
Maximum DC reverse current at rated DC blocking voltage T _A = 25°C T _A = 125°C	I _R	10.0 50.0							μA
Typical junction capacitance (NOTE 2)	C _J	4							pF
Typical thermal resistance (NOTE 3)	R _{θJA}	95							°C/W
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +150							C

NOTES:

1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
2. Thermal Resistance from Junction to Ambient.

FIG.1-TYPICAL FORWARD CHARACTERISTICS

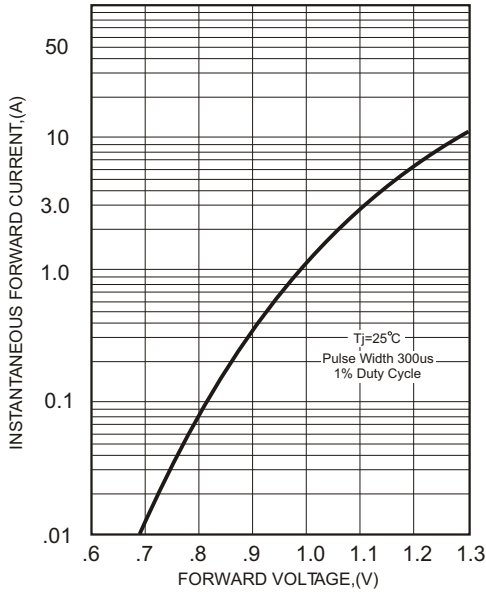


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

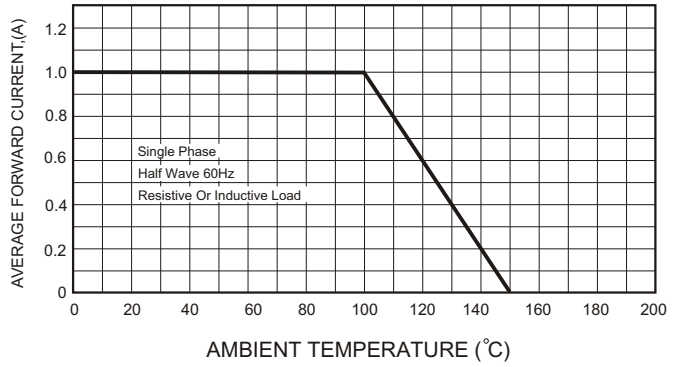


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

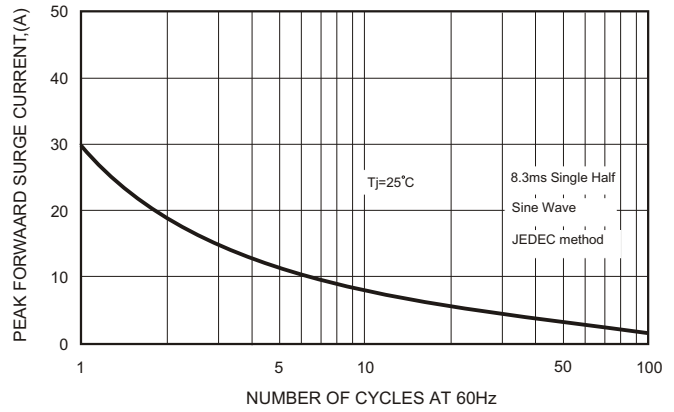


FIG.3 - TYPICAL REVERSE CHARACTERISTICS

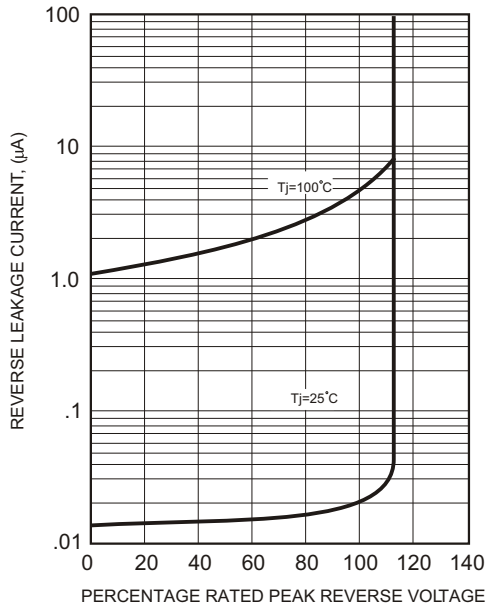
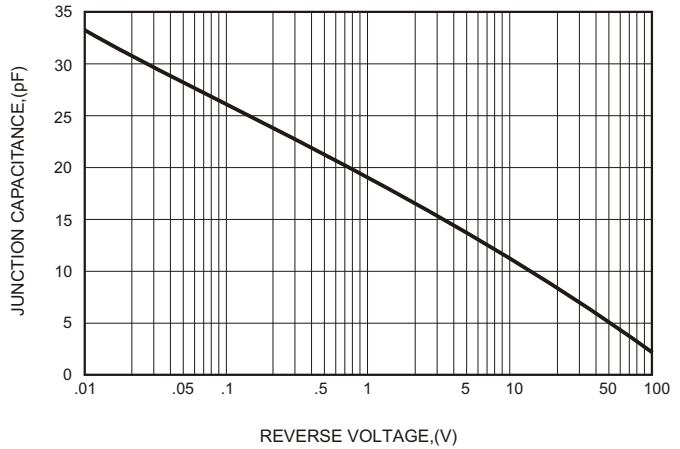
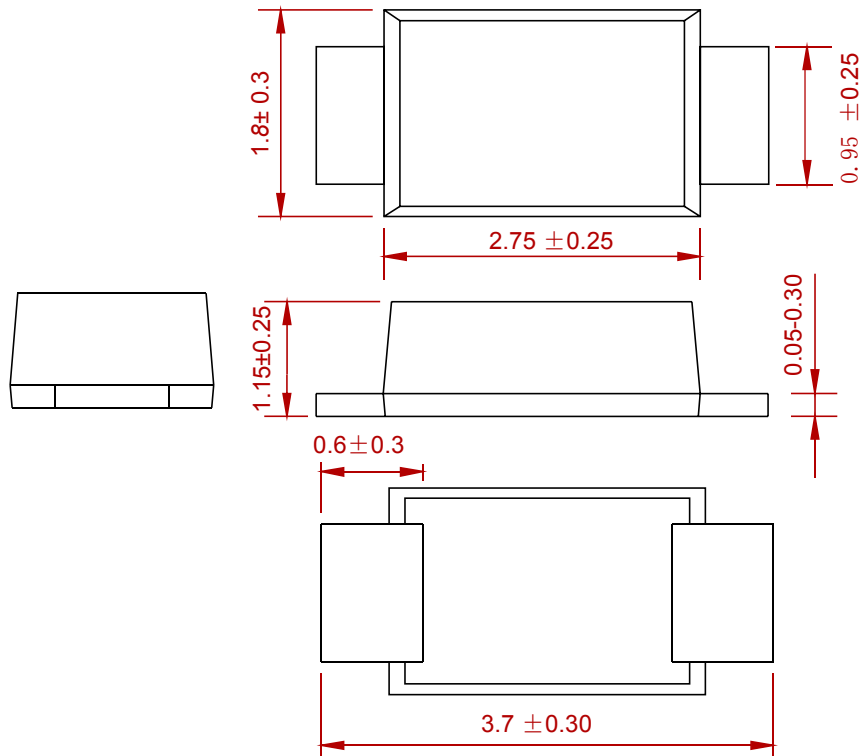


FIG.5-TYPICAL JUNCTION CAPACITANCE

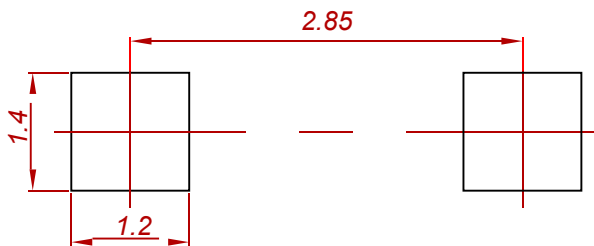


PACKAGE MECHANICAL DATA



Dimensions in millimeters

Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.05 mm.
3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
DSR1A TRHU DSR1M	SOD-123FL	3000

Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringement of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.