

Description

The HXY3416MI uses advanced trench technology

to provide excellent $R_{\text{DS}(\text{ON})},$ low gate charge and

operation with gate voltages as low as 2.5V. This

device is suitable for use as a Battery protection

or in other Switching application.

General Features

V_{DS} = 20V I_D =6.5A

 $R_{DS(ON)} < 27m\Omega @ V_{GS}=4.5V$

ESD=2500HBM

Application

Battery protection

Load switch

Uninterruptible power supply

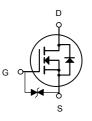
Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY3416MI	SOT23-3L	AGBV 1N	3000

Absolute Maximum Ratings (T_A=25[°]C unless otherwise noted)

Symbol	Parameter	Limit	Unit	
Vds	Drain-Source Voltage	20	V	
V _{GS}	Gate-Source Voltage	±12	V	
ID	Drain Current-Continuous	6.5	А	
Ідм	Drain Current-Pulsed (Note 1)	30	А	
PD	Maximum Power Dissipation	1.4	W	
Тј,Тѕтс	Operating Junction and Storage Temperature Range	-55 To 150	°C	
Reja	Thermal Resistance, Junction-to-Ambient (Note 2)	89	°C/W	





N-Channel MOSFET

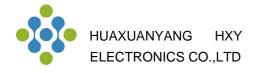


Electrical Characteristics (TA=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V I _D =250µA	20		-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =20V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	lgss	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	±10	μA
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =250µA	0.45	0.7	1.0	V
		V _{GS} =4.5V, I _D =6.5A	-	17	27	mΩ
Drain-Source On-State Resistance	Rds(on)	V _{GS} =2.5V, I _D =5.5A	-	21	33	mΩ
		V _{GS} =1.8V, I _D =5A	-	28	40	mΩ
Forward Transconductance	gfs	V _{DS} =5V,I _D =6.5A	8	-	-	S
Input Capacitance	Clss		-	660	-	PF
Output Capacitance	Coss	V _{DS} =10V,V _{GS} =0V,	-	160	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	87	-	PF
Turn-on Delay Time	td(on)		-	0.5		nS
Turn-on Rise Time	tr	V _{DD} =10V,RL=1.5Ω	-	1		nS
Turn-Off Delay Time	td(off)	V_{GS} =5V, R_{GEN} =3 Ω	-	12		nS
Turn-Off Fall Time	t _f		-	4		nS
Total Gate Charge	Qg		-	8		nC
Gate-Source Charge	Q _{gs}	V _{DS} =10V,I _D =6.5A,	-	2.5	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =4.5V	-	3	-	nC
Diode Forward Voltage (Note 3)	Vsd	V _{GS} =0V,I _S =6.5A	-	-	1.2	V
Diode Forward Current (Note 2)	ls		-	-	6.5	A

Notes:

Repetitive Rating: Pulse width limited by maximum junction temperature. Surface Mounted on FR4 Board, t \leq 10 sec. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%. Guaranteed by design, not subject to production



Typical Characteristics

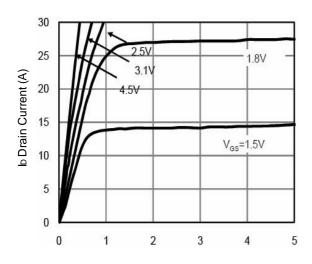


Fig.1 Typical Output Characteristics

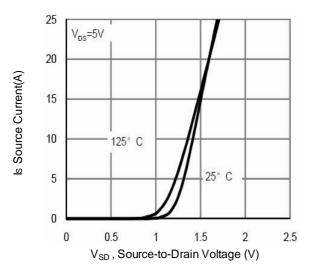


Fig.3 Forward Characteristics of Reverse

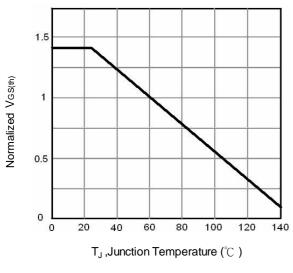


Fig.5 Normalized V_{GS(th)} vs. T_J

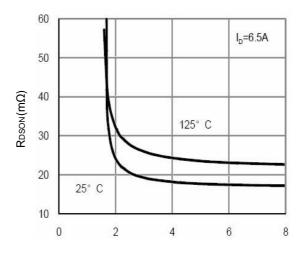


Fig.2 On-Resistance vs. Gate-Source

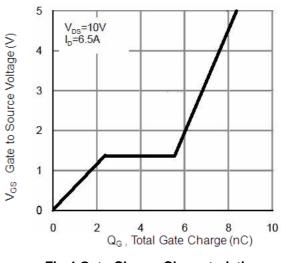


Fig.4 Gate-Charge Characteristics

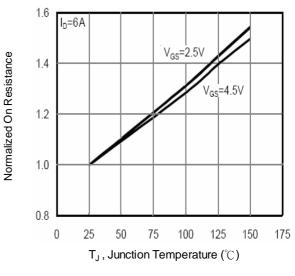
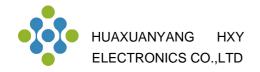


Fig.6 Normalized R_{DSON} vs. T_{J}



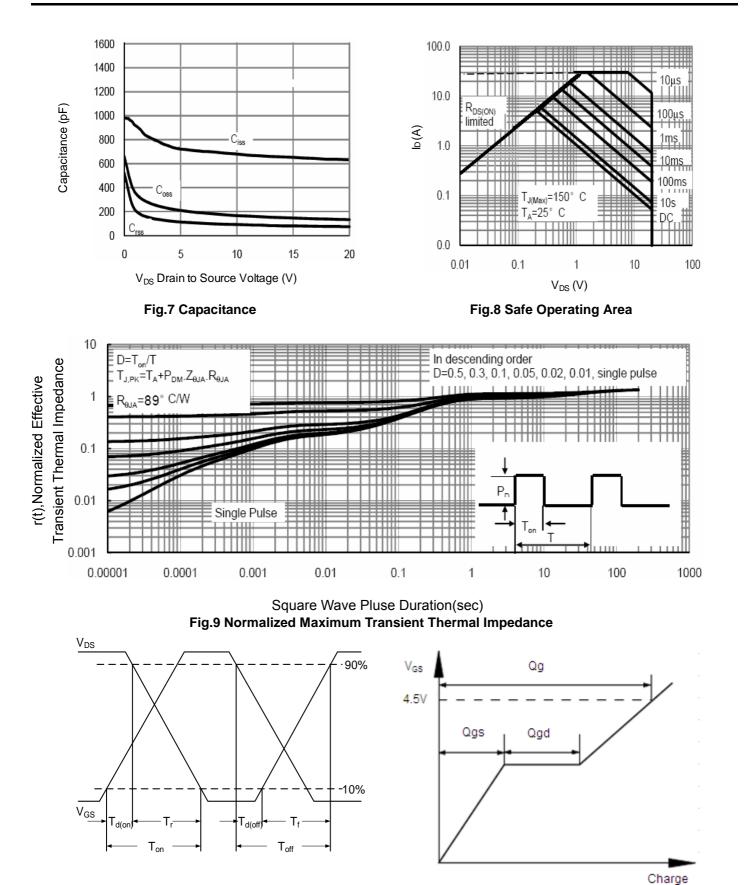
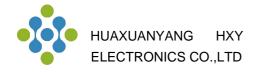
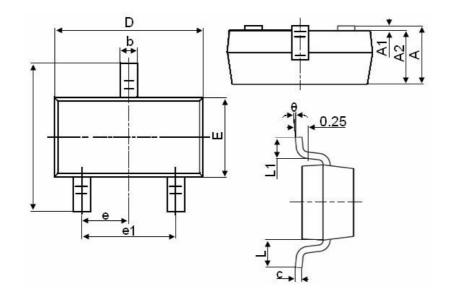


Fig.10 Switching Time Waveform

Fig.11 Gate Charge Waveform



SOT23-3L Package Information



Symbol	Dimensions in Millimeters		
	MIN.	MAX.	
A	1.050	1.250	
A1	0.000	0.100	
A2	1.050	1.150	
b	0.300	0.500	
С	0.100	0.200	
D	2.800	3.000	
E	1.500	1.700	
E1	2.650	2.950	
е	0.950TYP		
e1	1.800	2.000	
L	0.550REF		
L1	0.300	0.600	
θ	0°	8°	



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