

### Description

The HXY20P06DF uses advanced trench technology

to provide excellent  $R_{\text{DS}(\text{ON})},$  low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = -60V I<sub>D</sub> =-20A

 $R_{DS(ON)} < 85 \text{ m}\Omega @ V_{GS}=4.5V$ 

### Application

Battery protection

Load switch

Uninterruptible power supply

### Package Marking and Ordering Information

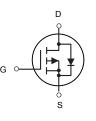
Product ID	Pack	Marking	Qty(PCS)
HXY20P06DF	DFN3X3-8L	20P06 XXX YYYY	5000

### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-60	V
VGS	Gate-Source Voltage	±20	V
I₀@Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-20	А
I₀@Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-12	А
IDM	Pulsed Drain Current <sup>2</sup>	-30	А
P₀@Tc=25°C	Total Power Dissipation <sup>4</sup>	25	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-ambient <sup>1</sup>	62	°C/W
R₀JC	Thermal Resistance Junction-Case <sup>1</sup>	5	°C/W







P-Channel MOSFET



# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-60			V
$\triangle BV_{\text{DSS}} / \triangle T_J$	BV <sub>DSS</sub> Temperature Coefficient	Reference to $25^{\circ}C$ , I <sub>D</sub> =-1mA		-0.023		V/°C
р	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V , I <sub>D</sub> =-10A		70	85	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-6A		83	90	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage		-1.2		-2.5	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_{D}=-250$ uA		4		mV/°C
		$V_{DS}$ =-24V , $V_{GS}$ =0V , $T_J$ =25°C			-1	
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			-5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-15A		12		S
Qg	Total Gate Charge (-4.5V)			6.1		
$Q_gs$	Gate-Source Charge	$V_{\text{DS}}\text{=-}15\text{V}$ , $V_{\text{GS}}\text{=-}4.5\text{V}$ , $I_{\text{D}}\text{=-}15\text{A}$		3.1		nC
$Q_{gd}$	Gate-Drain Charge			1.8		
T <sub>d(on)</sub>	Turn-On Delay Time			2.6		
Tr	Rise Time	$V_{\text{DD}}\text{=-}15\text{V}$ , $V_{\text{GS}}\text{=-}10\text{V}$ , $\text{R}_{\text{G}}\text{=}3.3\Omega,$		8.6		20
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =-15A		33.6		ns
T <sub>f</sub>	Fall Time			6		
Ciss	Input Capacitance			585		
Coss	Output Capacitance	$V_{DS}$ =-15V , $V_{GS}$ =0V , f=1MHz		100		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			85		

# **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	V V OV Force Current			-20	А
I <sub>SM</sub>	Pulsed Source Current <sup>2,5</sup>	$V_G = V_D = 0V$ , Force Current			-30	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	IF=-15A,dI/dt=100A/µs,		6.1		nS
Q <sub>rr</sub>	Reverse Recovery Charge	TJ=25°C		1.4		nC

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width  $\,\leq\,$  300us , duty cycle  $\,\leq\,$  2%

3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =-25V,  $V_{GS}$ =-10V, L=0.1mH, I<sub>AS</sub>=-19A

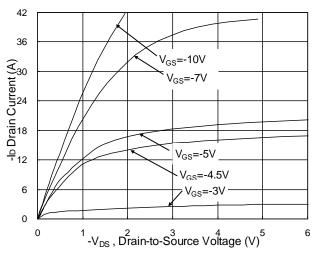
4. The power dissipation is limited by 150°C junction temperature

5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



# HXY20P06DF P-Channel Enhancement Mode MOSFET

# **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

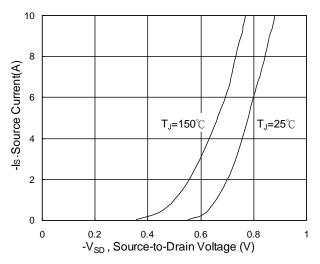
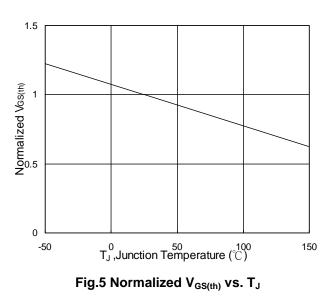


Fig.3 Forward Characteristics Of Reverse



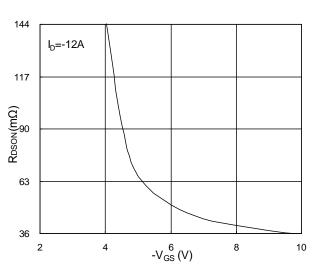


Fig.2 On-Resistance v.s Gate-Source

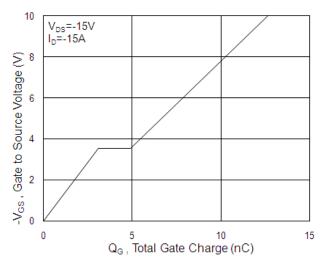
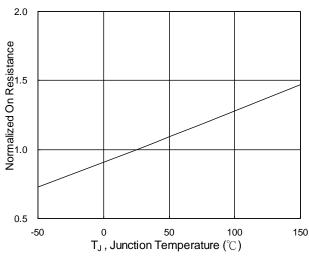
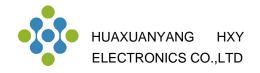
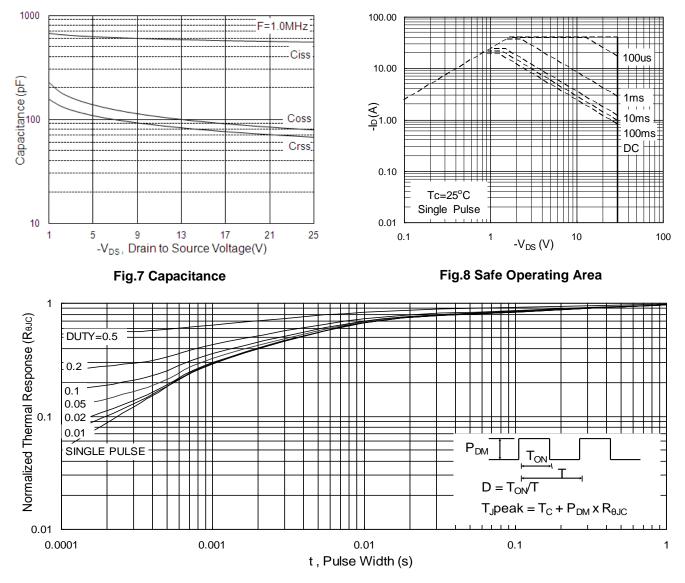


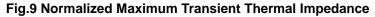
Fig.4 Gate Charge Characteristics











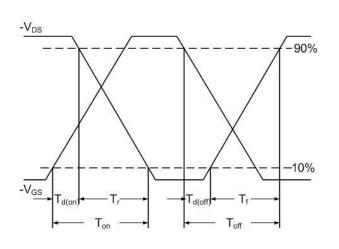


Fig.10 Switching Time Waveform

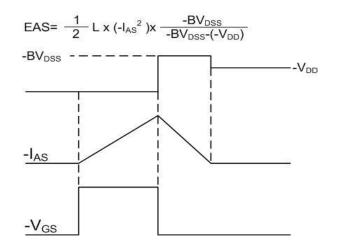
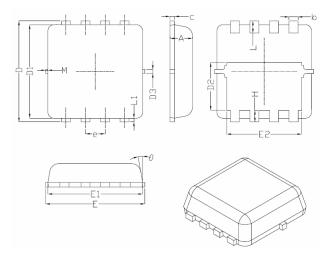


Fig.11 Unclamped Inductive Switching Waveform



# **DFN3X3-8L Package Information**



Symptical	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
с	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
e		0.65BSC		
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
М	*	*	0.15	
θ		10 <sup>°</sup>	12 <sup>°</sup>	



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