## Description

The HXY20P06DF uses advanced trench technology
to provide excellent $\mathrm{R}_{\mathrm{DS}(O \mathrm{O})}$, low gate charge and operation with gate voltages as low as 4.5 V . This
device is suitable for use as a


DFN3X3-8L


P-Channel MOSFET

Load switch
Uninterruptible power supply

## Package Marking and Ordering Information

| Product ID | Pack | Marking | Qty(PCS) |
| :--- | :--- | :--- | :--- |
| HXY20P06DF | DFN3X3-8L | 20 P 06 XXX YYYY | 5000 |

Absolute Maximum Ratings ( $\mathrm{T}_{\mathrm{c}}=\mathbf{2 5} 5^{\circ} \mathrm{C}$ unless otherwise noted)

| Symbol | Parameter | Rating | Units |
| :---: | :---: | :---: | :---: |
| VDS | Drain-Source Voltage | -60 | V |
| VGS | Gate-Source Voltage | $\pm 20$ | V |
| $\mathrm{lo} @ \mathrm{~T}_{\mathrm{c}}=25^{\circ} \mathrm{C}$ | Continuous Drain Current, VGs @ 10V1 | -20 | A |
| $\mathrm{ld} @ \mathrm{Tc}=100^{\circ} \mathrm{C}$ | Continuous Drain Current, VGs @ 10V ${ }^{1}$ | -12 | A |
| IDM | Pulsed Drain Current ${ }^{2}$ | -30 | A |
| PD @ $\mathrm{T}_{\mathrm{c}}=25^{\circ} \mathrm{C}$ | Total Power Dissipation ${ }^{4}$ | 25 | W |
| TSTG | Storage Temperature Range | -55 to 150 | ${ }^{\circ} \mathrm{C}$ |
| TJ | Operating Junction Temperature Range | -55 to 150 | ${ }^{\circ} \mathrm{C}$ |
| R $\mathrm{\theta} \mathrm{~A}$ | Thermal Resistance Junction-ambient ${ }^{1}$ | 62 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{R}_{\theta} \mathrm{JC}$ | Thermal Resistance Junction-Case ${ }^{1}$ | 5 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

## Electrical Characteristics ( $\mathrm{T}_{\mathrm{J}}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $B V_{\text {DSS }}$ | Drain-Source Breakdown Voltage | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-250 \mathrm{uA}$ | -60 | --- | --- | V |
| $\triangle B V_{\text {DSS }} / \triangle T_{J}$ | BV ${ }_{\text {DSS }}$ Temperature Coefficient | Reference to $25^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{D}}=-1 \mathrm{~mA}$ | --- | -0.023 | --- | V/ ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{R}_{\mathrm{DS} \text { (ON) }}$ | Static Drain-Source On-Resistance ${ }^{2}$ | $\mathrm{V}_{\mathrm{GS}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-10 \mathrm{~A}$ | --- | 70 | 85 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\mathrm{G} S}=-4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-6 \mathrm{~A}$ | --- | 83 | 90 |  |
| $\mathrm{V}_{\mathrm{GS} \text { (th) }}$ | Gate Threshold Voltage | $\mathrm{V}_{\mathrm{GS}}=\mathrm{V}_{\text {DS }}, \mathrm{I}_{\mathrm{D}}=-250 \mathrm{uA}$ | -1.2 | --- | -2.5 | V |
| $\triangle \mathrm{V}_{\mathrm{GS}(\mathrm{th})}$ | $\mathrm{V}_{\mathrm{GS}(\text { th })}$ Temperature Coefficient |  | --- | 4 | --- | $\mathrm{mV} /{ }^{\circ} \mathrm{C}$ |
| Idss | Drain-Source Leakage Current | $\mathrm{V}_{\mathrm{DS}}=-24 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | --- | --- | -1 | uA |
|  |  | $\mathrm{V}_{\mathrm{DS}}=-24 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=55^{\circ} \mathrm{C}$ | --- | --- | -5 |  |
| $\mathrm{I}_{\text {GSS }}$ | Gate-Source Leakage Current | $\mathrm{V}_{\mathrm{GS}}= \pm 20 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ | --- | --- | $\pm 100$ | nA |
| gfs | Forward Transconductance | $V_{D S}=-5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-15 \mathrm{~A}$ | --- | 12 | --- | S |
| $\mathrm{Q}_{\mathrm{g}}$ | Total Gate Charge (-4.5V) | $\mathrm{V}_{\mathrm{DS}}=-15 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=-4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-15 \mathrm{~A}$ | --- | 6.1 | --- | nC |
| $\mathrm{Q}_{\mathrm{gs}}$ | Gate-Source Charge |  | --- | 3.1 | --- |  |
| $\mathrm{Q}_{\mathrm{gd}}$ | Gate-Drain Charge |  | --- | 1.8 | --- |  |
| $\mathrm{T}_{\mathrm{d}(\text { on) }}$ | Turn-On Delay Time | $\begin{aligned} & V_{D D}=-15 \mathrm{~V}, V_{G S}=-10 \mathrm{~V}, R_{G}=3.3 \Omega, \\ & \mathrm{I}_{\mathrm{D}}=-15 \mathrm{~A} \end{aligned}$ | --- | 2.6 | --- | ns |
| $\mathrm{T}_{\mathrm{r}}$ | Rise Time |  | --- | 8.6 | --- |  |
| $\mathrm{T}_{\mathrm{d} \text { (off) }}$ | Turn-Off Delay Time |  | --- | 33.6 | --- |  |
| $\mathrm{T}_{\mathrm{f}}$ | Fall Time |  | --- | 6 | --- |  |
| $\mathrm{C}_{\text {iss }}$ | Input Capacitance | $\mathrm{V}_{\mathrm{DS}}=-15 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | --- | 585 | --- | pF |
| $\mathrm{C}_{\text {oss }}$ | Output Capacitance |  | --- | 100 | --- |  |
| Crss | Reverse Transfer Capacitance |  | --- | 85 | --- |  |

## Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Is | Continuous Source Current ${ }^{1,5}$ | $\mathrm{V}_{\mathrm{G}}=\mathrm{V}_{\mathrm{D}}=0 \mathrm{~V}$, Force Current | --- | --- | -20 | A |
| ISM | Pulsed Source Current ${ }^{2,5}$ |  | --- | --- | -30 | A |
| $V_{\text {SD }}$ | Diode Forward Voltage ${ }^{2}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{S}}=-1 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | --- | --- | -1.2 | V |
| $\mathrm{trr}_{\text {r }}$ | Reverse Recovery Time | $\begin{aligned} & \mathrm{IF}=-15 \mathrm{~A}, \mathrm{~d} / / \mathrm{dt}=100 \mathrm{~A} / \mu \mathrm{s}, \\ & \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \end{aligned}$ | --- | 6.1 | --- | nS |
| $Q_{\text {rr }}$ | Reverse Recovery Charge |  | --- | 1.4 | --- | nC |

[^0]
## Typical Characteristics



Fig. 1 Typical Output Characteristics


Fig. 3 Forward Characteristics Of Reverse


Fig. 5 Normalized $\mathrm{V}_{\mathrm{GS}(\mathrm{th})}$ vs. $\mathrm{T}_{\mathrm{J}}$


Fig. 2 On-Resistance v.s Gate-Source


Fig. 4 Gate Charge Characteristics


Fig. 6 Normalized R $_{\text {Dson }}$ vs. $T_{J}$


Fig. 7 Capacitance


Fig. 8 Safe Operating Area


Fig. 9 Normalized Maximum Transient Thermal Impedance


Fig. 10 Switching Time Waveform
$E A S=\frac{1}{2} L \times\left(-I_{A S}{ }^{2}\right) \times \frac{-B V_{D S S}}{-B V_{D S S}-\left(-V_{D D}\right)}$


Fig. 11 Unclamped Inductive Switching Waveform

## DFN3X3-8L Package Information



| Symbol | Dimensions In Millimeters |  |  |
| :---: | :---: | :---: | :---: |
|  | Min. | Nom. | Max. |
| A | 0.70 | 0.75 | 0.80 |
| b | 0.25 | 0.30 | 0.35 |
| c | 0.10 | 0.15 | 0.25 |
| D | 3.25 | 3.35 | 3.45 |
| D1 | 3.00 | 3.10 | 3.20 |
| D2 | 1.48 | 1.58 | 1.68 |
| D3 | - | 0.13 | - |
| E | 3.20 | 3.30 | 3.40 |
| E1 | 3.00 | 3.15 | 3.20 |
| E2 | 2.39 | 2.49 | 2.59 |
| H |  | $0.65 B S C$ |  |
| L | 0.30 | 0.39 | 0.50 |
| L1 | 0.30 | 0.40 | 0.50 |
| M | - | 0.13 | - |
| ( |  | $*$ | 0.15 |

HUAXUANYANG HXY


#### Abstract

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[^0]:    Note :
    1.The data tested by surface mounted on a 1 inch $^{2}$ FR-4 board with $2 O Z$ copper.
    2.The data tested by pulsed, pulse width $\leqq 300$ us , duty cycle $\leqq 2 \%$
    3.The EAS data shows Max. rating . The test condition is $\mathrm{V}_{\mathrm{DD}}=-25 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=-10 \mathrm{~V}, \mathrm{~L}=0.1 \mathrm{mH}, \mathrm{I}_{\mathrm{AS}}=-19 \mathrm{~A}$
    4. The power dissipation is limited by $150^{\circ} \mathrm{C}$ junction temperature
    5. The data is theoretically the same as $I_{D}$ and $I_{D M}$, in real applications, should be limited by total power dissipation.

