



RABS22 THRU RABS210

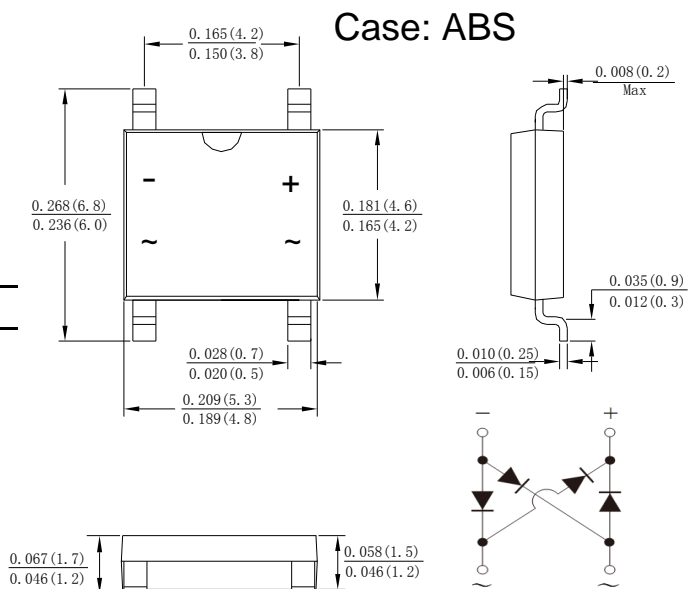
Single Phase 2.0AMP Fast Glass Passivated Bridge Rectifier

Features

- Glass passivated die construction
- Low forward voltage drop
- High current capability
- High surge current capability
- Designed for surface mount application
- Plastic material-UL flammability 94V-0

Mechanical Data

- Case: SOPA-4, molded plastic ABS
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- Polarity: as marked on case
- Mounting position: Any
- Marking: type number



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	RABS22	RABS24	RABS26	RABS28	RABS210	UNITS
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM}	200	400	600	800	1000	V
	V _{RWM}						
	V _{DC}						
RMS Reverse Voltage	V _{RMS}	140	280	420	560	700	V
Average Rectified Output Current (Note:1)@T _c =100 °C	IF(AV)	2.0					A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	60					A
I ² t Rating for Fusing (t < 8.3ms)	I ² t	14.94					A ² s
Forward Voltage per element @IF=2.0A	V _{FM}	1.3					V
Maximum Forward Power Dissipation	P _D	5.2					W
Maximum Reverse Recovery Time (Note2)	T _{rr}	150		250	500		ns
Peak Reverse Current @T _J =25°C At Rated DC Blocking Voltage @T _J =125°C	I _R	5.0 100					uA
Typical Junction Capacitance (Note3)	C _J	23					pF
Typical Thermal Resistance	R _{θJA}	95					°C/W
	R _{θJL}	25					
	R _{θJC}	20					
Operating and Storage Temperature Range	T _J ,T _{STG}	-55to+150					°C

Note:1.Mounted on glass epoxy PC board with 1.3mm² solder pad.

2.Reverse Recovery Test Conditions: $I_F = 0.5\text{A}$, $I_R = 1.0\text{A}$, $IRR = 0.25\text{A}$.

3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C. For reference only



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FIG.1 FORWARD CURRENT DERATING CURVE

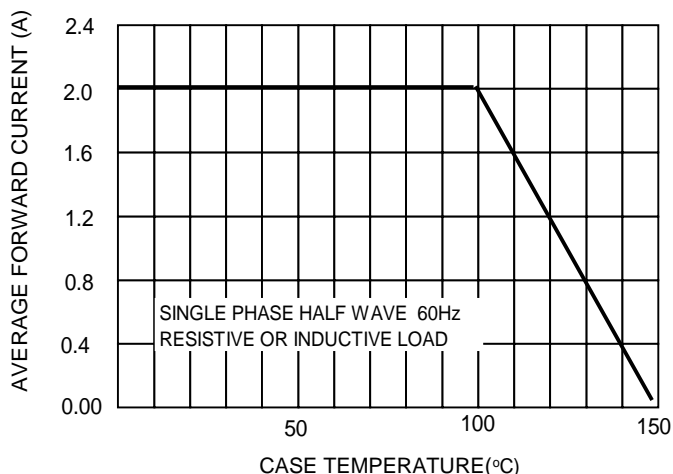


FIG.2 TYPICAL FORWARD CHARACTERISTICS

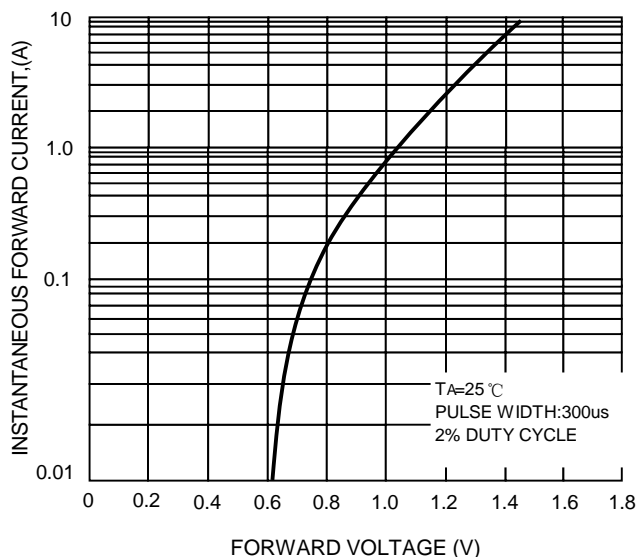


Fig. 3 Maximum Peak Forward Surge Current

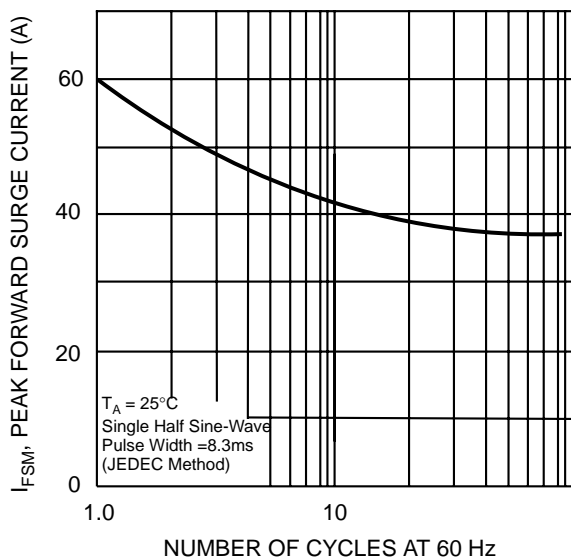


Fig. 4 Typical Reverse Characteristics

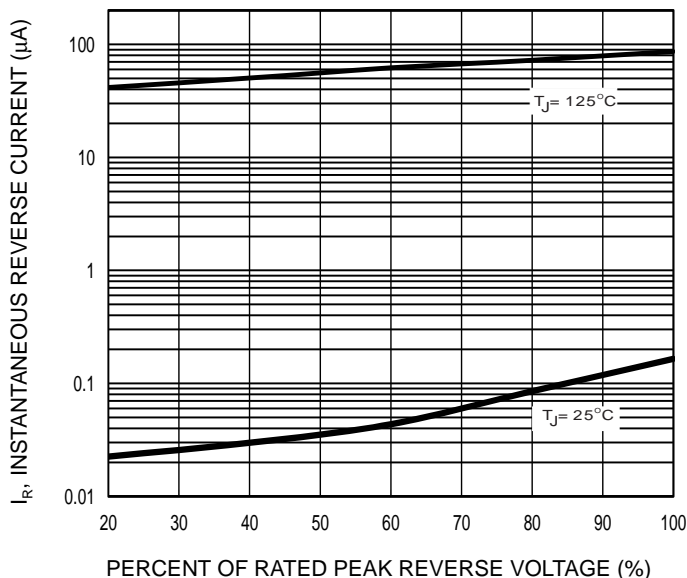
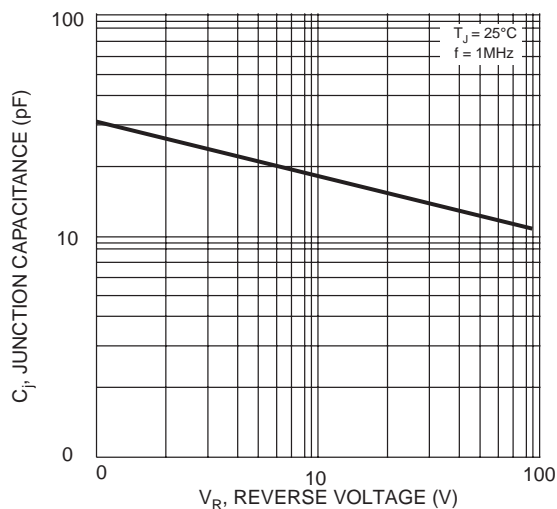
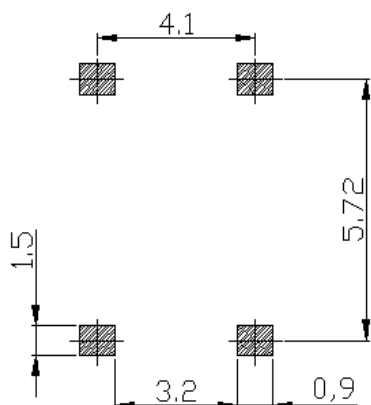


Fig. 5 Typical Junction Capacitance



ABS PAD LAYOUT





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