

Features And Application

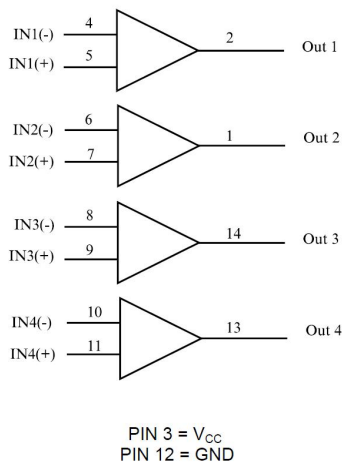
- Single or Split Supply Operation
- Low Input Bias Current
- Low Input Offset Current
- Input Common Mode Voltage Range to Gnd
- Low Output Saturation Voltage
- TTL and CMOS Compatible

Description

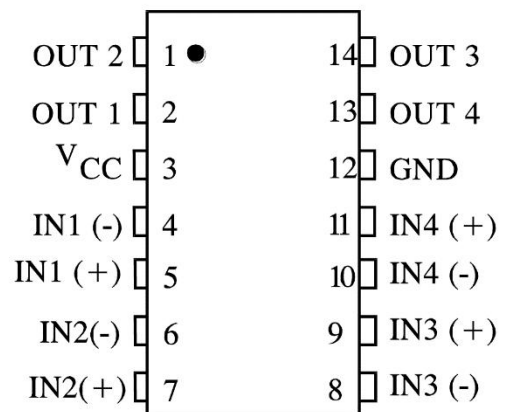
The CBM2901 consists of four independent precision voltage comparators with an offset voltage specification as low as 2.0 mV max for four comparators which were designed specifically to operate from a single power supply over a wide range of voltages.

Application areas include limit comparators, simple analog to digital converters; pulse, squarewave and time delay generators; wide range VCO; MOS clock timers; multivibrators and high voltage digital logic gates.

LOGIC DIAGRAM



PIN ASSIGNMENT



Maximum Ratings

Symbol	Parameter	Value	Unit
V_{CC}	Power Supply Voltages		
	Single Supply Split Supplies	36 ± 18	V
V_{IDR}	Input Differential Voltage Range	36	V
V_{ICR}	Input Common Mode Voltage Range (1)	-0.3 to V_{CC}	V
I_{SC}	Output Short Circuit to Ground	Continuous	
I_{IN}	Input Current, per pin (2)	50	mA
T_J	Junction Temperature		
	Plastic Packages	150	$^{\circ}C$
T_{stg}	Storage Temperature	-65 to +150	$^{\circ}C$
T_L	Lead Temperature, 1mm from Case for 10 Seconds	260	$^{\circ}C$
P_D	Power Dissipation @ $T_A=25^{\circ}C$		
	Plastic Package	1.0	W
	Derate above $25^{\circ}C$	8.0	mW/ $^{\circ}C$

* Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied.

Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Functional operation should be restricted to the Recommended Operating Conditions.

Notes:

1. Split Power Supplies.
2. $V_{IN} < -0.3V$. This input current will only exist when voltage at any of the input leads is driven negative.

Recommended Operating Conditions($T_a = -40 \sim 85^{\circ}C$)

Symbol	Parameter	Min	Max	Unit
V_{CC}	DC Supply Voltage	± 2.5 or 5.0	± 15 or 30	V
T_A	Operating Temperature, All Package Types	-40	+85	$^{\circ}C$

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V_{IN} and V_{OUT} should be constrained to the range $GND \leq (V_{IN} \text{ or } V_{OUT}) \leq V_{CC}$.

Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or V_{CC}). Unused outputs must be left open.

DC ELECTRICAL CHARACTERISTICS ($T_A = -40$ to $+85^\circ\text{C}$)

Symbol	Parameter	Test Conditions	Guaranteed Limit			Unit
			Min	Typ	Max	
V_{IO}	Input Offset Voltage	$V_0 = 1.4\text{V}$ $V_{CC} = 5.0\text{--}30\text{V}; R_S \leq 100\Omega$ $V_{ICR} = 0\text{V} - (V_{CC} - 1.5)\text{V}$	-		9.0 5.0*	mV
I_{IB}	Input Bias Current	$V_0 = 1.4\text{V}$ $V_{CC} = 5.0\text{--}30\text{V}$ $V_{ICR} = 0\text{V} - (V_{CC} - 1.5)\text{V}$	-		400	nA
I_{IO}	Input Offset Current	$V_0 = 1.4\text{V}$ $V_{CC} = 5.0\text{--}30\text{V}$ $V_{ICR} = 0\text{V} - (V_{CC} - 1.5)\text{V}$	-		± 150	nA
V_{ICR}	Input Common Mode Voltage Range	$V_{CC} = 5.0\text{--}30\text{V}$	0		$V_{CC} - 2.0\text{V}$	V
I_{CC}	Supply Current	$R_L = \infty, V_{CC} = 5.0$ $R_L = \infty, V_{CC} = 30\text{V}$	-		2.0* 2.5*	mA
A_{VOL}	Voltage Gain	$V_{CC} = 15\text{V}, R_L = 15\text{K}\Omega$	-	200*	-	V/mV
t_1	Large Signal Response Time	$V_{IN} = \text{TTL Logic Swing}$, $V_{ref} = 1.4\text{V}, V_{CC} = 5.0\text{V}$, $R_L = 5.1\Omega, V_{RL} = 5.0\text{V}$	-	300*	-	ns
t_2	Response Time	$V_{CC} = 5.0\text{V}, R_L = 5.1\text{K}\Omega, V_{RL} = 5.0\text{V}$	-	1.3*	-	μs
I_{sink}	Output Sink Current	$V_I(-) = 1.0\text{V}, V_I(+) = 0\text{V}$, $V_0 \leq 1.5\text{V}, V_{CC} = 5.0\text{V}$	6.0*	-	-	mA
V_{sat}	Saturation Voltage	$V_I(-) = 1.0\text{V}, V_I(+) = 0\text{V}$, $I_{sink} \leq 4.0\text{mA}, V_{CC} = 5.0\text{V}$	-	-	700	mV
I_{OL}	Output Leakage Current	$V_I(+) = 1.0\text{V}, V_I(-) = 0\text{V}$, $V_0 = 5.0\text{V}$ $V_0 = 30\text{V}$		0.1*	1000	nA
V_{IDR}	Differential Input Voltage Range	All $V_{IN} \geq \text{GND}$ or V-Supply (if used)			V_{CC}^*	V

* = @25°C

TYPICAL PERFORMANCE CHARACTERISTICS

($V_{CC}=1.5V$, $T_A=+25^{\circ}C$, (each comporator))

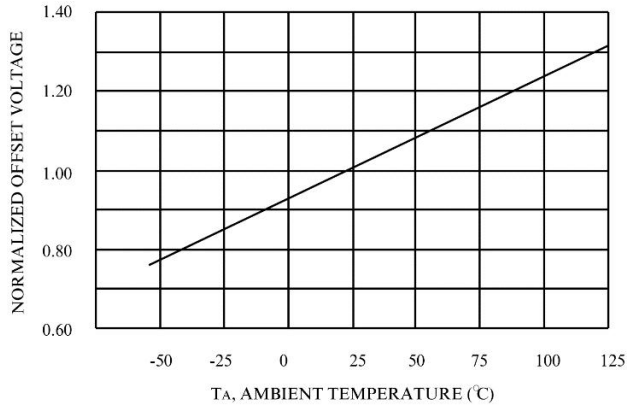


Figure 1. Normalized Input Offset Voltage

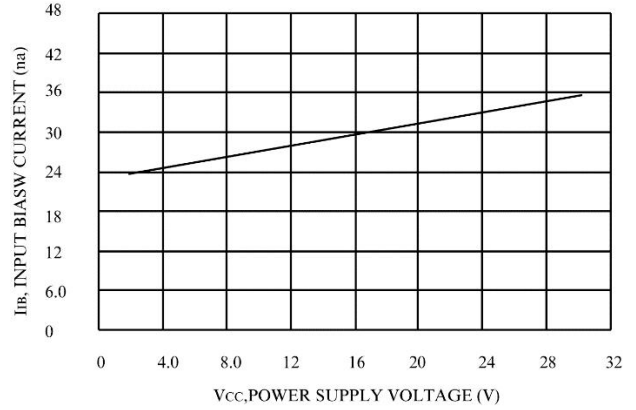


Figure 2. Input Bias Current

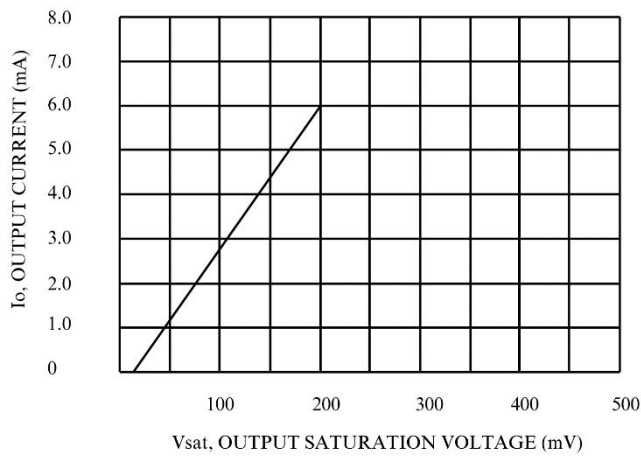
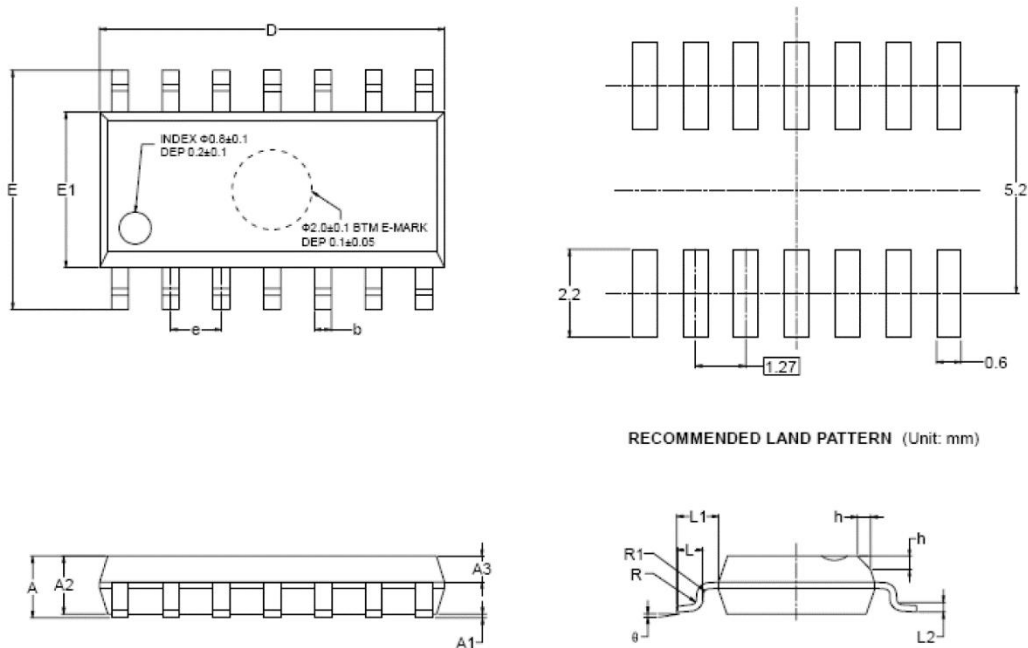


Figure 3. Output Sink Current versus Output Saturation Voltage

Package Dimensions (SOP14)



RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		Dimensions Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.250	1.650	0.049	0.065
A3	0.550	0.750	0.022	0.030
b	0.360	0.490	0.014	0.019
D	8.530	8.730	0.336	0.344
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270 BSC		0.050 BSC	
L	0.450	0.800	0.018	0.032
L1	1.040 REF		0.040 REF	
L2	0.250 BSC		0.010 BSC	
R	0.070		0.003	
R1	0.070		0.003	
h	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

PACKAGE/ORDERING INFORMATION

PRODUCT	ORDERING	TEMPRANGE	PACKAGE	PAKEAGE	TRANSPOT
CBM2901	CBM2901AS14	-40°C~85°C	SOP-14	CBM2901A	Tape and Reel,2500