

#### P-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
001/	$80m\Omega @ V_{GS} = -4.5V$	-3.5A
-20V	$110 \text{m}\Omega$ @ $V_{GS} = -2.5 \text{V}$	-3.0A

## **Description**

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

## **Applications**

- Backlighting
- Power Management Functions
- DC-DC Converters
- Motor Control

### **Features**

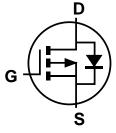
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

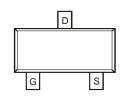
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound;
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)







Internal Schematic



Top View Pin Configuration

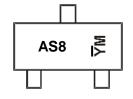
### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMP2110U-7	SOT23	3,000/Tape & Reel
DMP2110U-13	SOT23	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



AS8 = Product Type Marking Code

YM = Date Code Marking

Y or Y = Last Digit of Year (ex: F = 2018)

M = Month (ex: 9 = September)

Date Code Key

Year	2017	2018	20	019	2020	2021	I	2022	2023	20:	24	2025
Code	E	F		G	Н			J	K	L	-	M
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		$V_{DSS}$	-20	V	
Gate-Source Voltage		$V_{GSS}$	±10	V	
Continuous Drain Current (Note 6) V <sub>GS</sub> = -4.5V	I <sub>D</sub>	-3.5 -2.8	А		
Continuous Drain Current (Note 6) $V_{GS} = -2.5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			I <sub>D</sub>	-3.0 -2.4	А
Maximum Continuous Body Diode Forward Curre	ent (Note 6)	Is	-1.5	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)	I <sub>DM</sub>	-15	A	

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		$P_D$	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	158	°C/W	
Total Power Dissipation (Note 6)		P <sub>D</sub>	1.2	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	100	°C/W	
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$		
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	-1.0	μA	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V		
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.45	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$		
Chatia Dunia Carras On Basistanas			55	80	mΩ	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.8A		
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	67	110	mt2	$V_{GS} = -2.5V, I_D = -2.0A$		
Diode Forward Voltage	$V_{SD}$		-0.7	-1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A		
DYNAMIC CHARACTERISTICS (Note 8)	0 00 1 0							
Input Capacitance	C <sub>iss</sub>	_	443	_	pF			
Output Capacitance	Coss	_	59	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ - f = 1.0MHz		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	47	_	pF	-1 = 1.0WHZ		
Gate Resistance	$R_{G}$		8.5		Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$		
Total Gate Charge	Qq	_	6.0	_	nC			
Gate-Source Charge	$Q_{gs}$	_	0.6	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V, I_{D} = -3A$		
Gate-Drain Charge	$Q_{gd}$	_	1.8	_	nC			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	4.0	_	ns			
Turn-On Rise Time	t <sub>R</sub>	_	3.7	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>		24.5	_	ns	$R_L = 10\Omega, R_G = 1.0\Omega, I_D = -1A$		
Turn-Off Fall Time	t <sub>F</sub>	_	9.5	_	ns	7		
Reverse Recovery Time	t <sub>RR</sub>		8.3	_	ns	I <sub>F</sub> = -1.0A, di/dt = 100A/μs		
Reverse Recovery Charge	$Q_{RR}$		2.0	_	nC	I <sub>F</sub> = -1.0A, di/dt = 100A/µs		

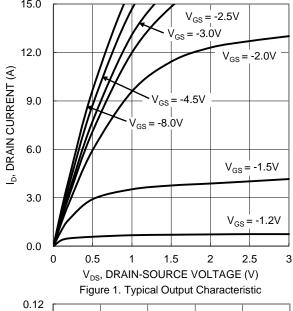
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

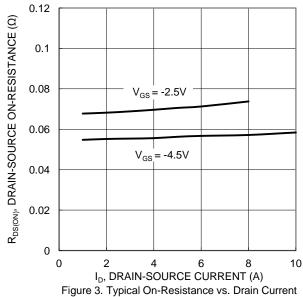
<sup>6.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.7. Short duration pulse test used to minimize self-heating effect.

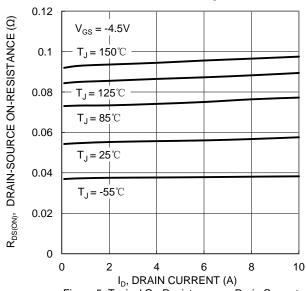
<sup>8.</sup> Guaranteed by design. Not subject to product testing.





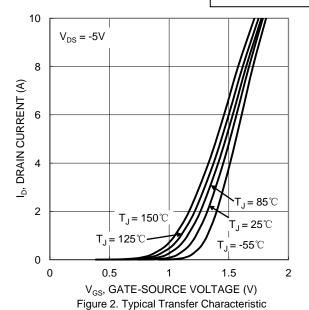






and Gate Voltage

Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature



0.5 (G) 0.45 0.45 0.30 0.35 0.35 0.35 0.35 0.25 0.15 0.05 0.05 0.05 0.05

8

V<sub>GS</sub>, GATE-SOURCE VOLTAGE (V)

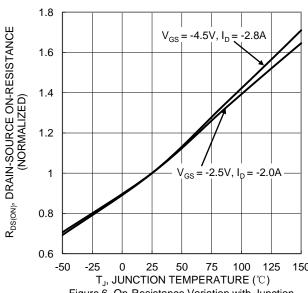
Figure 4. Typical Transfer Characteristic

10

12

0

2





## **DMP2110U**

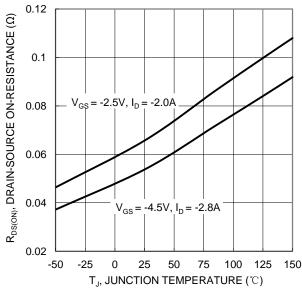


Figure 7. On-Resistance Variation with Junction Temperature

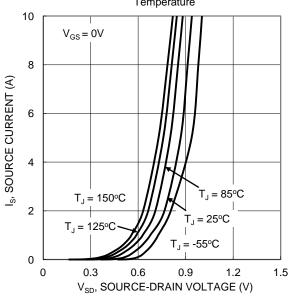


Figure 9. Diode Forward Voltage vs. Current 10 8 6  $V_{GS}(V)$ 4  $V_{DS} = -10V, I_{D} = -3.0A$ 2 0 0 2 6 8 10 12 14 4  $Q_g$  (nC)

Figure 11. Gate Charge

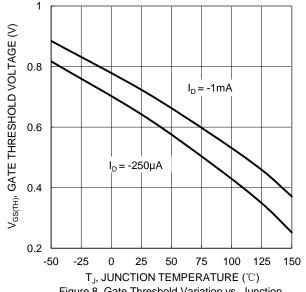
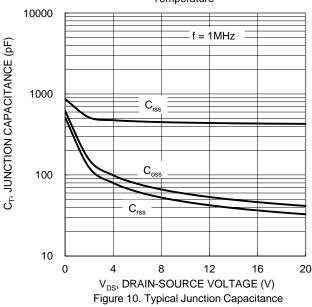
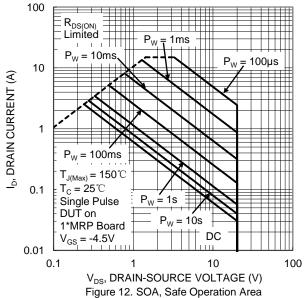


Figure 8. Gate Threshold Variation vs. Junction Temperature







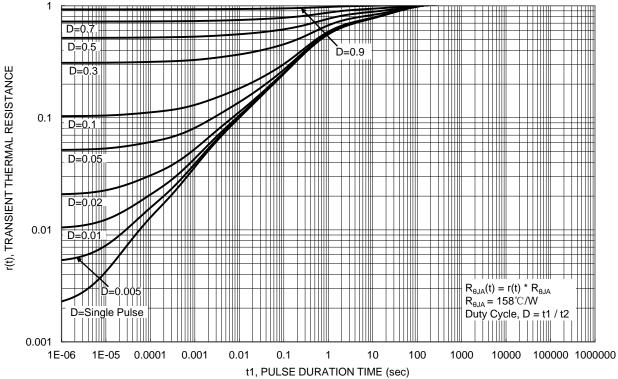


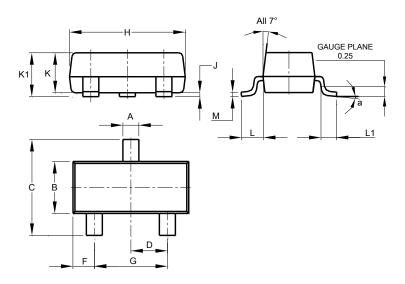
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23

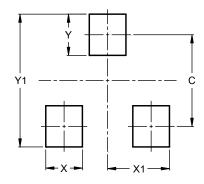


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



Dimensions	Value (in mm)		
С	2.0		
Х	0.8		
X1	1.35		
Υ	0.9		
Y1	29		



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