

N-Channel MOSFET

Applications:

- Adaptor
- Charger
- .SMPS

Features:

- RoHS Compliant
- Low ON Resistance
- .Low Gate Charge
- Peak Current vs Pulse Width Curve
- Inductive Switching Curves

Ordering Information

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PART NUMBER	BRAND						
ITA12N65R	TO-220F	IPS					

Absolute Maximum Ratings $T_C=25^{\circ}C$ unle

T _C =25℃	unless	otherwise	specified

Symbol	Parameter	ITA12N65R	Units
V _{DSS}	Drain-to-Source Voltage	650	V
I _D	Continuous Drain Current	12	А
I _{DM}	Pulsed Drain Current, V _{GS} @10V (NOTE *2)	48	А
D	Power Dissipation	42	W
P _D	Derating Factor above 25℃	0.34	W/℃
V _{GS}	Gate-to-Source Voltage	±30	V
E _{AS}	Single Pulse Avalanche Energy (L=10mH)	550	mJ
TL	Maximum Temperature for Soldering	300	
$T_{\rm J}$ and $T_{\rm STG}$	Operating Junction and Storage Temperature Range (NOTE *1)	150,-55 to150	°C

Thermal Resistance

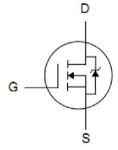
Symbol	Parameter	Тур.	Units	Test Conditions
Б	Junction-to-Case	2.98		Water cooled heatsink, P_{D} adjusted for a
$R_{ extsf{ heta}JC}$	Junction-to-Case	2.90	°C /W	peak junction temperature of +150 $^\circ\!\!{ m C}$.
R _{0JA}	Junction-to-Ambient	62.5		1 cubic foot chamber, free air.

P6 Lead Free Package and Finish

ITA12N65R

V _{DSS}	R _{DS(ON)} (Typ.)	I _D
650V	0.66Ω	12A

G_{DS} TO-220F Packages Not to Scale





OFF Characteristics $T_C=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	650			V	V _{GS} =0V, I _D =250µA
I _{DSS}	Drain-to-Source Leakage Current			1		V _{DS} =650V, V _{GS} =0V
				I	μA	T J=25 ℃
				100	μΑ	V_{DS} =520V, V_{GS} =0V
				100		T 」=125 ℃
	Gate-to-Source Forward Leakage +100	+100	nA	V_{GS} =+30V		
I _{GSS}	Gate-to-Source Reverse Leakage			-100	ПА	V _{GS} = -30V

ON Characteristics $T_J=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Р	StaticDrain-to-Source		0.66	0.0	0	V _{GS} =10V, I _D =6A
R _{DS(ON)}	On-Resistance(NOTE *3)		0.66	0.8	Ω	
V _{GS(TH)}	Gate Threshold Voltage	2		4	V	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$
g _{fs}	Forward Transconductance(NOTE *3)		12		S	V _{DS} =15V, I _D =6A

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
C _{iss}	Input Capacitance		1993			(1 - 0)(1) = 2E(1)
C _{oss}	Output Capacitance		160		pF	V_{GS} = 0V, V_{DS} = 25V f =1.0MHz
C _{rss}	Reverse Transfer Capacitance		9.5			
Qg	Total Gate Charge		40			
Q _{gs}	Gate-to-Source Charge		10		nC	I _D =12A,V _{DD} =520V V _{GS} = 10V
Q_{gd}	Gate-to-Drain ("Miller") Charge		14			v _{GS} – 10V

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
t _{d(ON)}	Turn-on Delay Time		28			
t _{rise}	Rise Time		26		200	V_{DD} =325V, I_{D} =12A,
t _{d(OFF)}	Turn-Off Delay Time		64		ns	V_G =10V R_G =10 Ω
t _{fall}	Fall Time		45			



Source-Drain Diode Characteristics	Tc=25℃
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Tc=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
1-	Continuous Source Current			12	А	
IS	(Body Diode)			12	A	T _c =25℃
1	Maximum Pulsed Current			48	А	1 _C -25 C
I _{SM}	(Body Diode)			40	A	
V_{SD}	Diode Forward Voltage			1.5	V	I _{SD} =12A, V _{GS} =0V
t _{rr}	Reverse Recovery Time		651		ns	I _F = I _S
Q _{rr}	Reverse Recovery Charge		4297		nC	di/dt=100A/us

Notes:

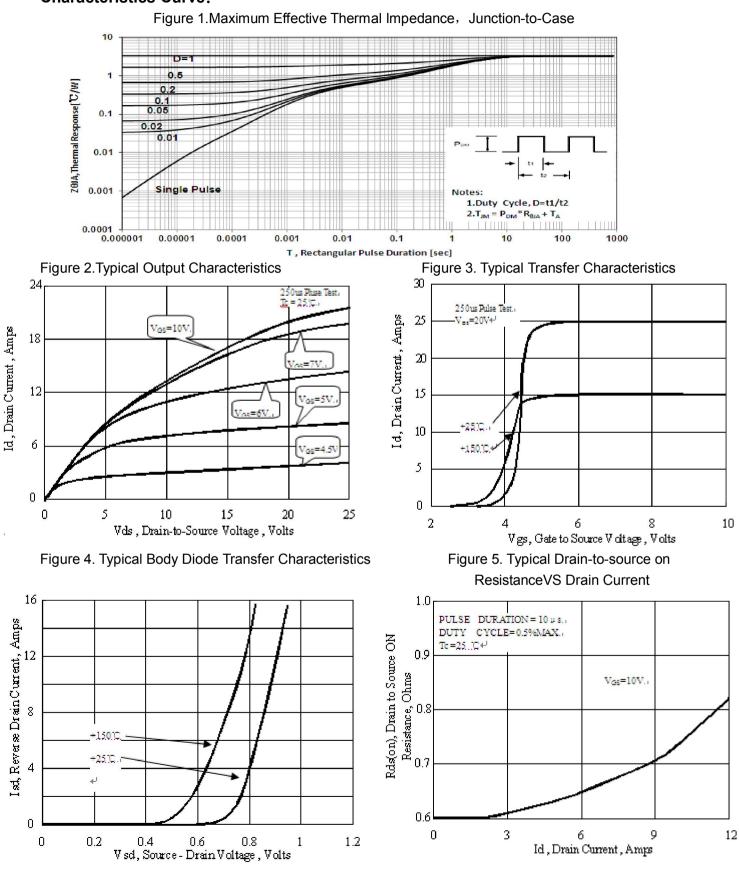
*1. T_J = +25 $^\circ \rm C$ to +150 $^\circ \rm C$.

*2. Repetitive rating; pulse width limited by maximum junction temperature.

*3. Pulse width < 380μ s; duty cycle < 2%.



Characteristics Curve:





I_D=12A

50

150

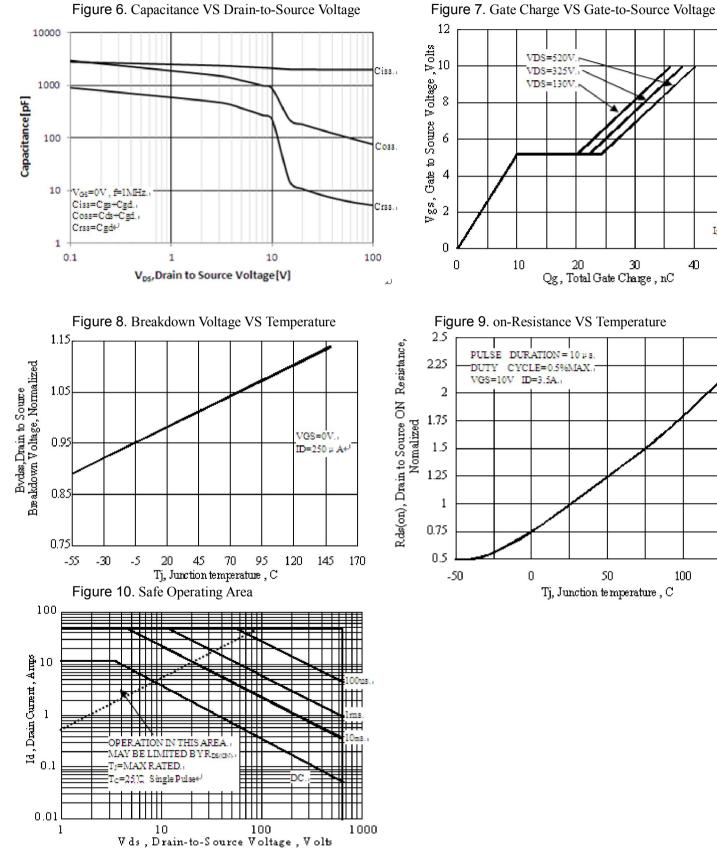
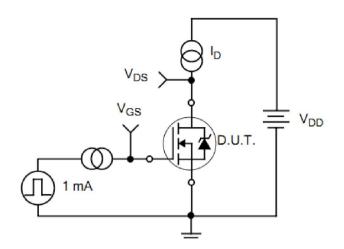


Figure 6. Capacitance VS Drain-to-Source Voltage



ITA12N65R

Test Circuits and Waveforms



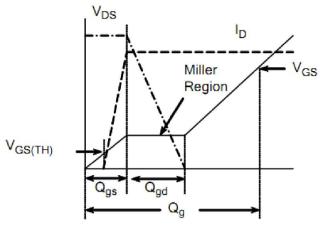


Figure 11. Gate Charge Test Circuit

Figure 12. Gate Charge Waveforms

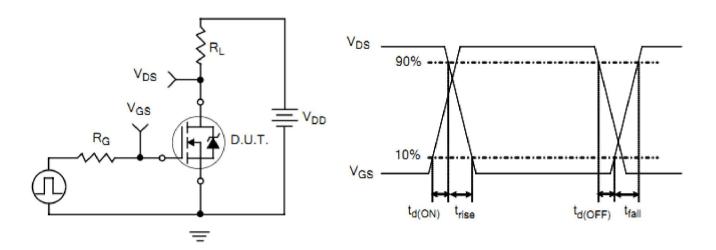
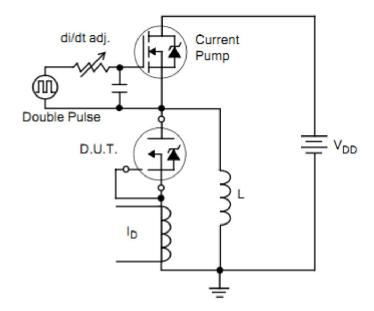


Figure 13. Resistive Switching Test Circuit







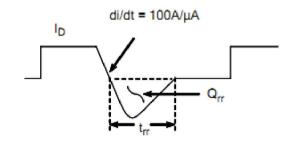


Figure 15. Diode Reverse Recovery Test Circuit

Figure 16. Diode Reverse Recovery Waveform

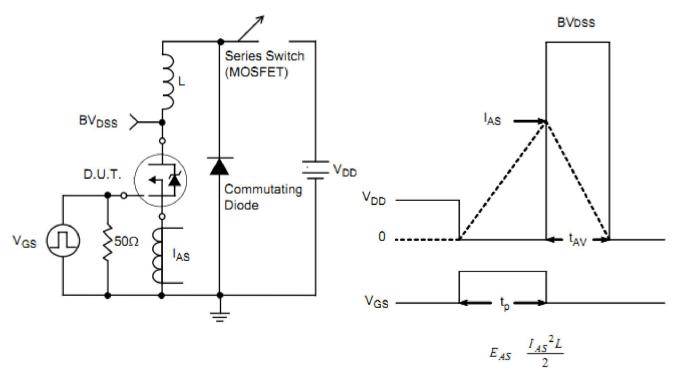


Figure 17. Unclamped Inductive Switching Test Circuit Figure 18. Unclamped Inductive Switching Waveform



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