

6ns, 3V/5V, Single-Supply, Low Power, Rail-to-Rail I/O Comparator

GENERAL DESCRIPTION

The SGM8744 is a single, high speed, low power comparator, which features a fast 6ns propagation delay. The device is optimized for low voltage operation on 3V or 5V supply, and consumes only 1.3mA supply current.

The SGM8744 supports rail-to-rail input and output operation. The input common mode voltage range is from -0.1V to $(+V_S)$ + 0.1V, and the output voltage swing is within 0.2V of the rails without external pull-up or pull-down resistor. The device can be compatible with CMOS and TTL logics. Any input or output pin has a continuous short-circuit protection to both power supply rails. The SGM8744 has an internal hysteresis for reducing comparator sensitivity to noise, even when the input signals move slowly.

The SGM8744 is available in Green SOT-23-5 and SC70-5 packages. It is rated over the -40°C to +85°C temperature range.

FEATURES

 High Speed: 6ns Propagation Delay (100mV Overdrive)

SGM8744

- Low Supply Current: 1.3mA (TYP) at V_S = 3V
- Low Offset Voltage: 0.8mV (TYP)
- Rail-to-Rail Input and Output
- Supply Voltage Range: 2.7V to 5.5V
- Optimized for 3V and 5V Applications
- Output Swing to within 195mV from Rails with 4mA Output Current
- Supports CMOS or TTL Logic
- Internal Hysteresis for Reducing Comparator Sensitivity to Noise
- -40°C to +85°C Operating Temperature Range
- Available in Green SOT-23-5 and SC70-5 Packages

APPLICATIONS

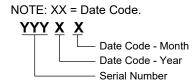
3V or 5V Applications
Portable/Battery-Powered Equipment
Mobile Phones
Zero-Crossing Detectors
Threshold Detectors
Line Receiver Units



PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION	
SGM8744	SOT-23-5	-40°C to +85°C	SGM8744YN5G/TR	SM9XX	Tape and Reel, 3000	
3GIVI0744	SC70-5	-40°C to +85°C	SGM8744YC5G/TR	SM8XX	Tape and Reel, 3000	

MARKING INFORMATION



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, +V _S to -V _S	6V
V _{IN} Differential	±2.5V
Voltage at Input/Output Pins (-Vs) -	$0.3V$ to $(+V_S) + 0.3V$
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	
HBM	6000V
MM	400V

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range-40°C to +85°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

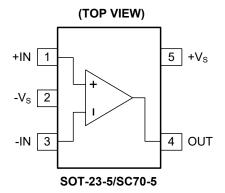
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATIONS



ELECTRICAL CHARACTERISTICS

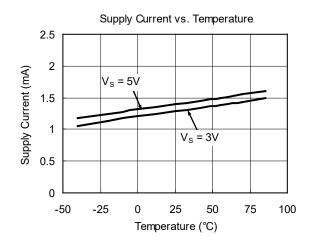
 $(V_S = 5.0V, V_{CM} = 0V, C_L = 15pF, typical values are at T_A = +25°C, unless otherwise noted.)$

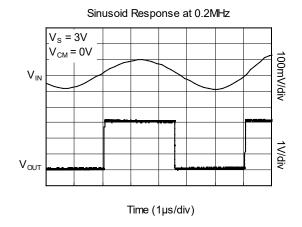
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS		
Operating Supply Voltage (1)	Vs		2.7		5.5	V		
Input Common Mode Voltage Range (2)	V_{CM}		-0.1		V _S + 0.1	V		
Input Offset Voltage (3)		V _S = 5V, V _{CM} = 0V		0.8	4.9			
Input Offset Voltage	Vos	-40°C ≤ T _A ≤ +85°C			5.6	mV		
Input Hysteresis (4)	V _{HYST}	V _S = 5V, V _{CM} = 0V		3		mV		
		$V_S = 5V$, Out to $V_S/2$	23.5	35		mA		
Output Short Circuit Current	I _{SOURCE}	-40°C ≤ T _A ≤ +85°C	19.5					
Output Short-Circuit Current		$V_S = 5V$, Out to $V_S/2$		-31	-25			
	I _{SINK}	-40°C ≤ T _A ≤ +85°C			-20			
O	OMBB	V _S = 5V, V _{CM} = 0V to 5V	60	77		dB		
Common Mode Rejection Ratio (5)	CMRR	-40°C ≤ T _A ≤ +85°C	57					
David Overska Daila etian Datie		V _{CM} = 0V, V _S = 2.7V to 5.5V	56	74		dB		
Power Supply Rejection Ratio	PSRR	-40°C ≤ T _A ≤ +85°C	51					
	V _{OH}	V _S = 5V, I _{OUT} = 4mA		195	275	m)/		
Output Valtage Swing from Beil		-40°C ≤ T _A ≤ +85°C			308			
Output Voltage Swing from Rail	V _{OL}	V _S = 5V, I _{OUT} = -4mA		188	245	mV		
		-40°C ≤ T _A ≤ +85°C			277			
	Is	V _S = 3V, I _{OUT} = 0		1.3	1.7			
Comple Compant		-40°C ≤ T _A ≤ +85°C			2.0	1		
Supply Current		V _S = 5V, I _{OUT} = 0		1.4	1.9	mA		
		-40°C ≤ T _A ≤ +85°C			2.1			
Decreasing Delay (High to Law)		V _S = 3V, Overdrive = 10mV		11				
Propagation Delay (High to Low)		V _S = 3V, Overdrive = 100mV		6		ns		
Daniel and Balance (Laurette High)		V _S = 3V, Overdrive = 10mV		11				
Propagation Delay (Low to High)		V _S = 3V, Overdrive = 100mV		6		ns		
Rise Time	t _{RISE}	V _S = 3V, Overdrive = 10mV		8		ns		
Trise Tillie		V _S = 3V, Overdrive = 100mV		8				
Fall Time	+	V _S = 3V, Overdrive = 10mV	6			no		
Fall Time	t _{FALL}	V _S = 3V, Overdrive = 100mV		6		ns		

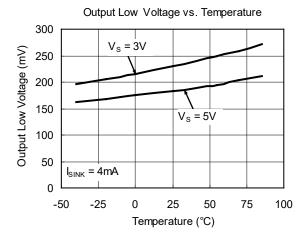
NOTES:

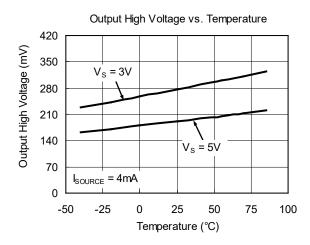
- 1. This value is from PSRR test.
- 2. This value is from PD test. For the range of common mode voltage, the maximum input common mode voltage can reach (+V_S)
- + 0.1V without any damage to SGM8744.
- 3. V_{OS} is the midway voltage for the hysteresis zone of the comparator.
- 4. The input hysteresis is the gap between the upper threshold where the output of the comparator switches to high position and the lower threshold where the output of the comparator switches to low position.
- 5. CMRR is defined over the condition of whole input common mode range.

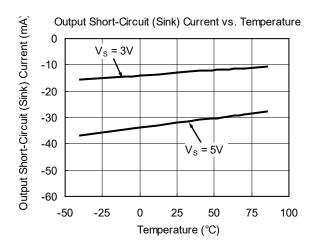
TYPICAL PERFORMANCE CHARACTERISTICS

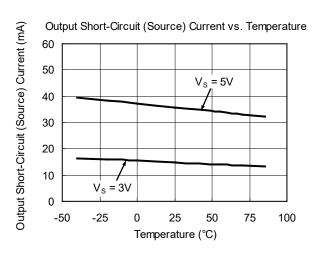




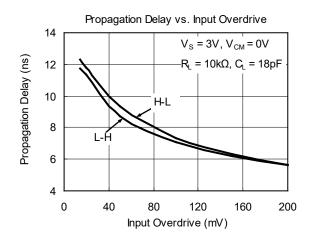


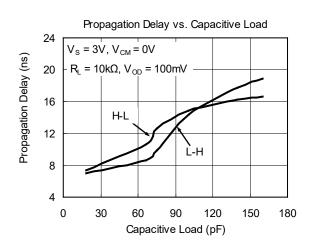


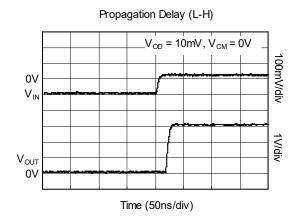


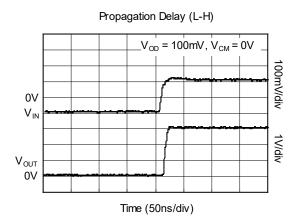


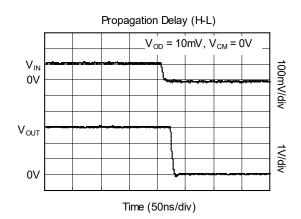
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

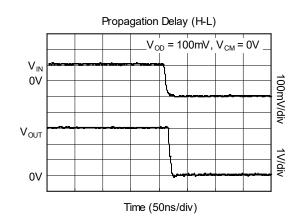




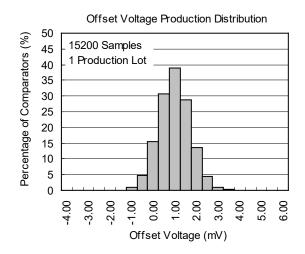








TYPICAL PERFORMANCE CHARACTERISTICS (continued)



DETAILED DESCRIPTION

The SGM8744 is a single, high speed, low power comparator with internal hysteresis. The device is optimized for low voltage operation from 2.7V to 5.5V single supply. It supports rail-to-rail input and output operation. With 4mA output current, the output voltage swing is within 195mV of the rails without external pull-up or pull-down circuitry. The SGM8744 is suitable for portable equipment. It can be compatible with CMOS and TTL logics.

There are a lot of comparators switched frequently for the linear region as the effect of noise and parasitic parameters, and the condition of this negative situation is when the one input of the comparator tends to reach the other input voltage. In order to ease the effect of noise and parasitic parameter, there is a 3mV internal hysteresis inside the comparator.

There are two trip points which are made by the comparator: the trip points when rising edge occurs and the trip points when falling edge occurs. And the gap between two trip points is the hysteresis of the comparator. The offset voltage Vos is defined as the average value of the two trip points. For the condition which two inputs of the comparator are nearly equal, the internal hysteresis will launch to avoid the frequently switching at this case. For the normal comparator other than SGM8744 the users usually use external resistors connected at +IN pin to provide hysteresis, while the internal hysteresis of SGM8744 can provide internal hysteresis without any external component. However, if users need more hysteresis to reject the influence of noise or parasitic parameters, please add the external resistors at +IN pin to increase the hysteresis.

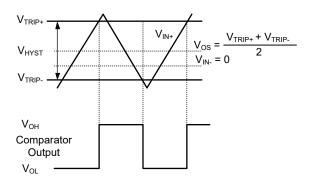


Figure 1. The Waveform for Input and Output, Non-Inverting Input Varied

On the condition shown in Figure 1, the -IN is fixed while +IN is varied, and the corresponding output is shown under the figure of input signal. However, if +IN is fixed while -IN is varied, the output will be inverted.

Output Structure

In Figure 2, the SGM8744 has a push-pull output stage. When output is changed from logic high/low to low/high, the changed sink/source current pulls/pushes output pin to logic low/high. Beginning this transition, larger sink/source current is used to create a high slew rate transit from high/low to low/high. Once the output voltage reaches $V_{\text{OL}}/V_{\text{OH}}$, it will reduce the sink/source current to a just right value to maintain the $V_{\text{OL}}/V_{\text{OH}}$ static condition. This current-driven push-pull output stage will significantly reduce the power consumption in application system.

If low slew rate transition is needed in system design, adjusting the load capacitance will change the slew rate. The heavier capacitive load will slow down the output voltage transition. This feature will be used to reduce the interference generated by fast edge of transition between 1 and 0 in noise-sensitive system.

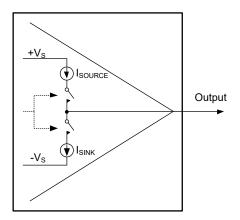


Figure 2. Push-Pull Output Structure

APPLICATION INFORMATION

Application Circuits

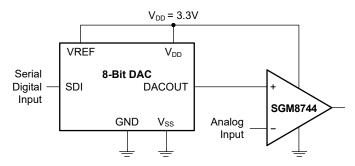


Figure 3. A Threshold Detector Controlled by 8-Bit DAC

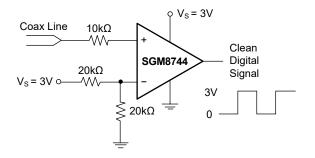


Figure 4. The Application of Line Receiver

Layout and Bypassing

Good power supply decoupling, layout and grounding are very important for SGM8744 to realize the full high-speed capabilities in system, following skills will be used:

- A $0.1\mu F$ to $4.7\mu F$ range ceramic capacitor is used to provide good power supply decoupling. This ceramic capacitor must be placed as close to +V_S pin as possible.
- ◆ For grounding, unbroken and low-inductance ground plane is a good choice.
- For Layout, use short PCB trace to avoid unwanted parasitic feedback around the comparator. SGM8744 must be soldered directly to the PCB and the socket is not recommended.

6ns, 3V/5V, Single-Supply, Low Power, Rail-to-Rail I/O Comparator

SGM8744

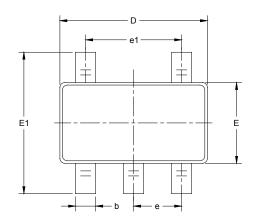
REVISION HISTORY

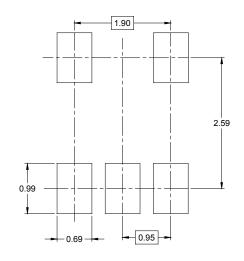
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Original (NOVEMBER 2014) to REV.A

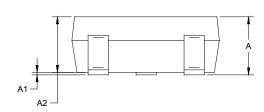
Page

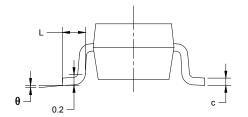
PACKAGE OUTLINE DIMENSIONS SOT-23-5





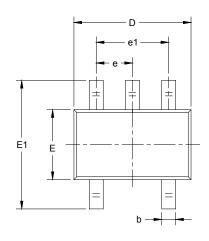
RECOMMENDED LAND PATTERN (Unit: mm)

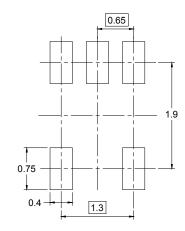




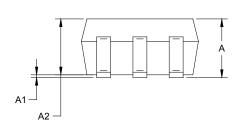
Symbol	-	nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	800.0	
D	2.820	3.020	0.111	0.119	
Е	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	BSC	BSC 0.037 BSC		
e1	1.900 BSC		0.075	BSC	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

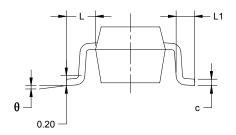
PACKAGE OUTLINE DIMENSIONS SC70-5





RECOMMENDED LAND PATTERN (Unit: mm)

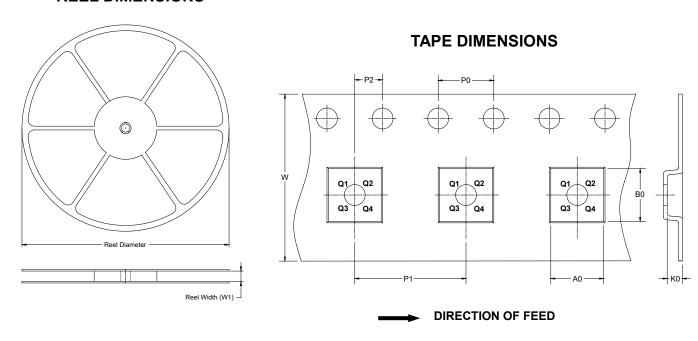




Symbol		nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.150	0.350	0.006	0.014	
С	0.080	0.150	0.003	0.006	
D	2.000	2.200	0.079	0.087	
Е	1.150	1.350	0.045	0.053	
E1	2.150	2.450	0.085	0.096	
е	0.65 TYP		0.026 TYP		
e1 1.300 BSC		BSC	0.051 BSC		
L	0.525 REF		0.021	REF	
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	

TAPE AND REEL INFORMATION

REEL DIMENSIONS

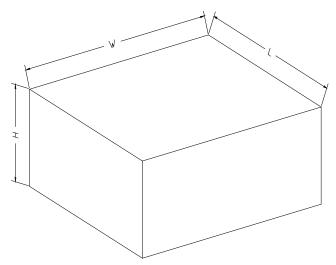


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT-23-5	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	Q3
SC70-5	7"	9.5	2.25	2.55	1.20	4.0	4.0	2.0	8.0	Q3

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton	
7" (Option)	368	227	224	8	
7"	442	410	224	18	