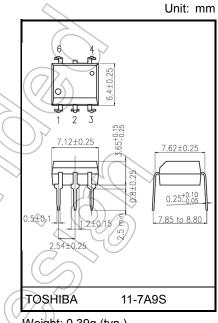
TLP560G

TOSHIBA Photocoupler IRED & Photo-Triac

TLP560G

Triac Driver **Programmable Controllers AC-Output Module** Solid State Relay The TOSHIBA TLP560G consists of a photo-triac optically coupled to an infrared emitting diode in a six lead plastic DIP package. Peak off-state voltage: 400 V (min) On-state current: 100 mA (max) Isolation voltage: 2500 Vrms (min) UL-recognized: UL 1577, File No.E67349 cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349 VDE-approved: EN 60747-5-5 (Note 1) Note 1: When a VDE approved type is needed, TOSHIBA Weight: 0.39g (typ.) please designate the Option(D4).



Classification	Trigger LED Current (mA) VT = 3V, Ta = 25°C		Marking of Classification		
(Note 2)	Min	Max	Classification		
(IFT5)	-	5	T5		
(IFT7)	- (0	7	T5, T7		
Standard		10	T5, T7, blank		

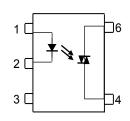
Note 2: Ex. (IFT5); TLP560G(IFT5) Note: Application type name for certification test, please use standard product type name, i.e. TLP560G(IFT5); TLP560G

Note: According to VDE0110, table 4

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Pin Configuration (top view)



1 : Anode

2 : Cathode

3 : N.C.

4 : Triac Terminal

6 : Triac Terminal

Start of commercial production 1982-12

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit		
Forward current		١ _F	50	mA		
	Forward current derating (Ta ≥ 53°C)		ΔI _F / °C	-0.7	mA / °C	
	Peak forward current (100µs pulse, 100pps)		IFP	1	A	
LED	Reverse voltage		VR	5	v	
	Diode power dissipation		PD	100	mW	$\sum r$
	Diode power dissipation derating (Ta ≥ 53°C)		ΔP _D /°C	-1.4	mW/°C	
	Junction temperature		Tj	125	(°C))
	Off-state output terminal voltage		VDRM	400	V	
	On-state RMS current	Ta = 25°C		100	\mathcal{I}	
		Ta = 70°C	IT(RMS)	50	-mA	
	On-state current derating (Ta ≥ 25°C)		ΔI _T / °C	-14	mA / °C	
Detector	Peak on-state current (100µs pulse, 120pps)		ITP	(//2)	A	$\langle \rangle >$
Dete	Peak nonrepetitive surge current (Pw = 10ms)			1.2	A	()
			ITSM	1.2	A	
	Output power dissipation		PO	300	mW	$\mathcal{D}_{\mathcal{A}}$
	Output power dissipation derating (Ta \ge 25°C)		ΔP ₀ /°C	-3.0	mW / °C	
	Junction temperature		Tj /	115 ((2°	
Storage temperature range		Tstg	-55 to 125)°¢		
Operating temperature range		Topr	-40 to 100	°C		
Lead soldering temperature (10 s)		T _{sol}	260	°C		
Isolatic	lsolation voltage (AC, 60 s, R.H. ≤ 60 %)			2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	_	_	120	Vac
Forward current	Le construction de la constructi	15	20	25	mA
Peak on-state current	भग	—	—	1	А
Operating temperature	T _{opr}	-25	-	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

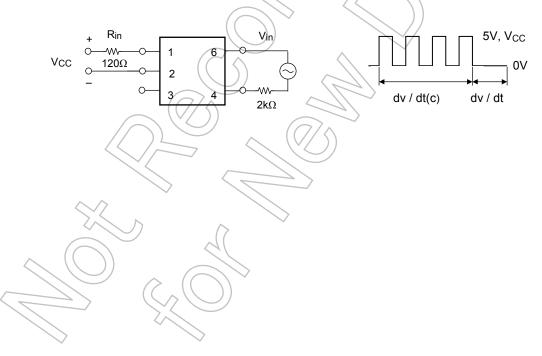
Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μA
	Capacitance	CT	V = 0 V, f = 1 MHz	γ	30	_	pF
Detector	Peak off-state current	IDRM	V _{DRM} = 400 V		10	100	nA
	Peak on-state voltage	Vтм	I _{TM} = 100 mA		1.7	3.0	V
	Holding current	lΗ	6)(0.6	_	mA
	Critical rate of rise of off-state voltage	dv / dt	V _{in} = 120 Vrms, Ta = 85 °C (Fig.1)	200	500	-	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	V _{in} = 30 Vrms, I _T = 15 mA (Eig.1)	_	0.2	_	V / µs

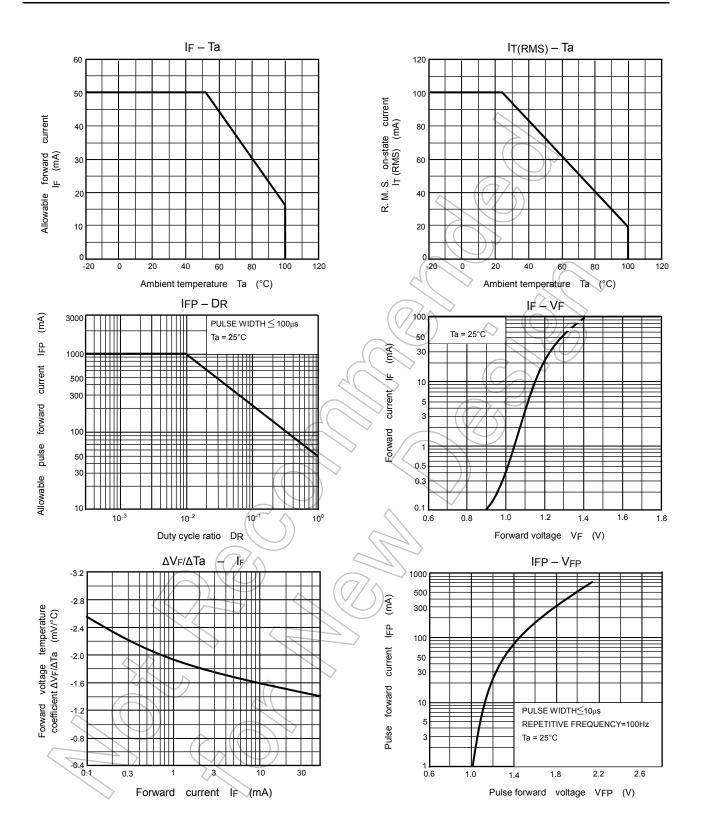
Coupled Electrical Characteristics (Ta = 25°C)

					b	
Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
Trigger LED current	IFT	V _T = 3 V	\sim	5	10	mA
Capacitance (input to output)	CS	V _S = 0 V, f = 1 MHz		0.8	_	pF
Isolation resistance	Rs	Vs = 500 V, R.H. ≤ 60 %	5×10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage	BVs	AC, 60 s	2500	—	_	Vrms

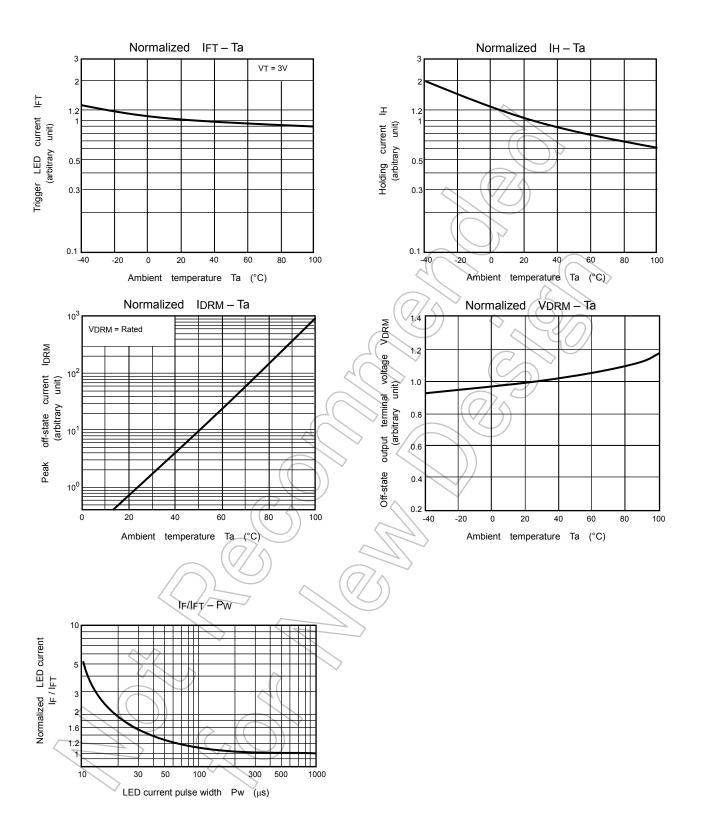
Fig.1: dv / dt test circuit



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NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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