

WSR70P10D

P-Ch MOSFET

### **General Description**

The WSR70P10D is the highest performance trench P-Ch MOSFET with extreme high cell density , which provide excellent  $R_{\text{DSON}}$  and gate charge for most of the small power switching and load switch applications.

The WSR70P10D meet the RoHS and Green Product requirement with full function reliability approved.

### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

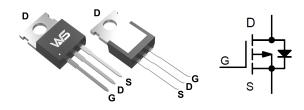
### **Product Summery**

BV <sub>DSS</sub>	R <sub>DSON</sub>	I <sub>D</sub>
-100V	19mΩ	-70A

#### **Applications**

Inverters

### **TO-220AB** Pin Configuration



### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit		
Common Ra	atings (T <sub>c</sub> =25°C Unless Otherwise Noted)		<b>.</b>		
V <sub>DSS</sub>	Drain-Source Voltage	-100	V		
$V_{GSS}$	Gate-Source Voltage	±25			
TJ	Maximum Junction Temperature	175	°C		
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C		
I <sub>S</sub>	Diode Continuous Forward Current	T <sub>C</sub> =25°C	-70	А	
Mounted or	n Large Heat Sink	-			
$I_{DP}^{(1)}$	300µs Pulse Drain Current Tested	T <sub>C</sub> =25°C	-240	А	
$I_D^{(2)}$	Continuous Drain $Current() = 1000$	T <sub>C</sub> =25°C	-70	А	
	Continuous Drain Current(V <sub>GS</sub> =-10V)	T <sub>c</sub> =100°C	-45		
P <sub>D</sub>	Maximum Dawar Dissinction	T <sub>C</sub> =25°C	190	W	
	Maximum Power Dissipation	T <sub>C</sub> =100°C	95		
$R_{ ext{ heta}JC}$	Thermal Resistance-Junction to Case	0.8	°C/W		
$R_{ ext{ heta}JA}$	Thermal Resistance-Junction to Ambient	62.5	°C/W		
Drain-Sourc	ce Avalanche Ratings				
E <sub>AS</sub> <sup>③</sup>	Avalanche Energy, Single Pulsed		400	mJ	



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## Electrical Characteristics (T<sub>C</sub>=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit
Static Cha	racteristics			•		
$BV_{DSS}$	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =-250µA	-100			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V			-1	μA
		T <sub>J</sub> =125°C			-30	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =-250μA	-1.2	-1.6	-2.5	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0V			±100	nA
$R_{DS(ON)}^{(4)}$	Drain-Source On-state Resistance	nce V <sub>GS</sub> =-10V, I <sub>DS</sub> =-20A		19	25	mΩ
	racteristics					
V <sub>SD</sub> <sup>(4)</sup>	Diode Forward Voltage	I <sub>SD</sub> =-30A, V <sub>GS</sub> =0V			-1.2	V
trr	Reverse Recovery Time			208		ns
Qrr	Reverse Recovery Charge	Isp=-5A, dlsp/dt=100A/µs		560		nC
Dynamic C	Characteristics <sup>5</sup>					
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz		2		Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V,		4230		pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-50V,		388		
C <sub>rss</sub>	Reverse Transfer Capacitance	Frequency=1.0MHz		26		
t <sub>d(ON)</sub>	Turn-on Delay Time			26		ns
t <sub>r</sub>	Turn-on Rise Time	V <sub>DD</sub> =-50V,I <sub>DS</sub> =-5A,		78		
$t_{d(OFF)}$	Turn-off Delay Time	$V_{GEN}$ =-10V, $R_{G}$ =6 $\Omega$		200		
t <sub>f</sub>	Turn-off Fall Time			210		
Gate Char	ge Characteristics <sup>5</sup>		-	-	-	
Qg	Total Gate Charge			80		nC
$Q_{gs}$	Gate-Source Charge	V <sub>DS</sub> =-50V, V <sub>GS</sub> =-10V, I <sub>DS</sub> =-5A		15.6		
$Q_{gd}$	Gate-Drain Charge	- <sup>CO</sup> - CO		17.2		

Notes: ①Pulse width limited by safe operating area.

②Calculated continuous current based on maximum allowable junction temperature.

③Limited by T<sub>Jmax</sub>, I<sub>AS</sub> =-40A, V<sub>DD</sub> =-60V, R<sub>G</sub> = 50Ω, Starting T<sub>J</sub> = 25° C.

4Pulse test;Pulse width $\leqslant$ 300µs, duty cycle $\leqslant$ 2%.

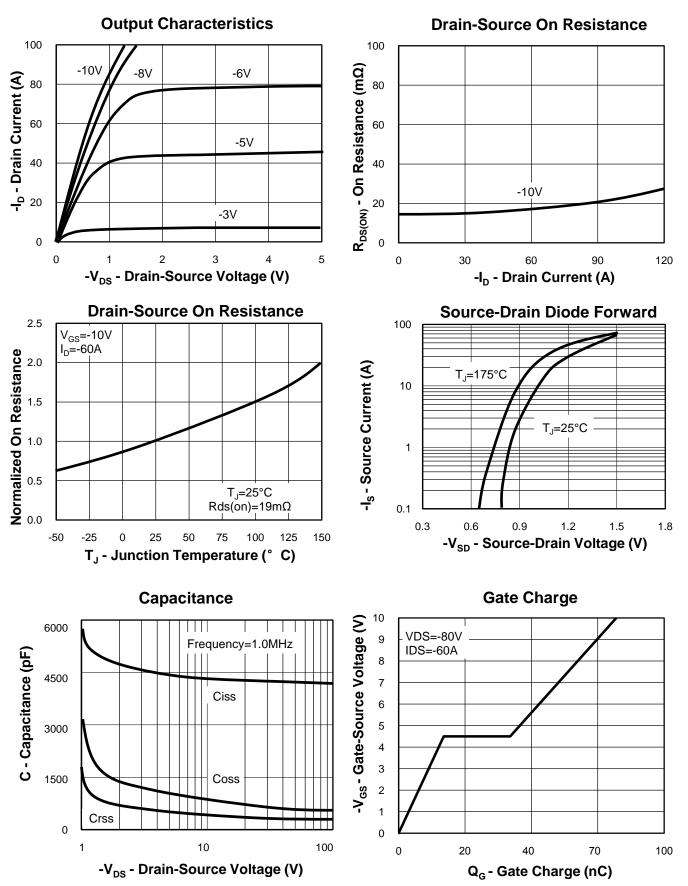
⑤Guaranteed by design, not subject to production testing.



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# **Typical Characteristics**

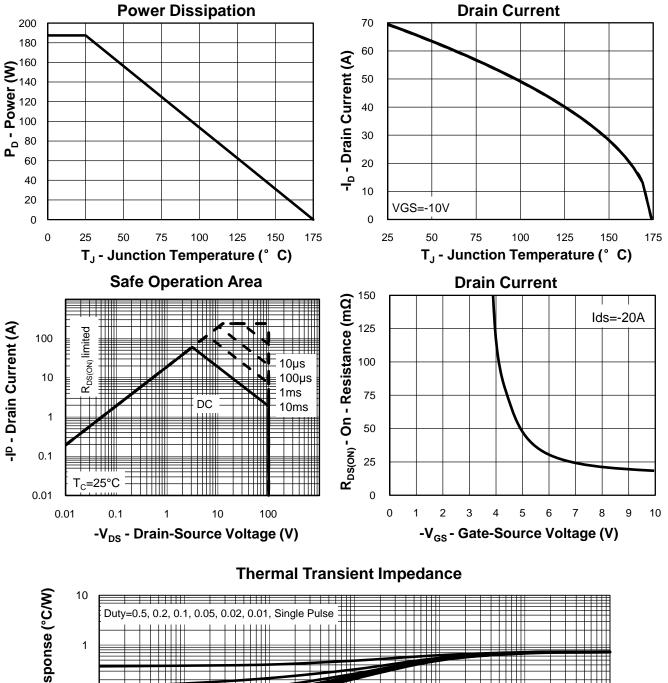


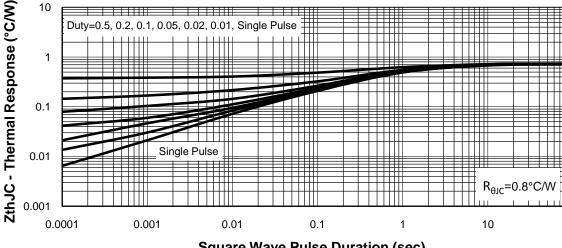


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# **Typical Characteristics**





Square Wave Pulse Duration (sec)



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