

#### **Description**

The AP6N03SI uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

 $V_{DS} = 30V I_{D} = 6A$ 

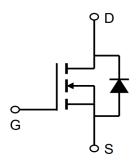
 $R_{DS(ON)}$  < 35m $\Omega$  @  $V_{GS}$ =4.5 V

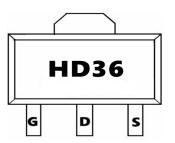
#### **Application**

Battery protection

Load switch

Uninterruptible power supply







**Package Marking and Ordering Information** 

Product ID	Pack	Marking	Qty(PCS)
AP6N03SI	SOT89-3L	HD36	1000PCS

Absolute Maximum Ratings (T<sub>c</sub>=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±12	V
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	6	А
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	4.7	Α
Ірм	Pulsed Drain Current <sup>2</sup>	30	А
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>3</sup>	1.5	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R <sub>0</sub> JA	Thermal Resistance Junction-ambient <sup>1</sup>	85	°C/W
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	30	°C/W



# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

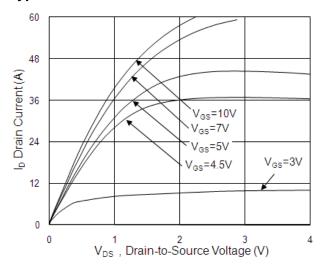
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V	
∆BV <sub>DSS</sub> /∆T <sub>J</sub>	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.021		V/°C	
		V <sub>GS</sub> =4.5V , b=5A		29	35		
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =2.5V , I <sub>D</sub> =4A		36	40	$\mathbf{m}\Omega$	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage		0.5	0.9	1.5	V	
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-5		mV/°C	
Ipss	Danie Common Lordon Commont	$V_{DS}$ =24V , $V_{GS}$ =0V , $T_{J}$ =25 $^{\circ}$ C			1	^	
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	uA	
Igss	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20V$ , $V_{DS}$ = $0V$			±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =5A		7		S	
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		2.5	5	Ω	
Qg	Total Gate Charge (4.5V)			6	8.4		
Qgs	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		2.5	3.5	nC	
Qgd	Gate-Drain Charge			2.1	2.9		
Td(on)	Turn-On Delay Time			2.4	4.8	- ns	
Tr	Rise Time	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V ,		7.8	14		
Td(off)	Turn-Off Delay Time	R <sub>G</sub> =3.3		22	44		
Tf	Fall Time	I <sub>D</sub> =5A		4	8		
Ciss	Input Capacitance			572	800		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		81	112	pF	
Crss	Reverse Transfer Capacitance			65	91	·	
Is	Continuous Source Current <sup>1,4</sup>				5.8	Α	
Isм	Pulsed Source Current <sup>2,4</sup>	−V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			30	Α	
VsD	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =3A , T <sub>J</sub> =25°C			1.2	V	
trr	Reverse Recovery Time	IF=5A , dl/dt=100A/μs , T <sub>J</sub> =25°C		19		nS	
Qrr	Reverse Recovery Charge			1.04		nC	

#### Note:

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300 \text{us}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150\,^{\circ}\text{C}$  junction temperature
- 4 .The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



# **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

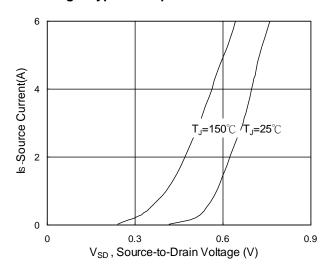


Fig.3 Forward Characteristics Of Reverse

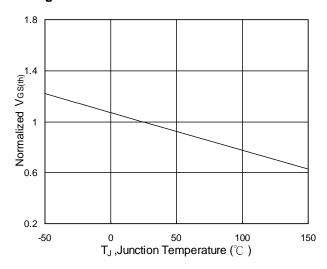


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

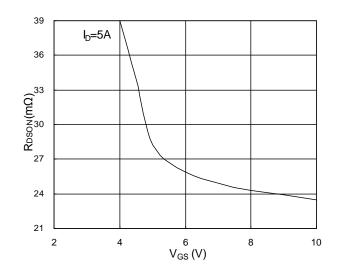


Fig.2 On-Resistance vs. G-S Voltage

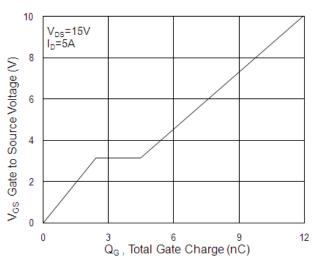


Fig.4 Gate-Charge Characteristics

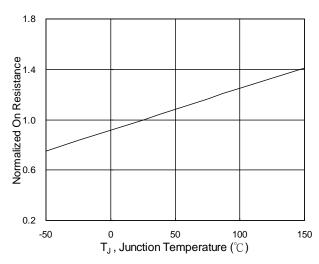
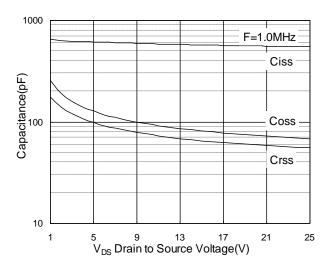


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>







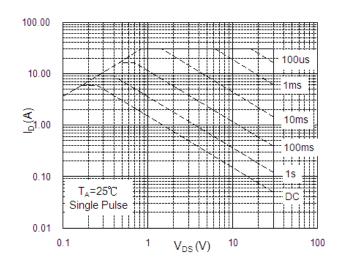


Fig.7 Capacitance

Fig.8 Safe Operating Area

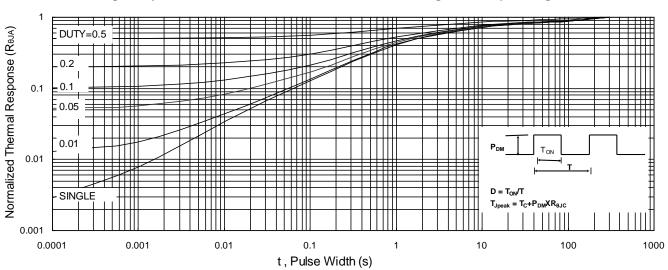


Fig.9 Normalized Maximum Transient Thermal Impedance

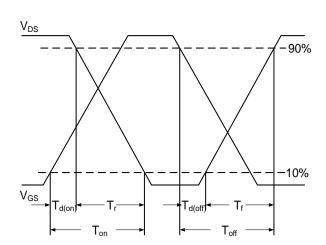


Fig.10 Switching Time Waveform

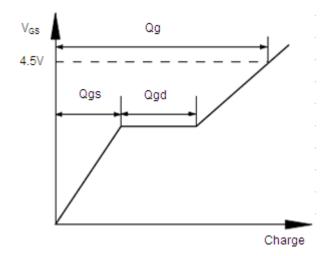
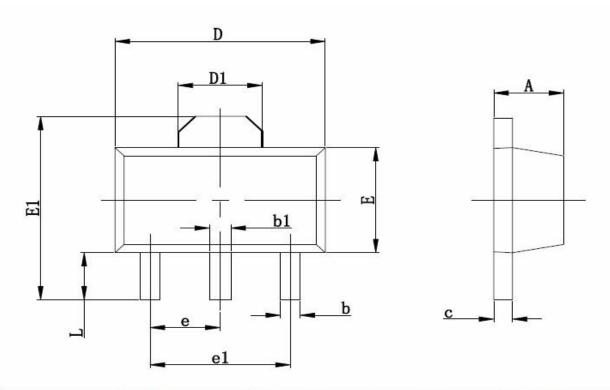


Fig.11 Gate Charge Waveform

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AP6N03SI RVE3.0





Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
Α	1.400	1.600	0.055	0.063
b	0.350	0.520	0.013	0.197
b1	0.400	0.580	0.016	0.023
С	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.350	2.550	0.091	0.102
E1	3.940	4.250	0.155	0.167
е	1.500 TYP		0.060TYP	
e1	3.000 TYP		0.118TYP	
Ĺ	0.900	1.100	0.035	0.047



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