

#### **Description**

The AP220N06MP uses advanced APM-SGT technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

 $V_{DS} = 60V I_{D} = 220A$ 

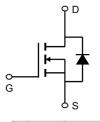
 $R_{DS(ON)} < 3m\Omega$  @  $V_{GS}$ =10V (Type: 2.4m $\Omega$ )

#### **Application**

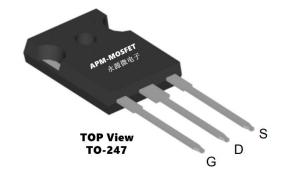
Battery protection

Load switch

Uninterruptible power supply







#### **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
AP220N06MP	TO-247-3L	AP220N06MP XXX YYYY	300

#### Absolute Maximum Ratings (T<sub>C</sub>=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	60	V
VGS	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current <sup>1,6</sup>	220	А
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current <sup>1,6</sup>	136	А
IDM	Pulsed Drain Current <sup>2</sup>	660	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	101	mJ
IAS	Avalanche Current	130	Α
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	168	W
TSTG	Storage Temperature Range	-55 to 150	℃
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-Ambient <sup>1</sup>	40	°C/W
R₀JC	Thermal Resistance Junction-Case <sup>1</sup>	1.5	°C/W







### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	60	67		V
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V			1	μΑ
IGSS	Gate-Body Leakage Current	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V			±100	nA
VGS(th)	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250µA	2.0	2.6	4.0	V
RDS(ON)	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> =20A		2.4	3.0	mΩ
		V <sub>GS</sub> = 6V, I <sub>D</sub> =15A		4.2	5.0	
Ciss	Input Capacitance			5950		
Coss	Output Capacitance	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V,f=100KHZ		1250		pF
Crss	Reverse Transfer Capacitance			85		
Qg	Total Gate Charge			93		0
Qgs	Gate-Source Charge	V <sub>GS</sub> =10V,V <sub>DS</sub> =50V,I <sub>D</sub> =50A		17		nC
$Q_{gd}$	Gate-Drain Charge			14		
Qrr	Reverse Recovery Chrage	I <sub>F</sub> =25A, di/dt=100A/us		73		
t <sub>rr</sub>	Reverse Recovery Time	1F-25A, di/dt-100A/d5		68		
td(on)	Turn-on Delay Time			22.5		ns
t <sub>r</sub>	Turn-on Rise Time	V <sub>GS</sub> =10V,V <sub>DD</sub> =30V,I <sub>D</sub> =25A		6.7		
td(off)	Turn-off Delay Time	R <sub>GEN</sub> =2Ω		80.3		
t <sub>f</sub>	Turn-off fall Time			26.9		
V <sub>SD</sub>	Diode Forward Voltage	I=204 \/==0\/			1.2	V
Is	Maximum Body-Diode Continuous Current	Is=20A,V <sub>GS</sub> =0V			200	Α

#### Note:

- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- $2_{\times}$  The data tested by pulsed , pulse width  $\leqq 300 us$  , duty cycle  $\leqq 2\%$
- 3 The EAS data shows Max. rating . The test condition is VDD=48V,VGS=10V, L=0.1mH IAS=130A
- 4. The power dissipation is limited by 150°C junction temperature
- 5 The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation



#### **Typical Characteristics**

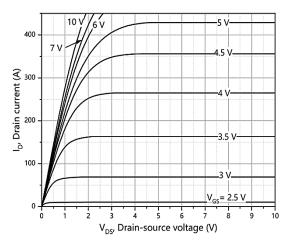


Figure 1. Typ. output characteristics

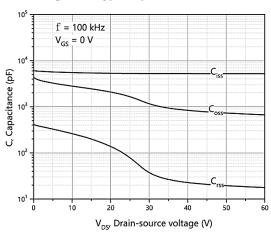


Figure 3. Typ. capacitances

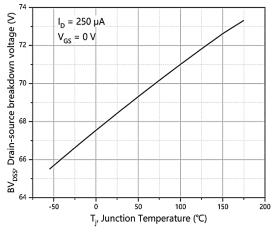


Figure 5. Drain-source breakdown voltage

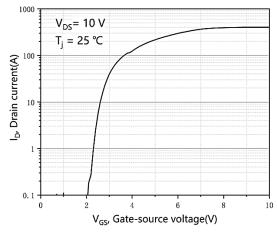


Figure 2. Typ. transfer characteristics

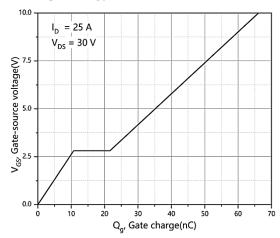


Figure 4. Typ. gate charge

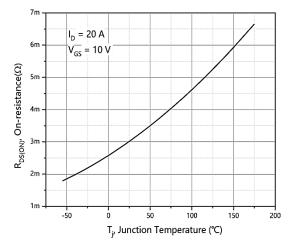


Figure 6. Drain-source on-state resistance





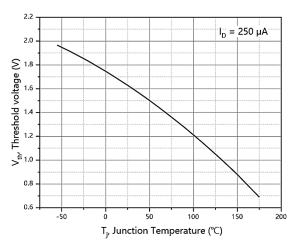


Figure 7. Threshold voltage

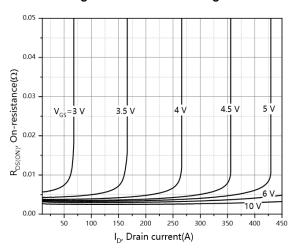


Figure 9. Drain-source on-state resistance

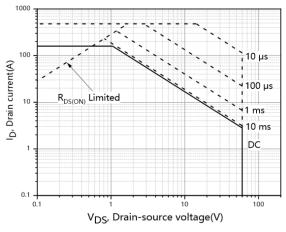


Figure 11. Safe operation area T<sub>C</sub>=25 °C

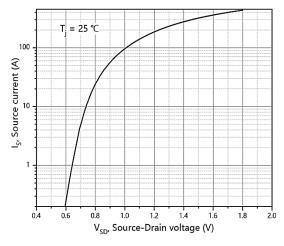


Figure 8. Forward characteristic of body diode

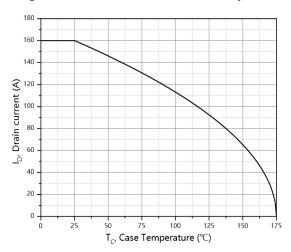


Figure 10. Drain current

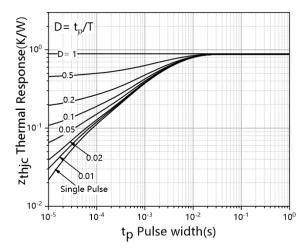
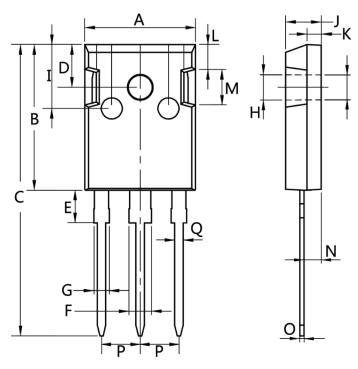


Figure 12. Max. transient thermal impedance



# Package Mechanical Data-TO-247-3L



Dim.	Min.	Max.
А	15.0	16. 0
В	20.0	21.0
С	41.0	42.0
D	5.0	6.0
E	4.0	5.0
F	2.5	3.5
G	1.75	2.5
Н	3.0	3.5
I	8.0	10.0
J	4.9	5.1
K	1.9	2.1
L	3.5	4.0
M	4.75	5.25
N	2.0	3.0
0	0.55	0.75
Р	Тур 5.08	
Q	1.2	1.3



# AP220N06MP

### **60V N-Channel Enhancement Mode MOSFET**

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# **AP220N06MP**

## **60V N-Channel Enhancement Mode MOSFET**

Edition	Date	Change
Rve1.0	2021/1/31	Initial release

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