

#### **Description**

The AP60P02D uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

 $V_{DS} = -20V I_{D} = -60A$ 

 $R_{DS(ON)} < 12m\Omega$  @  $V_{GS}$ =-4.5V (Type:  $8m\Omega$ )

### **Application**

Battery protection

Load switch

Uninterruptible power supply



**Package Marking and Ordering Information** 

Product ID	Pack	Marking	Qty(PCS)
AP60P02D	TO-252-3L	AP60P02D XXX YYYY	2500

#### Absolute Maximum Ratings (T<sub>c</sub>=25 ℃ unless otherwise noted)

Symbol	Parameter	Rating	Units		
VDS	Drain-Source Voltage	-20	V		
VGS	Gate-Source Voltage	±12	V		
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup>	-60	А		
I <sub>D</sub> @T <sub>C</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup>	-48	А		
IDM	Pulsed Drain Current <sup>2</sup>	-200	А		
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>3</sup>	60	W		
P <sub>D</sub> @T <sub>C</sub> =70 °C	Total Power Dissipation <sup>3</sup>	48	W		
TSTG	Storage Temperature Range	-55 to 150	$^{\circ}$		
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$		
R₀JA	Thermal Resistance Junction-Ambient <sup>1</sup>	75	°C/W		
R₀JA	Thermal Resistance Junction-Ambient ¹ (t ≤10s)	40	°C/W		
R₀JC	Thermal Resistance Junction-Case <sup>1</sup>	3.6	°C/W		



### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-20	-22		V
∆BVDSS/∆TJ	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA		-0.012		V/°C
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-10A		8	12	mΩ
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-2.5V , I <sub>D</sub> =-5.0A		11	116	11177
VGS(th)	Gate Threshold Voltage	\/aa=\/aa  a = 250uA	-0.4	0.65	-1.0	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=-250uA$		2.94		mV/°C
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =-20V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
IGSS	Gate-Source Leakage Current	V <sub>GS</sub> =±12V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =-10V , I <sub>D</sub> =-10A	12			S
Qg	Total Gate Charge (-4.5V)			63		
Qgs	Gate-Source Charge	V <sub>DS</sub> =-10V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =- 10A		9.1		nC
Qgd	Gate-Drain Charge	10/1		13		
Td(on)	Turn-On Delay Time			10		
Tr	Rise Time	V <sub>DD</sub> =-10V , V <sub>GS</sub> =-4.5V ,		15		
Td(off)	Turn-Off Delay Time	R <sub>G</sub> =6.0Ω, I <sub>D</sub> =-1A		110		ns
T <sub>f</sub>	Fall Time			70		
Ciss	Input Capacitance			1600		
Coss	Output Capacitance V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz			350		pF
Crss	Reverse Transfer Capacitance			300		
IS	Continuous Source Current <sup>1,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-50	Α
VSD	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-15A , T <sub>J</sub> =25°C			-1.2	V

#### Note:

- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width  $\leq 300 \text{us}$  , duty cycle  $\leq 2\%$
- 3、The EAS data shows Max. rating . The test condition is VDD=-16V,VGS=-10V,L=0.1mH,IAS=12A
- 4、The power dissipation is limited by 150℃ junction temperature
- 5. The data is theoretically the same as I D and I DM, in real applications, should be limited by total power dissipation.



## **Typical Characteristics**

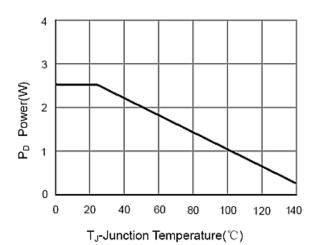


Figure 1: Power Dissipation

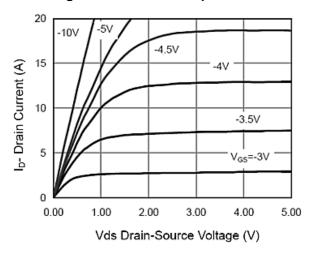


Figure 3: Output Characteristics

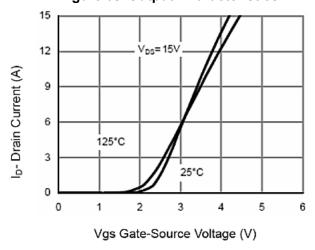


Figure 5: Transfer Characteristics

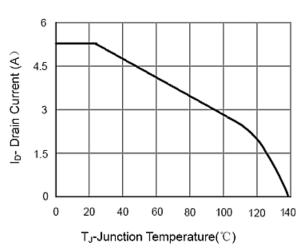


Figure 2: Drain Current

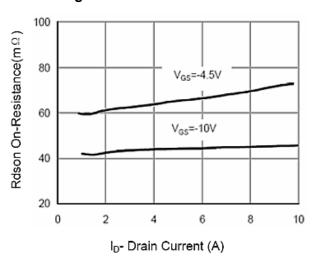


Figure 4: Drain-Source On-Resistance

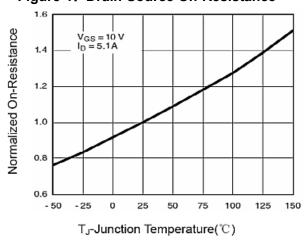


Figure 6: Drain-Source On-Resistance





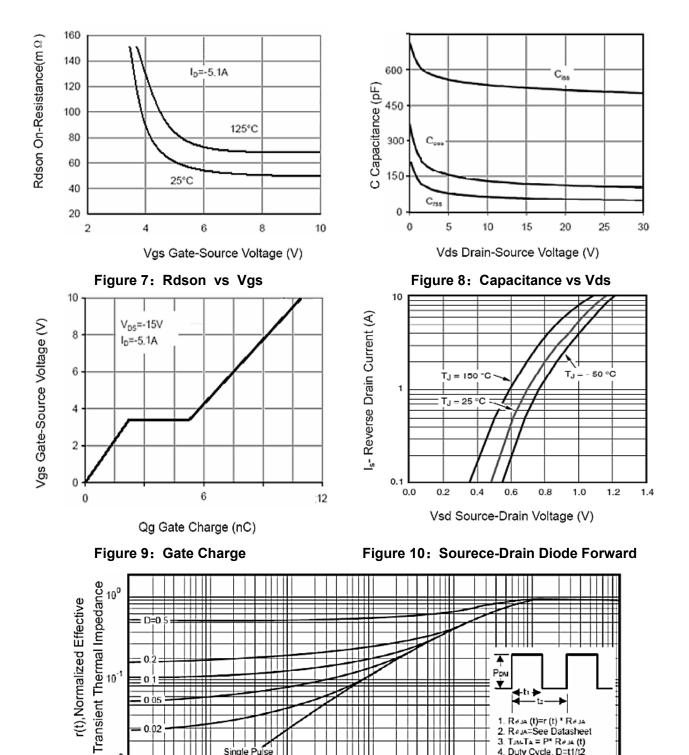


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

Square Wave Pluse Duration(sec)

Single Pulse

10<sup>-3</sup>

10<sup>-2</sup> 10<sup>-4</sup>

10<sup>2</sup>

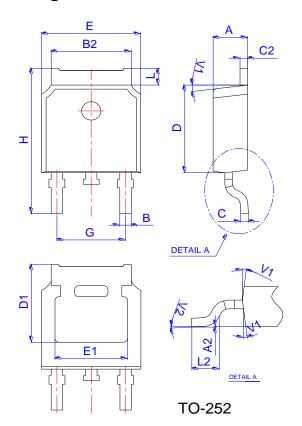
1. Reja (t)=r (t) \* Reja 2. Reja=See Datasheet 3. TJMTa = P\* Reja (t) 4. Duty Cycle, D=t1/t2

10<sup>1</sup>

10<sup>0</sup>

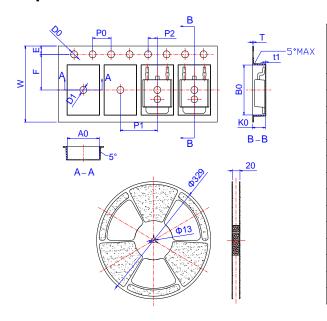


# Package Mechanical Data: TO-252-3L



	Dimensions						
Ref.	Millimeters		rs	Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	2.10		2.50	0.083		0.098	
A2	0		0.10	0		0.004	
В	0.66		0.86	0.026		0.034	
B2	5.18		5.48	0.202		0.216	
С	0.40		0.60	0.016		0.024	
C2	0.44		0.58	0.017		0.023	
D	5.90		6.30	0.232		0.248	
D1	5.30REF			0.209REF			
E	6.40		6.80	0.252		0.268	
E1	4.63			0.182			
G	4.47		4.67	0.176		0.184	
Н	9.50		10.70	0.374		0.421	
L	1.09		1.21	0.043		0.048	
L2	1.35		1.65	0.053		0.065	
V1		7°			7°		
V2	0°		6°	0°		6°	

## **Reel Spectification-TO-252**



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
Е	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
T	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583



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Edition	Date	Change		
Rve1.0	2021/1/31	Initial release		

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