

-60V P-Channel Enhancement Mode MOSFET

Description

The AP3P06MI uses advanced trench technology

to provide excellent $R_{\text{DS}(\text{ON})},$ low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

General Features

 $V_{DS} = -60V I_D = -3.8A$

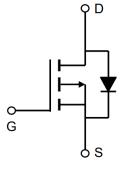
 $R_{\text{DS(ON)}} < -150 \text{m}\Omega \text{ (@V_{GS}=-10V (Type: 125 \text{m}\Omega))}$

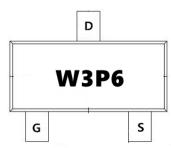
Application

Battery protection

Load switch

Uninterruptible power supply







Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP3P06MI	SOT23-3L	W3P6	3000

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units
Vds	Drain-Source Voltage	-60	V
Vgs	Gate-Source Voltage	±20	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-3.8	А
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -10V ¹	-2.4	A
Ідм	Pulsed Drain Current ²	-12	A
P _D @T _A =25°C	Total Power Dissipation ³	1.5	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R ₀ JA	Thermal Resistance Junction-Ambient ¹	125	°C/W
Rejc	Thermal Resistance Junction-Case ¹	80	°C/W

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Electrical Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V_{GS} =0V , I _D =-250uA	-60	-67		V	
$\triangle BV$ DSS/ $\triangle T_J$	$DSS/ \triangle T_J$ BV _{DSS} Temperature Coefficient Reference to 25°C , I _D =-1			-0.021		V/℃	
_	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-1.5A		125	150	mΩ	
Rds(on)		V _{GS} =-4.5V , I _D =-1A		158	200		
VGS(th)	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.0	1.7	-2.5	V	
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	VGS-VDS, ID2300A		4.08		mV/℃	
IDSS	Drain-Source Leakage Current	$V_{\text{DS}}\text{=-48V}$, $V_{\text{GS}}\text{=}0\text{V}$, $T_{\text{J}}\text{=}25^\circ\!\mathbb{C}$			1	uA	
IDSS	Drain-Source Leakage Current	$V_{\text{DS}}\text{=-48V}$, $V_{\text{GS}}\text{=}0\text{V}$, $T_{\text{J}}\text{=}55^\circ\!\mathbb{C}$			5	uA	
lgss	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA	
gfs	Forward Transconductance	V _{DS} =-5V , I _D =-1.5A		5.9		S	
Qg	Total Gate Charge (-4.5V)			4.6			
Qgs	Gate-Source Charge	V _{DS} =-20V , V _{GS} =-4.5V , I _D =- 1.5A		1.4		nC	
Q_{gd}	Gate-Drain Charge			1.62			
Td(on)	Turn-On Delay Time			17.4			
Tr	Rise Time	V _{DS} =-15V , V _{GS} =-10V , R _G =3.3 Ω,		5.4		20	
Td(off)	Turn-Off Delay Time	I _D =-1A		37.2		ns	
T _f	Fall Time			2.4			
Ciss	Input Capacitance			531			
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz		59		pF	
Crss	Reverse Transfer Capacitance			38			
ls	Continuous Source Current ^{1,4}				-1.7	А	
lsм	Pulsed Source Current ^{2,4}	$V_G=V_D=0V$, Force Current			-7	А	
Vsd	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , TJ=25℃			-1.2	V	

Note :

1、The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%

3. The power dissipation is limited by 150 $^\circ\!\!\!\mathrm{C}$ junction temperature

4. The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.

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P-Channel Typical Characteristics

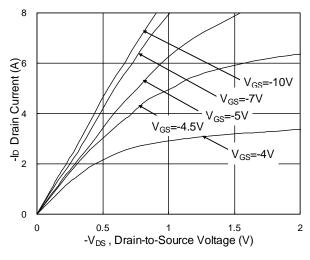


Fig.1 Typical Output Characteristics

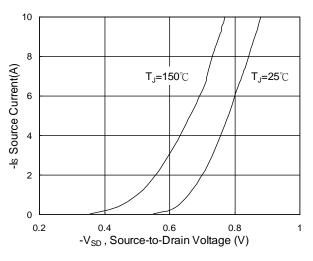


Fig.3 Forward Characteristics Of Reverse

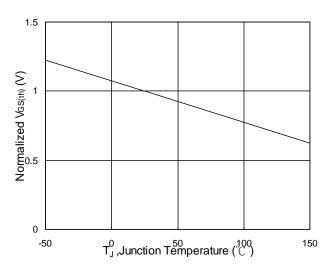


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

AP3P06MI RVE1.0

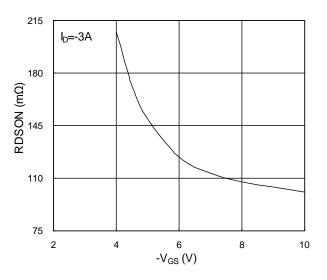


Fig.2 On-Resistance vs. G-S Voltage

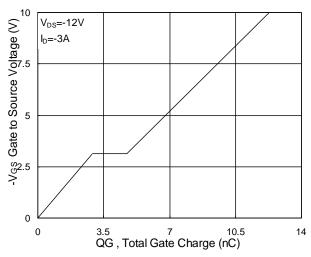
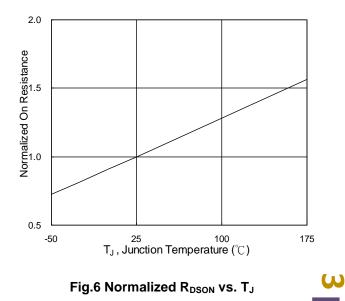


Fig.4 Gate-Charge Characteristics



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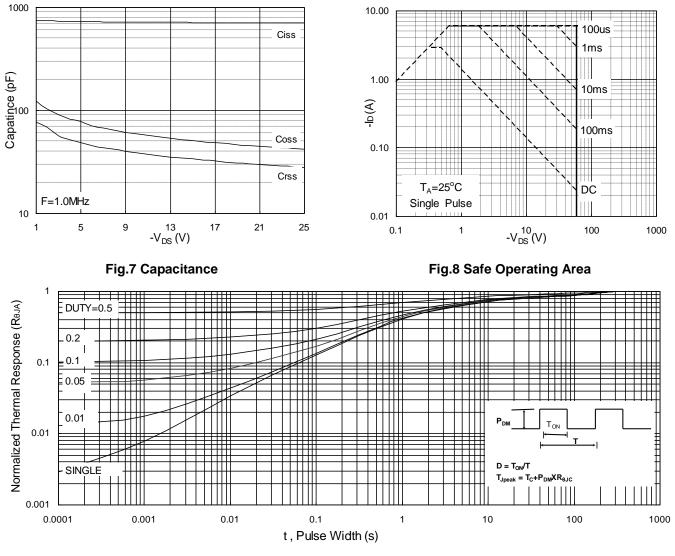


Fig.9 Normalized Maximum Transient Thermal Impedance

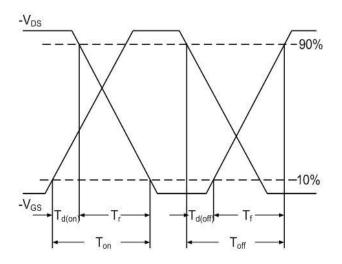
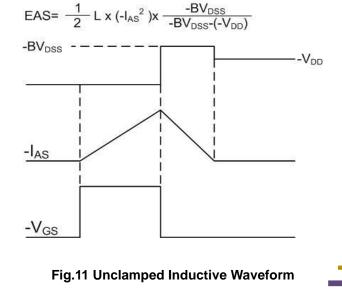


Fig.10 Switching Time Waveform



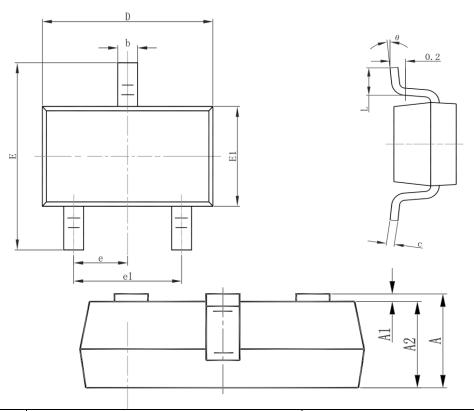
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Package Mechanical Data-SOT23-3



Sumbol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
А	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

С

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Edition	Date	Change
RVE1.0	2021/3/1	Initial release

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