

60V N+N-Channel Enhancement Mode MOSFET

Description

The AP15H06S uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = 60V$ $I_D = 15A$

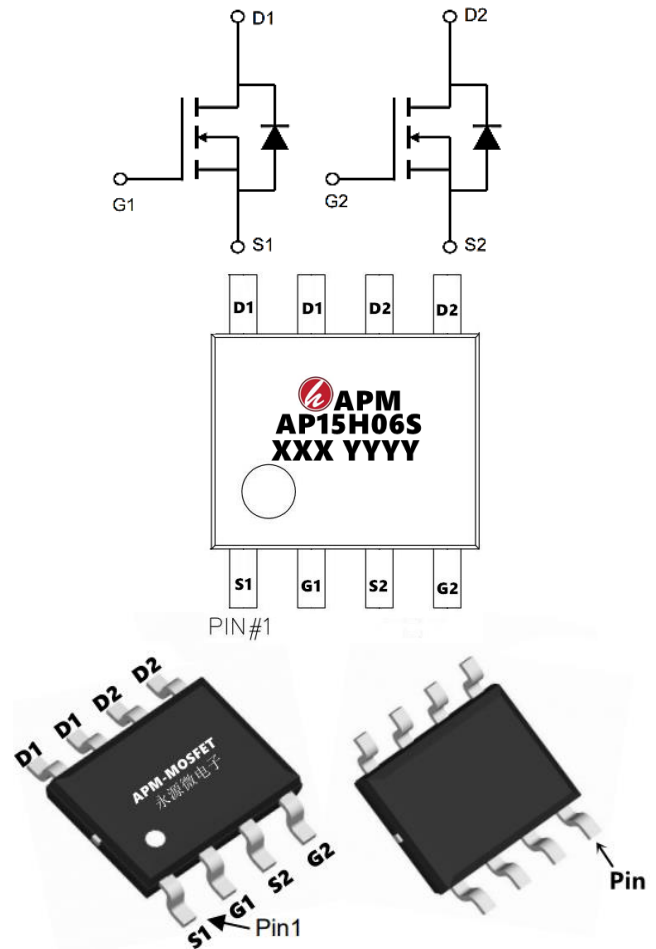
$R_{DS(ON)} < 18m\Omega$ @ $V_{GS}=10V$ (Type: 11m Ω)

Application

Battery protection

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP15H06S	SOP-8L	AP15H06S XXX YYYY	3000

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	15	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	9.2	A
IDM	Pulsed Drain Current ²	45	A
EAS	Single Pulse Avalanche Energy ³	64	mJ
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation ⁴	3.6	W
TSTG	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C
R _{θJA}	Thermal Resistance Junction-Ambient ¹	85	°C/W

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Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	65	---	V
$\Delta BVDSS/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.057	---	$\text{V}/^{\circ}\text{C}$
RDS(ON)	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=12A$	---	11	18	m Ω
		$V_{GS}=4.5V, I_D=10A$	---	15	20	
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.6	2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-5.68	---	$\text{mV}/^{\circ}\text{C}$
IDSS	Drain-Source Leakage Current	$V_{DS}=48V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=48V, V_{GS}=0V, T_J=55^{\circ}\text{C}$	---	---	5	
IGSS	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
gfs	Forward Transconductance	$V_{DS}=5V, I_D=15A$	---	45	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	1.7	---	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=48V, V_{GS}=4.5V, I_D=15A$	---	19.3	---	nC
Q_{gs}	Gate-Source Charge		---	7.1	---	
Q_{gd}	Gate-Drain Charge		---	7.6	---	
$T_d(on)$	Turn-On Delay Time		---	7.2	---	
T_r	Rise Time	$V_{DD}=30V, V_{GS}=10V, R_G=3.3\Omega, I_D=15A$	---	50	---	ns
$T_d(off)$	Turn-Off Delay Time		---	36.4	---	
T_f	Fall Time		---	7.6	---	
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$	---	2423	---	pF
C_{oss}	Output Capacitance		---	145	---	
C_{rss}	Reverse Transfer Capacitance		---	97	---	
I_S	Continuous Source Current ^{1,5}		$V_G=V_D=0V, \text{Force Current}$	---	---	
ISM	Pulsed Source Current ^{2,5}	---		---	80	A
VSD	Diode Forward Voltage ²	$V_{GS}=0V, I_S=A, T_J=25^{\circ}\text{C}$	---	---	1	V
t_{rr}	Reverse Recovery Time	$I_F=15A, di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	16.3	---	nS
Q_{rr}	Reverse Recovery Charge		---	11	---	nC

Notes:

- 1、The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width .The EAS data shows Max. rating .
- 3、The power dissipation is limited by 175 $^{\circ}\text{C}$ junction temperature
- 4、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics

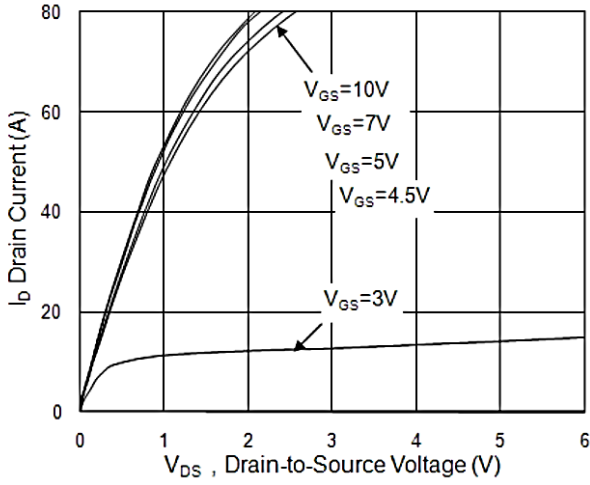


Fig.1 Typical Output Characteristics

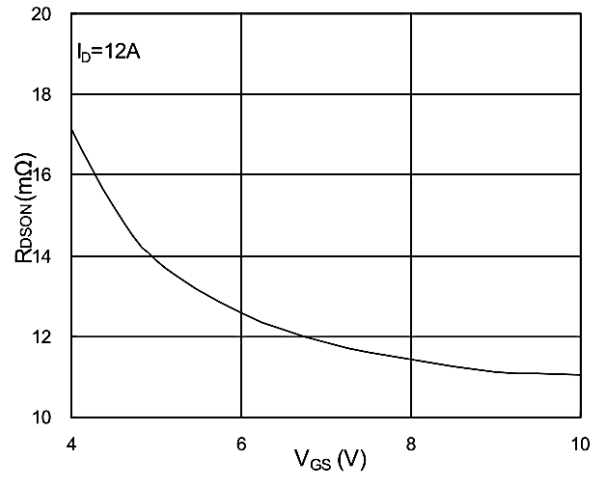


Fig.2 On-Resistance v.s Gate-Source

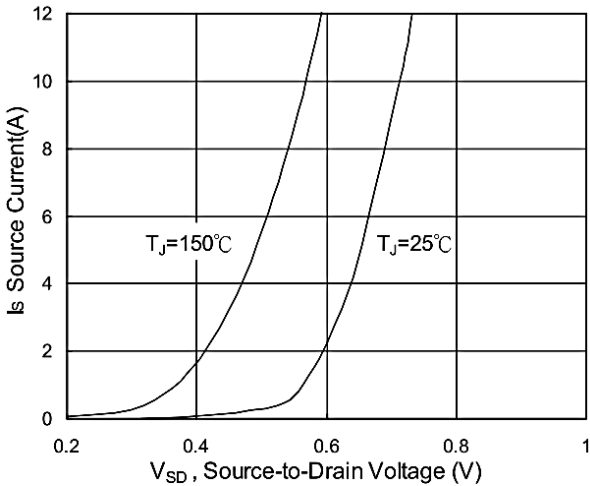


Fig.3 Forward Characteristics of Reverse

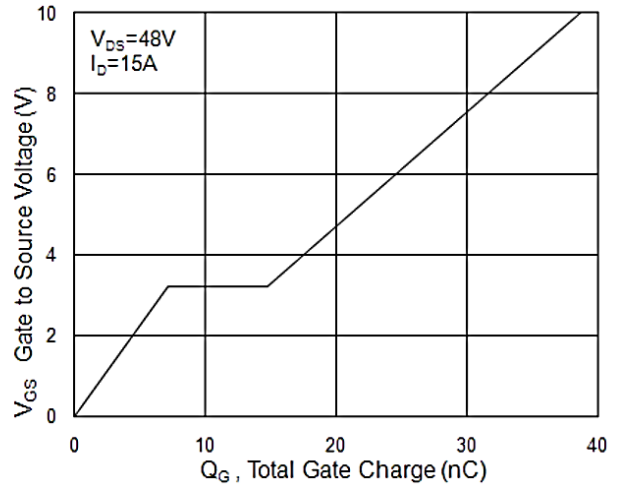


Fig.4 Gate-Charge Characteristics

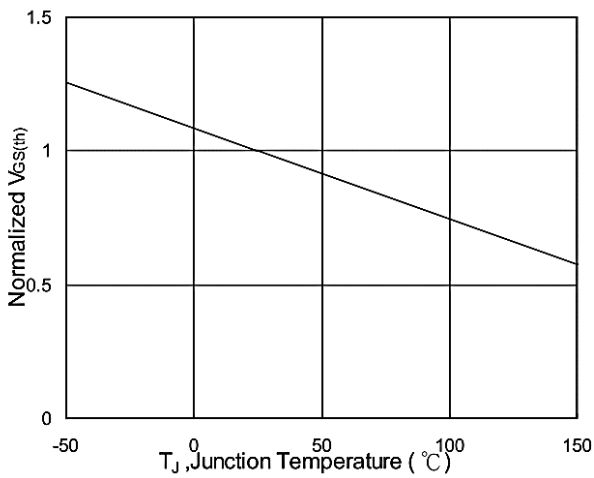


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

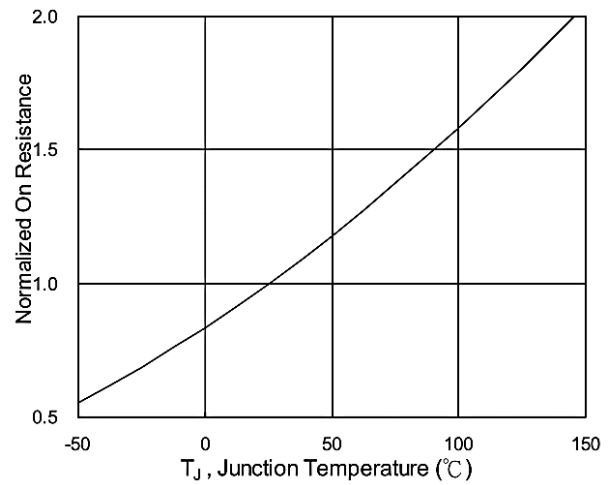


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

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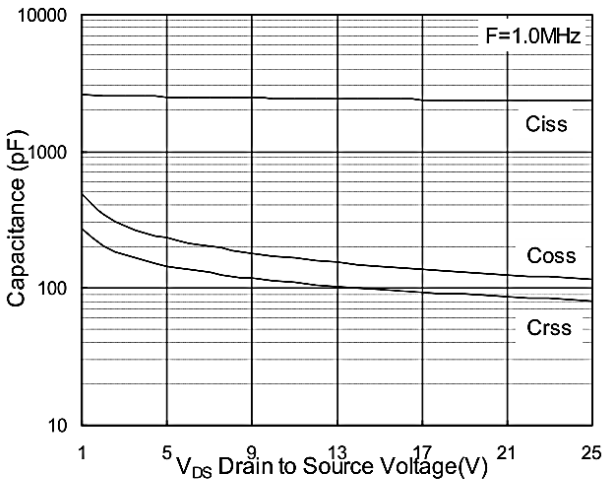


Fig.7 Capacitance

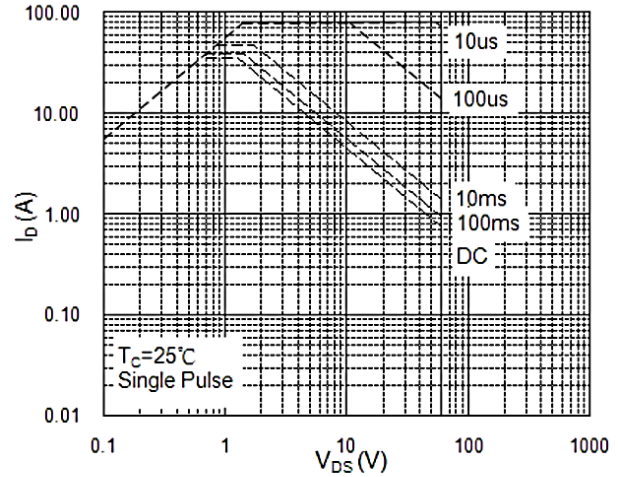


Fig.8 Safe Operating Area

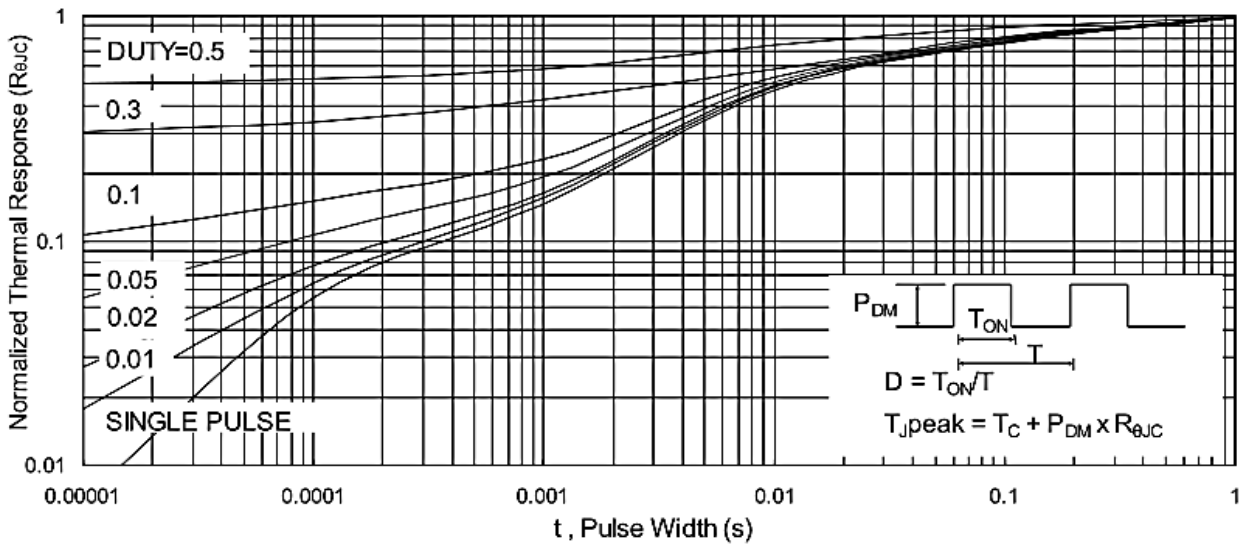


Fig.9 Normalized Maximum Transient Thermal Impedance

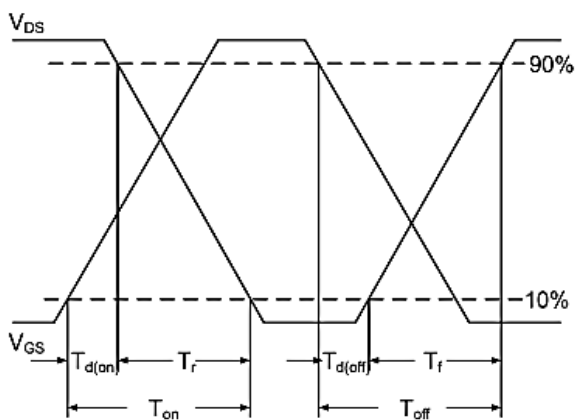


Fig.10 Switching Time Waveform

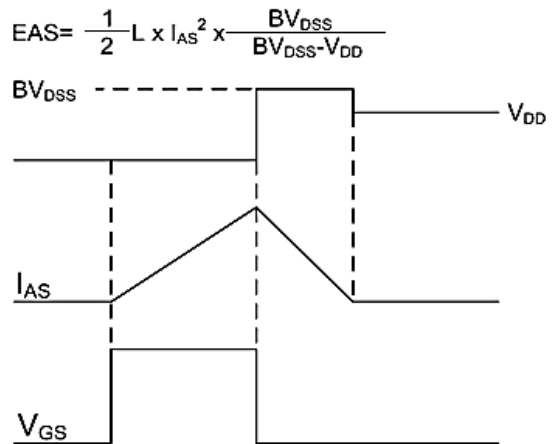
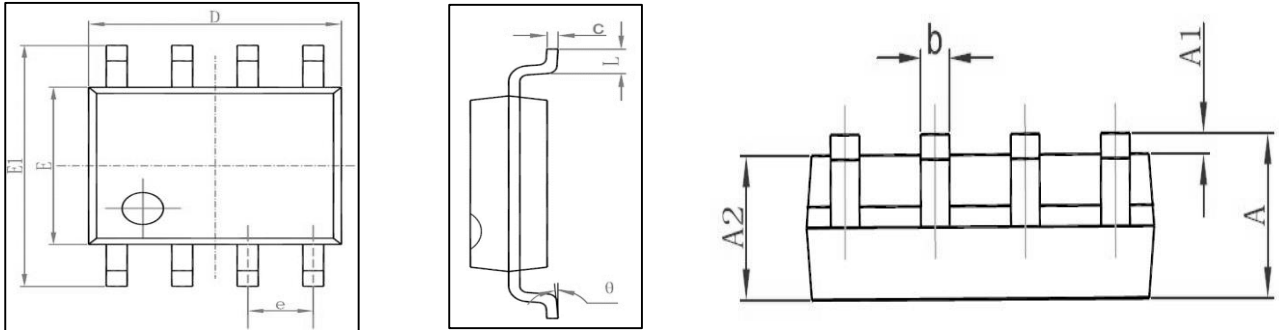
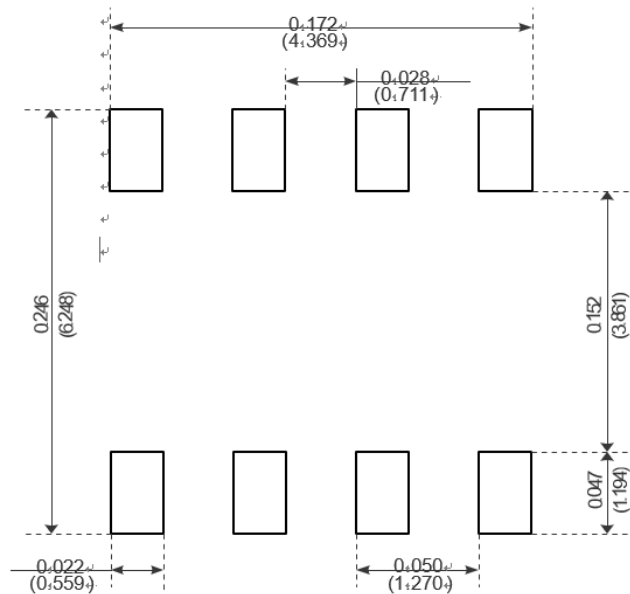


Fig.11 Unclamped Inductive Switching Waveform

Package Mechanical Data-SOP-8L-DX-Double



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Recommended Minimum Pads

60V N+N-Channel Enhancement Mode MOSFET**Attention**

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Edition	Date	Change
Rve1.0	2020/12/1	Initial release

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