

-60V P-Channel Enhancement Mode MOSFET

Description

The AP10N06MSI uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a Battery protection

or in other Switching application.

General Features

 $V_{DS} = -60V I_{D} = -10A$

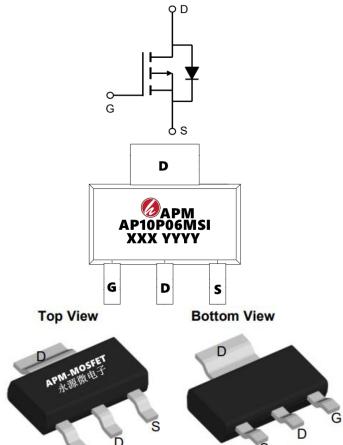
R_{DS(ON)} < 70mΩ @ V_{GS}=10V (Type: 55mΩ)

Application

Brushless motor

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)	
AP10P06MSI	SOT223-3L AP10P06MSI XXXX YYYY		3000	
bsolute Maximun	n Ratings (T _c =25 [°] C unless otherwise not	ed)		
Symbol	Parameter	Rating	Units	
Vds	Drain-Source Voltage	-60	V	
Vgs	Gate-Source Voltage	±20	V	
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-10	А	
I₀@T _A =70°C	Continuous Drain Current, V_{GS} @ -10V ¹	-3	А	
Ідм	Pulsed Drain Current ²	-30	А	
las	Avalanche Current	-26.6	А	
P _D @T _A =25°C	Total Power Dissipation ⁴	1.5	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
Reja	Thermal Resistance Junction-Ambient ¹	70	°C/W	
Rejc	Thermal Resistance Junction-Case ¹	36	°C/W	

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Electrical Characteristics (T_A=25°Cunless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-60			V	
$\triangle BVDSS / \triangle Tj$	BV _{DSS} Temperature Coefficient	Reference to 25℃ , I _D =-1mA		-0.03		V/° C	
RDS(ON)	Statia Drain Source On Desistance	V _{GS} =-10V , I _D =-12A		55	70	mΩ	
	Static Drain-Source On-Resistance	V _{GS} =-4.5V , I _D =-8A		64	105		
VGS(th)	Gate Threshold Voltage		-1.2	-1.5	-2.5	V	
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D = -250 uA$		4.56		mV/℃	
IDSS	Drain-Source Leakage Current	V _{DS} =-48V , V _{GS} =0V , TJ=25℃			1		
		V _{DS} =-48V , V _{GS} =0V , TJ=55℃			5	uA	
IGSS	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA	
gfs	Forward Transconductance	V _{DS} =-5V , I _D =-12A		15.4		S	
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		13.5		Ω	
Qg	Total Gate Charge (-4.5V)			9.86		nC	
Qgs	Gate-Source Charge	V _{DS} =-48V , V _{GS} =-4.5V , I _D =-10A		3.08			
Q _{gd}	Gate-Drain Charge			2.95			
Td(on)	Turn-On Delay Time			28.8			
Tr	Rise Time	V _{DD} =-15V , V _{GS} =-10V ,		19.8		ns	
Td(off)	Turn-Off Delay Time	R _G =3.3□, I _D =-1A		60.8			
T _f	Fall Time			7.2			
Ciss	Input Capacitance			1447			
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz		97.3		pF	
Crss	Reverse Transfer Capacitance			70			
ls	Continuous Source Current ^{1,5}				-18	А	
ISM	Pulsed Source Current ^{2,5}	$V_G=V_D=0V$, Force Current			-36	Α	
VSD	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25℃			-1.2	V	

Note :

 $1_{\mbox{\tiny V}}$ The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2. The data tested by pulsed , pulse width $\,\leq\,$ 300us , duty cycle $\,\leq\,$ 2%

3、The EAS data shows Max. rating . The test condition is VDD=-25V,VGS=-10V,L=0.1mH,IAS=-26.6A

4. The power dissipation is limited by 150 $^\circ\!\!\mathbb{C}$ junction temperature

5. The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.



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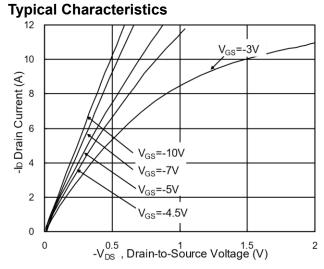


Fig.1 Typical Output Characteristics

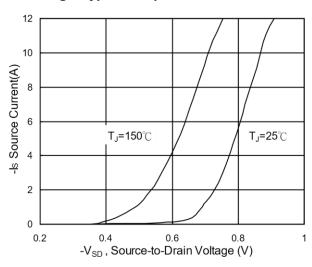
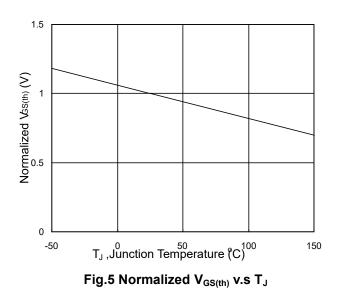


Fig.3 Forward Characteristics of Reverse



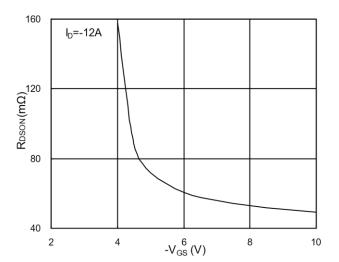


Fig.2 On-Resistance v.s Gate-Source

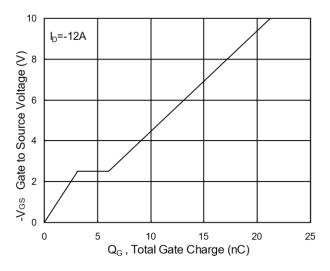
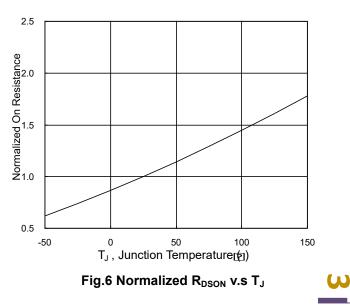


Fig.4 Gate-Charge Characteristics





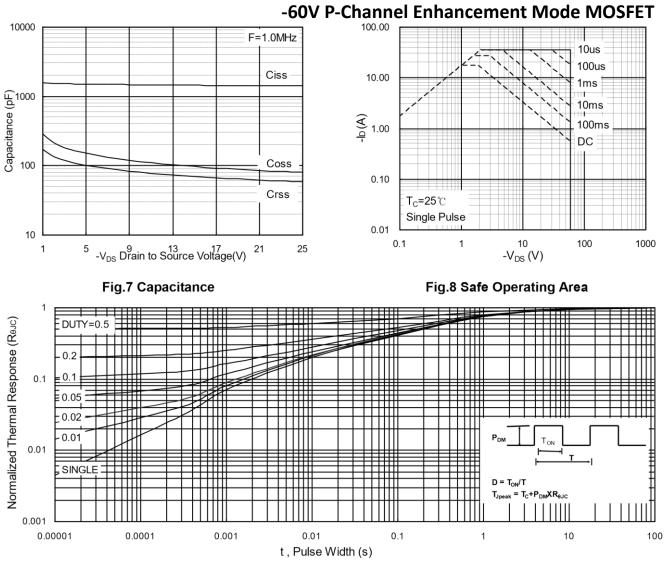


Fig.9 Normalized Maximum Transient Thermal Impedance

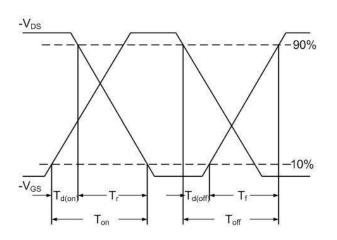
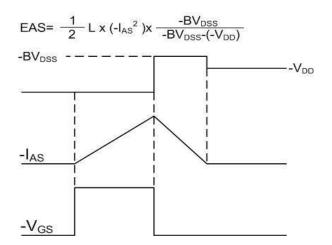


Fig.10 Switching Time Waveform

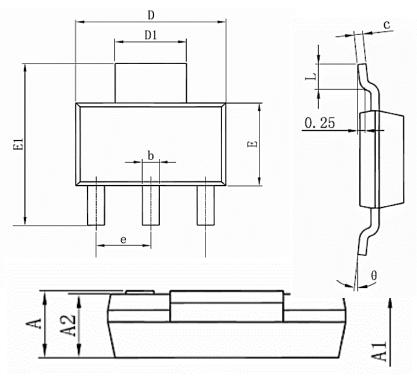






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Package Mechanical Data:SOT223-3L



Sympol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
A	1.52	1.8	0.06	0.049
A1	0.000	0.100	0.000	0.004
A2	1.5	1.7	0.059	0.045
b	0.66	0.82	0.026	0.032
с	0.25	0.35	0.010	0.014
D	6.2	6.4	0.244	0.252
D1	2.9	3.1	0.114	0.122
E	3.3	3.7	0.130	0.146
E1	6.83	7.07	0.269	0.278
е	2.300	(BSC)	0.037	7(BSC)
e1	4.500	4.700	0.177	0.185
L	0.900	1.15	0.035	0.045
θ	0°	10°	0°	10°



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Edition	Date	Change
Rve1.0	2021/11/31	Initial release

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