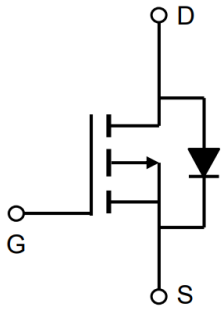
AP01P10I

-100V P-Channel Enhancement Mode MOSFET

Description

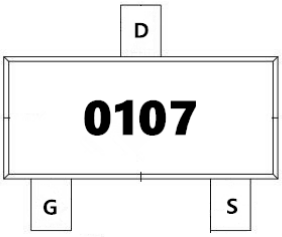
The AP01P10I uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



General Features

V_{DS} = −100V I_D =−0.9 A

$R_{DS(ON)}$ < 0.65Ω @ V_{GS}=10V



Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP01P10I	SOT-23	0107	3000

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_A=25℃	Continuous Drain Current, V_{GS} @ -10V[1]	-0.9	A
I_D@T_A=70℃	Continuous Drain Current, V_{GS} @ -10V[1]	-0.7	A
I_{DM}	Pulsed Drain Current[2]	-1.8	A
P_D@T_A=25℃	Total Power Dissipation[3]	1	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃
$R_{\theta JA}$	Thermal Resistance Junction-ambient [1]	125	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	80	℃/W

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Electrical Characteristics (T_J=25℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-100	---	---	V
△BVDSS/△TJ	BVDSS Temperature Coefficient	Reference to 25℃ , I_D=-1mA	---	-0.0624	---	V/℃
RDS(ON)	Static Drain-Source On-Resistance[2]	V_{GS}=-10V , I_D=-0.8A	---	0.52	0.65	Ω
		V_{GS}=-4.5V , I_D=-0.4A	---	0.56	0.7	
VGS(th)	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.0	-1.5	-2.5	V
△$V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	4.5	---	mV/℃
IDSS	Drain-Source Leakage Current	V_{DS}=-80V , V_{GS}=0V , T_J=25℃	---	---	10	uA
		V_{DS}=-80V , V_{GS}=0V , T_J=55℃	---	---	100	
IGSS	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=-5V , I_D=-0.8A	---	3	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	16	32	Ω
Q_g	Total Gate Charge (-4.5V)	V_{DS}=-15V , V_{GS}=-4.5V , I_D=-0.5A	---	4.5	---	nC
Qgs	Gate-Source Charge		---	1.14	---	
Qgd	Gate-Drain Charge		---	1.5	---	
Td(on)	Turn-On Delay Time	V_{DD}=-50V , V_{GS}=-10V , R_G=3.3□ I_D=-0.5A	---	13.6	---	ns
T_r	Rise Time		---	6.8	---	
Td(off)	Turn-Off Delay Time		---	34	---	
T_f	Fall Time		---	3	---	
Ciss	Input Capacitance	V_{DS}=-15V , V_{GS}=0V , f=1MHz	---	553	---	pF
Coss	Output Capacitance		---	29	---	
Crss	Reverse Transfer Capacitance		---	20	---	
IS	Continuous Source Current[1,4]	V_G=V_D=0V , Force Current	---	---	-0.9	A
ISM	Pulsed Source Current[2,4]		---	---	-1.8	A
VSD	Diode Forward Voltage[2]	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.2	V

Note :

1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%

3.The power dissipation is limited by 150℃ junction temperature

4 .The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

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Typical Characteristics

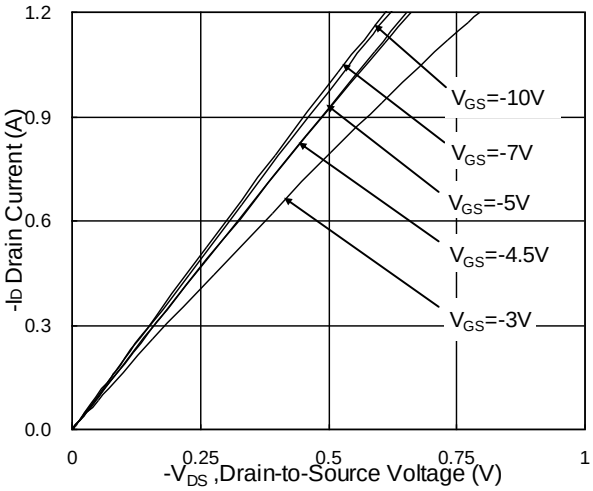

Fig.1 Typical Output Characteristics

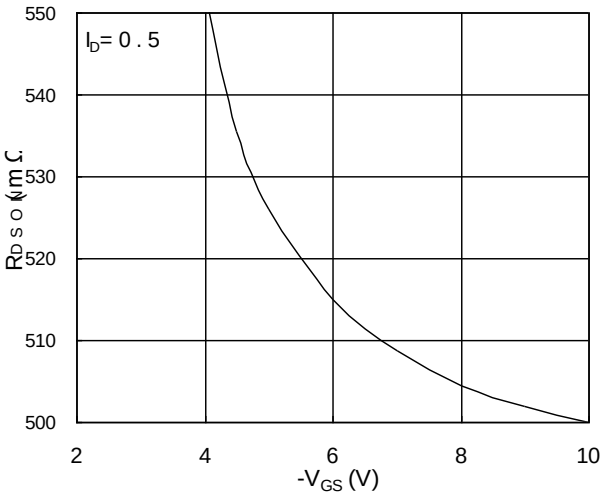

Fig.2 On-Resistance vs. Gate-Source

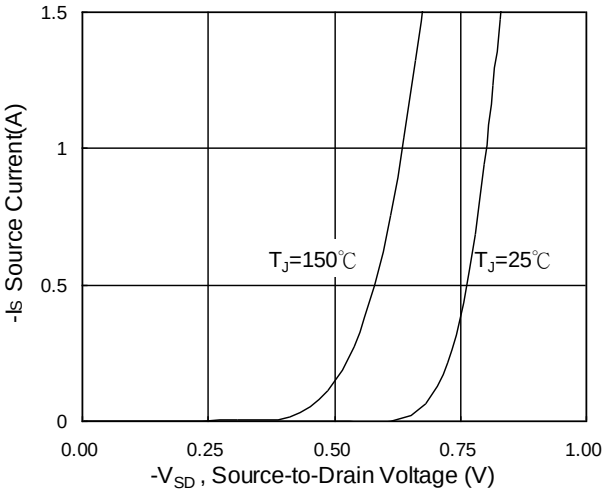

Fig.3 Forward Characteristics Of Reverse

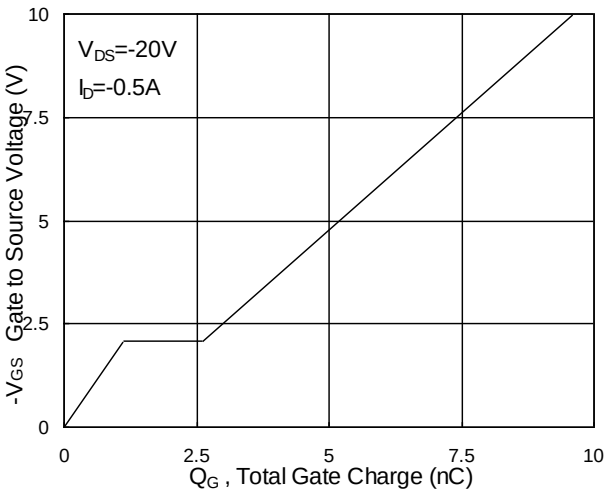

Fig.4 Gate-Charge Characteristics

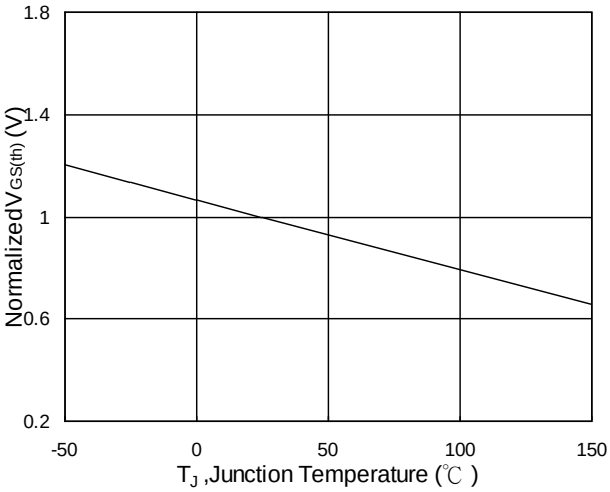

Fig.5 Normalized $V_{GS(th)}$ vs. T_J

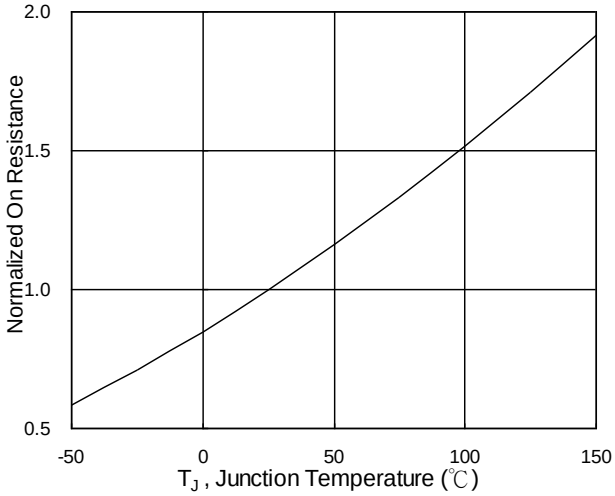

Fig.6 Normalized R_{DSON} vs. T_J

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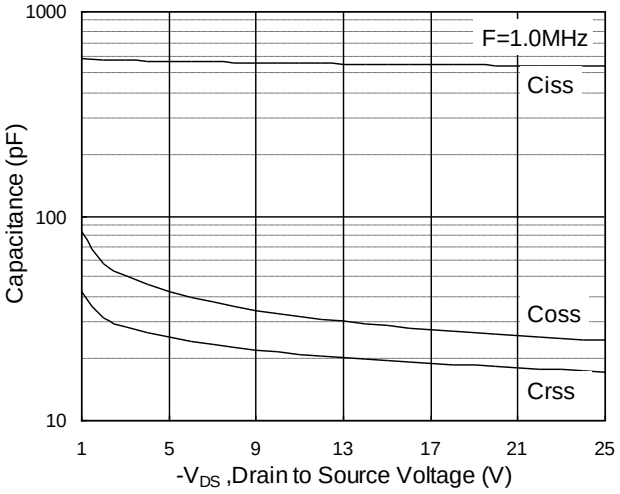


Fig.7 Capacitance

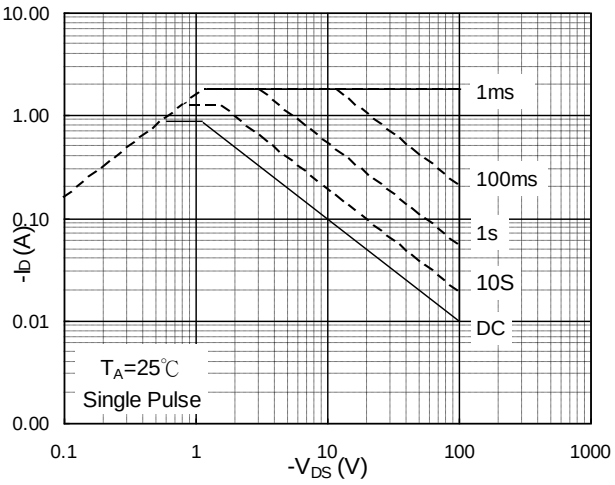


Fig.8 Safe Operating Area

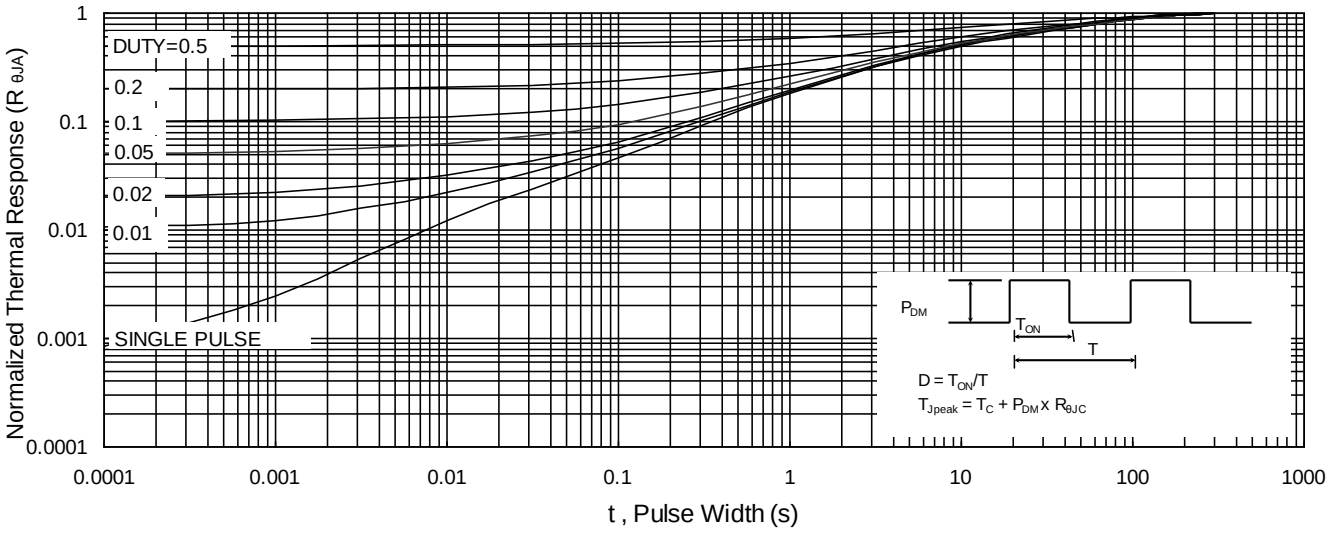


Fig.9 Normalized Maximum Transient Thermal Impedance

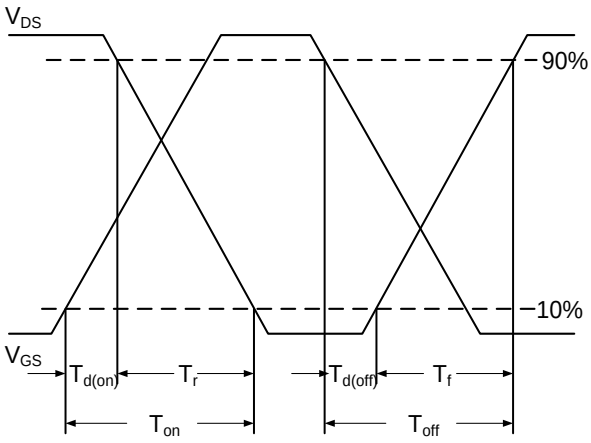


Fig.10 Switching Time Waveform

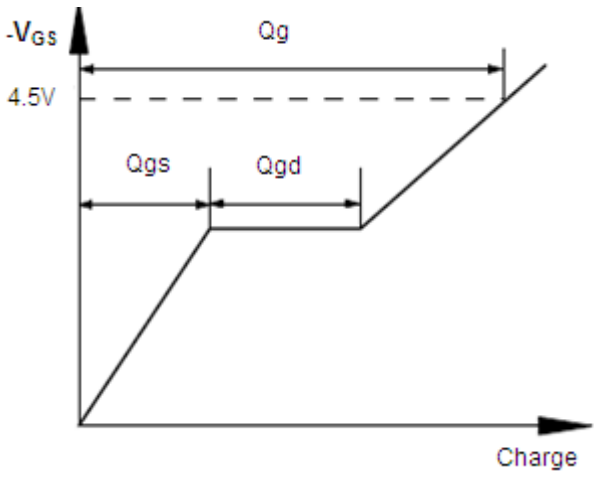
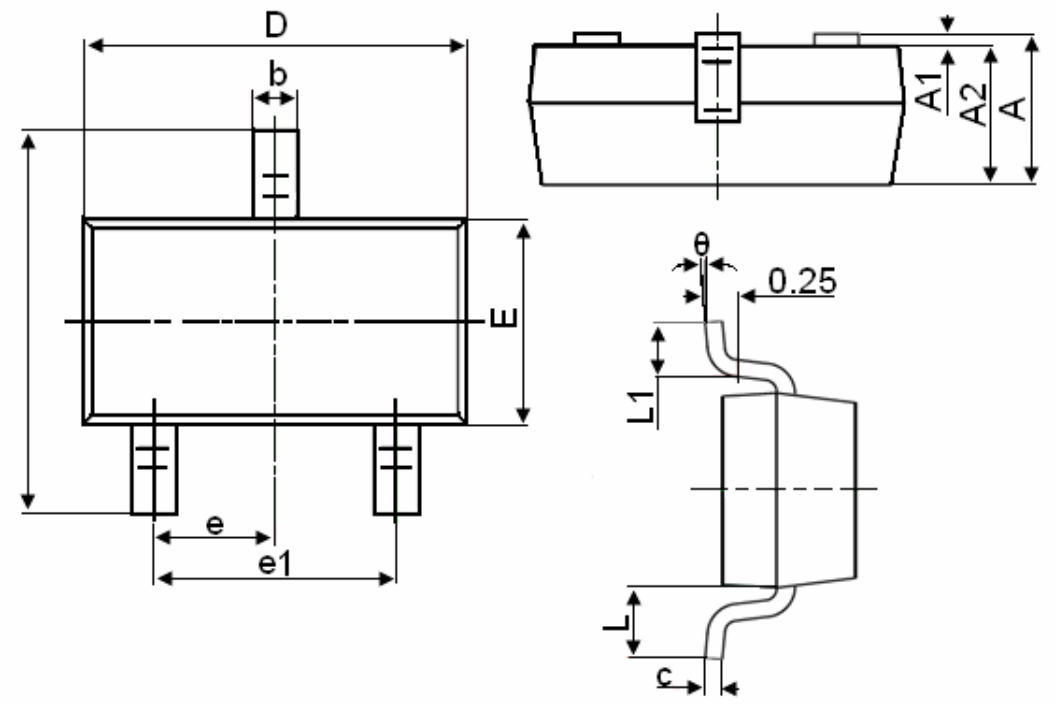


Fig.11 Gate Charge Waveform

SOT-23 Package Information



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

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