

Low Noise Bottom Port MEMS Microphone

DESCRIPTION

The ZTS6856SE is a high quality, low cost, low power analog output bottom-ported omni-directional MEMS microphone. ZTS6856SE consists of a MEMS microphone element and an preamplifier. ZTS6856SE has a high SNR and flat wideband frequency response, resulting in natural sound with high intelligibility. Extra EMI filter for RF noise attenuation is built inside. Due to the built-in filter, ZTS6856SE shows high immunity to EMI.

The ZTS6856SE is available in a thin 2.75mm \times 1.85mm \times 0.9mm surface-mount package. It is reflow solder compatible with no sensitivity degradation. The ZTS6856SE is Halogen and Lead free.

APPLICATIONS

- Mobile telephones
- Smart phones
- PDAs
- Digital video cameras
- Portable media devices with audio input

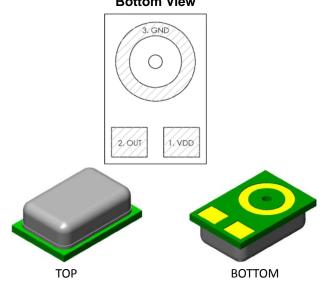
ORDERING INFORMATION

PART	RoHS	Ship, Quantity
ZTS6856SE	Yes	Tape and Reel, 5.2K

FEATURES

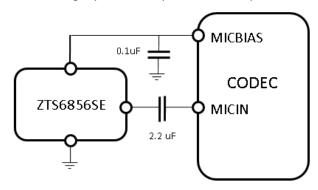
- Small package
- Flat Frequency Response SNR of 65 dBA
- Low Current
- MaxRF protection
- Ultra-Stable Performance
- Standard SMD Reflow
- Omni-directional

Pins Configuration and Description Bottom View



Typical Applications

The ZTS6856SE output can be connected to a codec microphone input or to a high input impedance gain stage. A dc-blocking capacitor is required at the output of the microphone.



Connect to Audio CODEC

Note:

All Ground pins must be connected to ground.

Capacitors near the microphone should not contain Class 2 dielectrics.

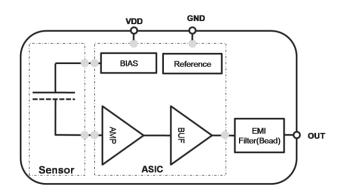


Absolute Maximum Ratings

V _{DD} to Ground	0.5V to +5V
OUT to Ground	0.3V to V _{DD} +0.3V
Input Current to Any Pin	± 5 mA
Temperature Range	40°C to +125°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Functional Block Diagram



Electro-Static Discharge Sensitivity

This integrated circuit can be damaged by ESD. It is recommended that all integrated circuits be handled with proper precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure.

Pins Description

Pin	Symbol	Description
1	VDD	Power Supply.
2	OUT	Analog output signal.
3	GND	Ground



Specifications

 $(T_A = +15^{\circ}C^{\sim} +25^{\circ}C, V_{DD} = +1.8V, unless otherwise noted.)$

PARAMETER	Symbol	CONDITIONS	MIN	ТҮР	MAX	UNIT
Supply Voltage ¹	V_{DD}		1.5	-	3.6	V
Supply Current ^{1,2}	I _{DD}		-	120	150	μΑ
Sensitivity ¹	S	94 dB SPL @ 1 kHz	-39	-38	-37	dBV/Pa
Signal to Noise Ratio	SNR	94 dB SPL @ 1 kHz	-	65	-	dB(A)
Total Harmonic Distortion	THD	94 dB SPL @ 1 kHz	-	0.1	1	%
Acoustic Over load Point	AOP	10% THD @ 1 kHz		130	-	dB SPL
Power Supply Rejection Ratio	PSRR	1 kHz, 200mVpp sine wave on V_{DD}	-	70	-	dBV/FS
Power Supply Rejection	PSR	217 Hz, 100mVpp square wave on V_{DD}	-	-99	-	dB FS (A)
DC Output		V _{DD} = 1.8V	-	0.7	-	V
Output Impedance	Z _{OUT}	@ 1 kHz	-	100	-	
Directivity			Omni-directional			
Polarity		Increasing sound pressure	Increasing output pressure		sure	

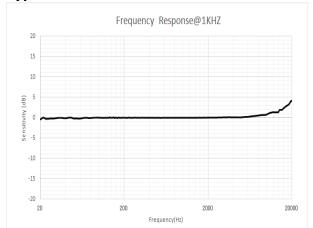
Note:

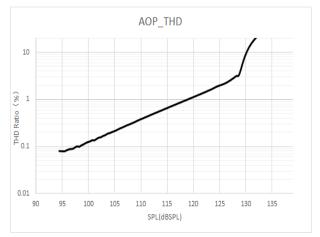
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¹ 100% tested

 $^{^2}$ Maximum specifications are measured at maximum V_{DD} . Typical specifications are measured at V_{DD} = 1.8V.

Typical Performance Characteristics





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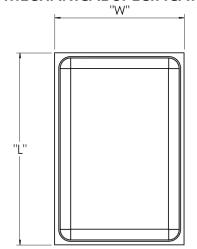
Reliability Specifications

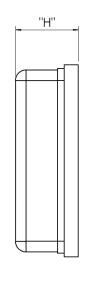
The microphone sensitivity after stress must deviate by no more than ±3dB from the initial value.

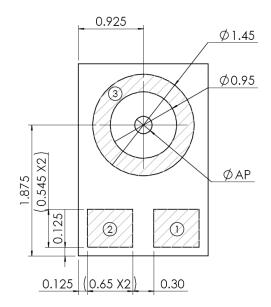
1.Heat Test, Operational	Temperature: 125±3 °C
1. Heat lest, Operational	Duration: 1000 hours
	Voltage: Applied
2 Cold Tost Operational	Temperature: -40±3°C
2.Cold Test, Operational	•
	Duration: 1000 hours
	Voltage: Applied
3.Heat Test, Non-Operational	Temperature: 125±3°C
	Duration: 1000 hours
	Voltage: Not Applied
4.Cold Test, Non-Operational	Temperature: -40±3°C
	Duration:1000 hours
- , - , , , , , , - , , , , , , , , , , , , , , , , , , ,	Voltage: Not Applied
5.Thermal Shock Test, Non-Operational	Temperature: -40±3°C and 125±3°C
	Duration: 30 minutes each, during 5
	minutes ramp, 256 cycles
	Voltage: Not applied
6.Temperature humidity storage	Temperature: 85±3°C
	Humidity: 85±3%RH
	Duration: 1000 hours
	Temperature: 65±3°C
	Humidity: 95±3%RH
	Duration: 168 hours
7.Free Fall Test 1.5m	Placed inside test fixture and dropped on
	concrete from height 1.5m.
	4 times by each surface and corner
8.Vibration	4 cycles of 20 to 2000 Hz sinusoidal sweep
	with 20G peak acceleration lasting 12
	minutes in X, Y, and Z directions
9.Mechanical Shock	5 pulses of 10000g in each of the \pm X, \pm Y,
	and $\pm Z$ directions
10.Electrostatic Discharge Test	Capacitance: 150pF
-	Resistance: 330Ω
	Duration: 10 times
	Air Discharge: Level 4(+/-15kV)
	Direct contact discharge: Level 4 (+/-8kV)
11.Human Body Mode	±2000 Volt
12.Charged-Device Model	±250 Volt
13.Reflow	5 reflow cycles with peak temperature of
	260°C
14. Solderability	245±5 °C ,5sec, 95% Tin on pad surface
*	•
15. Tumble test	300 tumbles from a height of 1m onto a steel base.
16. HAST	Temperature: 130±3°C
16. HAST	Temperature: 130±3°C Humidity: 85±3%RH
16. HAST	- I
16. HAST	Humidity: 85±3%RH

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MECHANICAL SPECIFICATIOPNS





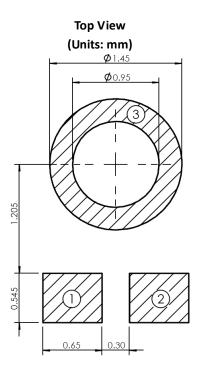


ITEM	DIMENSION	TOLERANCE	UNITS
Length (L)	2.75	±0.100	mm
Width (W)	1.85	±0.100	mm
Height (H)	0.90	±0.100	mm
Acoustic Port (AP)	Ø0.25	±0.050	mm



RECOMMENDED CUSTOMER LAND PATTERN

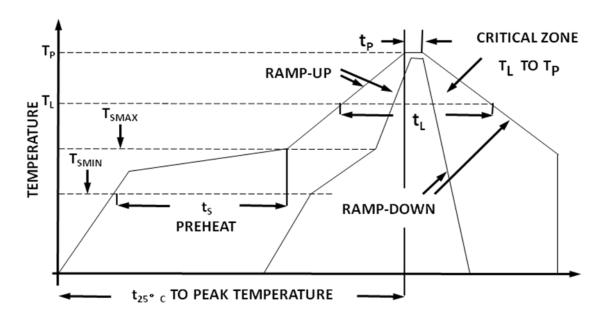
The recommended PCB land pattern for the ZTS6856SE should have a 1:1 ratio to the solder pads on the microphone package. Care should be taken to avoid applying solder paste to the sound hole in PCB. The dimensions of suggested solder paste pattern refer to the land pattern which should be shrunk by 0.025 per side.





SOLDER FLOW PROFILE

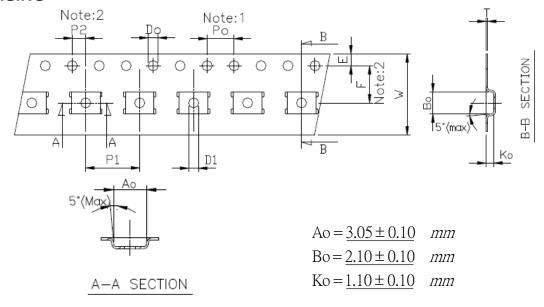
The reflow profile specified in this section describes expected maximum heat exposure of components during the reflow process of NMP product PWBs. Temperature is measured on top of component. All components have to tolerate at least this profile five times (5x) without affecting electrical performance, mechanical performance or reliability.



Pb-free and Sn63/Pb37 reflow profile requirements for soldering heat resistance:

Parameter		Reference	Pb-Free	Sn63/Pb37
Average Ramp	Rate	T _L to T _P	1.25°C/sec max	1.25 °C /sec max
	Minimum Temperature	T _{SMIN}	100°C	100 °C
Prehear	Maximum Temperature	T _{SMAX}	200°C	150 °C
	Time	T _{SMIN} to T _{SMAX}	60sec to 120sec	60sec to 120sec
Ramp-Up Rate		T _{SMAX} to T _L	1.25°C/sec	1.25 °C /sec
Time Maintain	ed Above Liquidous	tL	60sec to 150sec	60sec to 150sec
Liquidous Temp	perature	TL	217°C	183 °C
Peak Temperat	ure	T _P	260°C +0°C/-5°C	215 °C +3 °C /-3 °C
Time Within +5	5°C of Actual Peak Temperature	t₽	20 sec to 30 sec	20 sec to 30 sec
Ramp-Down Ra	nte	T _{peak}	6°C/sec max	6°C /sec max
Time +25°C (t ₂	_{50c}) to Peak Temperature		8 min max	6 min max

PACKAGING

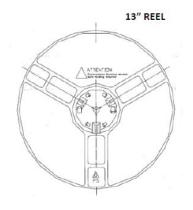


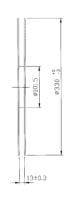
Unit: mm

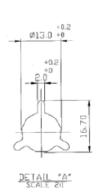
Symbol	Spec.	
K1	-	
Po	4.0 ± 0.10	
P1	8.0 ± 0.10	
P2	2.0 ± 0.05	
Do	1.55 ± 0.05	
D1	1.50 (MIN)	
E	1.75 ± 0.10	
F	5.50 ± 0.05	
10P ₀	40.0 ± 0.10	
W	12.0 ± 0.20	
Т	0.30 ± 0.05	

Notice:

- $1 \cdot 10$ Sprocket hole pitch cumulative tolerance is ± 0.1 mm.
- $2\,\cdot$ Pocket position relative to sprocket hole measured as true position of pocket not pocket hole.
- $3 \cdot A_0 \& B_0$ measured on a place 0.3mm above the bottom of the pocket to top surface of the carrier.
- $4\cdot K_{\text{O}}$ measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
- $5 \cdot \text{Carrier camber shall be not that 1mm per 100mm through a length of 250mm.}$

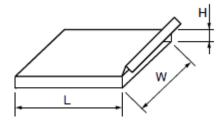






Part NO.	Reel Diameter	Quantity Per Reel	Quantity Per Inner Box	Quantity Per Outer Box
ZTS6856SE	13"	5,200	5,200	46,800

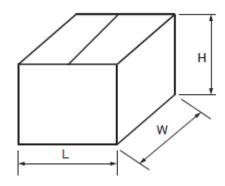
Dimensions for Inner Box



Unit: mm

L	W	Н
335	339	45

Dimensions for Outer Box



Unit: mm

L	W	Н
445	360	372

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