

Specification for Approval

Date: 2017/09/07

Customer : 深圳台慶

TAI-TECH P/N: WCM2012L2NF-SERIES

CUSTOMER P/N: _____

DESCRIPTION: _____

QUANTITY: _____ pcs

REMARK:		
Customer Approval Feedback		

西北臺慶科技股份有限公司
 TAI-TECH Advanced Electronics Co., Ltd
Headquarter:
 NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI,
 TAO-YUAN HSIEN, TAIWAN, R.O.C.
 TEL: +886-3-4641148 FAX: +886-3-4643565
 http://www.tai-tech.com.tw
 E-mail: sales@tai-tech.com.tw

Office:
 深圳辦公室
 11BC, Building B Fortune Plaza, NO.7002, Shennan Avenue, Futian
 District Shenzhen
 TEL: +86-755-23972371 FAX: +86-755-23972340

臺慶精密電子(昆山)有限公司
 TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD
 SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN,
 JIANG-SU, CHINA
 TEL: +86-512-57619396 FAX: +86-512-57619688
 E-mail: sales@tai-tech.cn

慶邦電子元器件(泗洪)有限公司
 TAIPAQ ELECTRONICS (SIHONG) CO., LTD
 Sihong development zone Suqian City, Jiangsu , CHINA.
 TEL: +86-527-88601191 FAX: +86-527-88601190
 E-mail: sales@taipaq.cn

Sales Dep.

APPROVED	CHECKED
管哲頌 Eric Kuan	劉璦瑄 Aries Liu

R&D Center

APPROVED	CHECKED	DRAWN
楊祥忠 Mike Yang	林志鴻 Zhi-Hong Lin	張展耀 Kevin Chang

Wire Wound Type Common Mode Filter

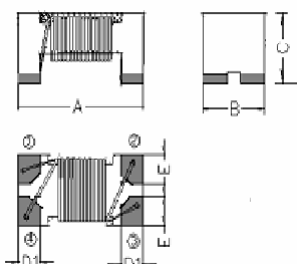
WCM2012L2NF-SERIES

1.Features

1. High common mode impedance at high frequency cause excellent noise suppression performance.
2. WCM2012L2NF series realizes small size and low profile. 2.0x1.2x1.0 mm.
3. 100% Lead(Pb) & Halogen-Free and RoHS compliant.



2.Dimension



Series	A(mm)	B(mm)	C(mm)	D1(mm)	E(mm)
2012L2NF	2.0±0.2	1.20±0.2	0.9± 0.1	0.50±0.1	0.51±0.1

Units: mm

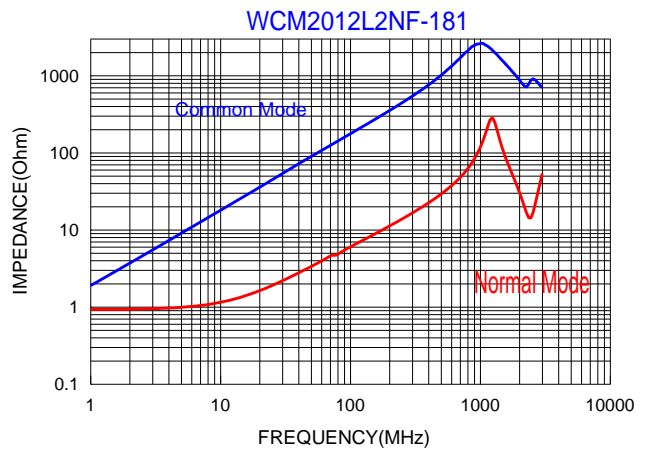
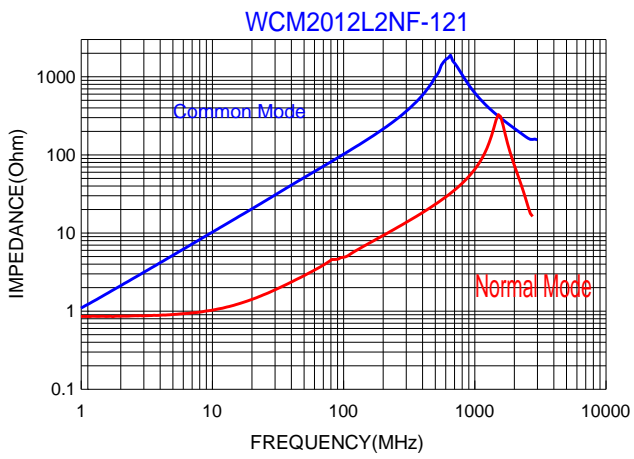
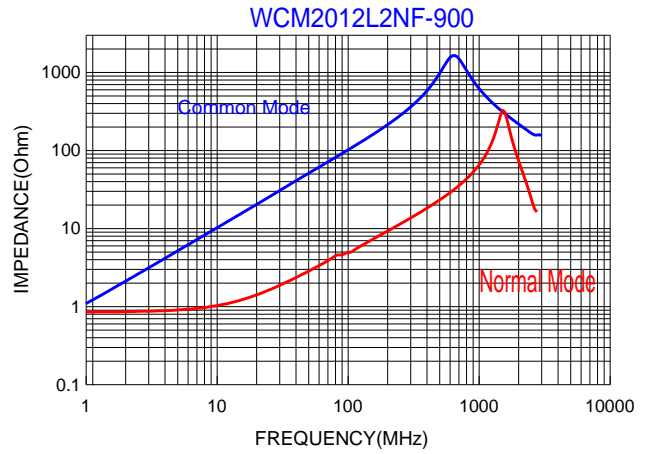
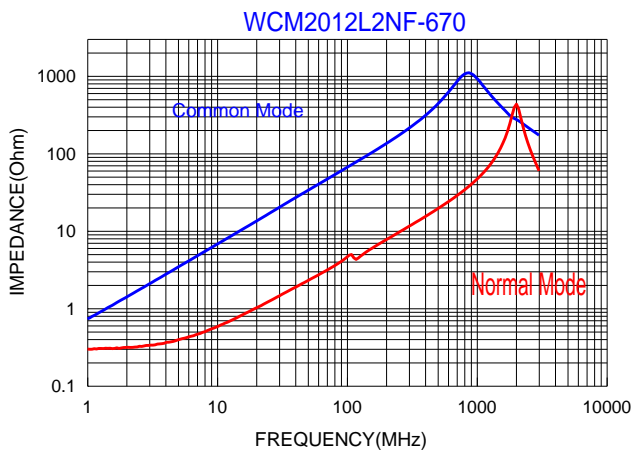
3.Part Numbering

WCM	2012	L	2	N	F	-	900	T	04
A	B	C	D	E	F		G	H	I

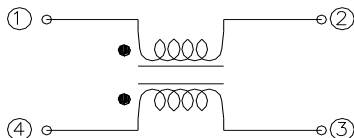
- A: Series
 B: Dimension
 C: Material Ferrite Core
 D: Number of Lines 2=2 lines
 E: Type S=Shielded , N=Unshielded
 F: Lead free type
 G: Impedance 900=90Ω
 H: Packaging T=Taping and Reel
 I: Rated Current 04=400mA

4.Specification

TAI-TECH Part Number	Common mode Impedance (Ω)	Test Frequency (MHz)	DC Resistance (Ω) max.	Rated Current (mA)max.	Rated Volt. (Vdc)max.	Withstand Volt. (Vdc) max.	IR (Ω) min.
WCM2012L2NF -670T04	67±25%	100	0.35	400	50	125	10M
WCM2012L2NF -900T04	90±25%	100	0.35	400	50	125	10M
WCM2012L2NF-121T03	120±25%	100	0.45	300	50	125	10M
WCM2012L2NF-181T03	180±25%	100	0.50	300	50	125	10M

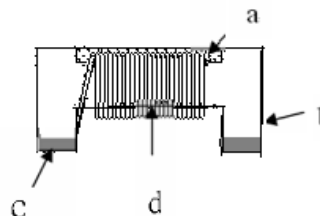


5.Schematic Diagram



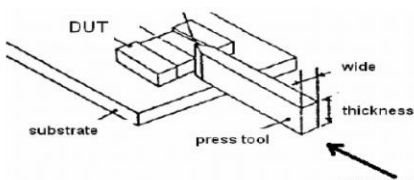
6. Materials

No.	Description	Specification
a.	Upper Plate	UV Glue
b.	Core	Ferrite Core
c.	Termination	Tin (Pb Free)
d.	Wire	Enameled Copper Wire



7. Reliability and Test Condition

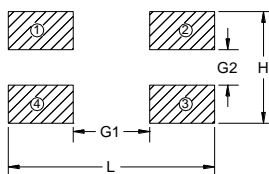
Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	-40~+125°C (on board)	
Electrical Performance Test		
Z(common mode)	Refer to standard electrical characteristics list.	Agilent-4291A+ Agilent -16197A
DCR		Agilent-4338B
I.R.		Agilent4339
Temperature Rise Test	Rated Current < 1A ΔT 20°C Max Rated Current ≥ 1A ΔT 40°C Max	1.Applied the allowed DC current. 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test	Appearance : No damage. Impedance : within±15% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature : 125±2°C Applied current : rated current Duration : 1000±12hrs Measured at room temperature after placing for 24±2 hrs
Load Humidity		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Humidity : 85±2%R.H, Temperature : 85°C±2°C Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs
Moisture Resistance		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs,keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1 : -40±2°C 30±5min Step2 : 25±2°C ≤0.5min Step3 : 125±2°C 30±5min Number of cycles : 500 Measured at room temperature after placing for 24±2 hrs
Vibration		Oscillation Frequency: 10 ~ 2K ~ 10Hz for 20 minutes Equipment : Vibration checker Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).

Item	Performance	Test Condition															
Bending	Appearance : No damage. Impedance : within±15% of initial value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.															
Shock	RDC : within ±15% of initial value and shall not exceed the specification value	<table border="1"> <thead> <tr> <th>Type</th> <th>Peak value (g's)</th> <th>Normal duration (D) (ms)</th> <th>Wave form</th> <th>Velocity change (Vi)ft/sec</th> </tr> </thead> <tbody> <tr> <td>SMD</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> <tr> <td>Lead</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> </tbody> </table>	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	SMD	50	11	Half-sine	11.3	Lead	50	11	Half-sine	11.3
Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec													
SMD	50	11	Half-sine	11.3													
Lead	50	11	Half-sine	11.3													
Solder ability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150°C,60sec. Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C Flux for lead free: Rosin. 9.5% Dip time: 4±1sec Depth: completely cover the termination															
Resistance to Soldering Heat		Depth: completely cover the termination <table border="1"> <thead> <tr> <th>Temperature(°C)</th> <th>Time(s)</th> <th>Temperature ramp/immersion and emersion rate</th> <th>Number of heat cycles</th> </tr> </thead> <tbody> <tr> <td>260 ±5 (solder temp)</td> <td>10 ±1</td> <td>25mm/s ±6 mm/s</td> <td>1</td> </tr> </tbody> </table>	Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1							
Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles														
260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1														
Terminal Strength	Appearance : No damage. Impedance : within±15% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 															

8. Soldering and Mounting

8-1. Recommended PC Board Pattern

	WCM2012L2NF
L(mm)	2.60
H(mm)	1.40
G1(mm)	1.25
G2(mm)	0.45



PC board should be designed so that products can prevent damage from mechanical stress when warping the board. Products shall be positioned in the sideways direction to against the mechanical stress to prevent failure.

8-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

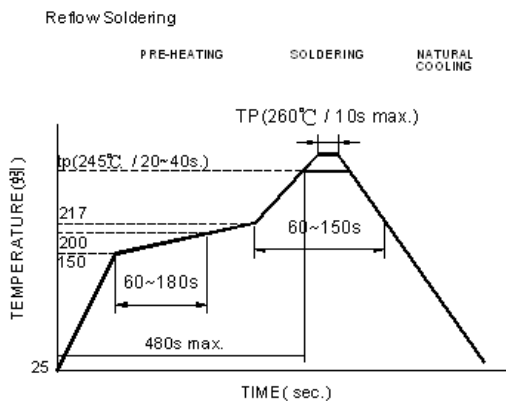
8-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

8-2.2 Soldering Iron(Figure 2):

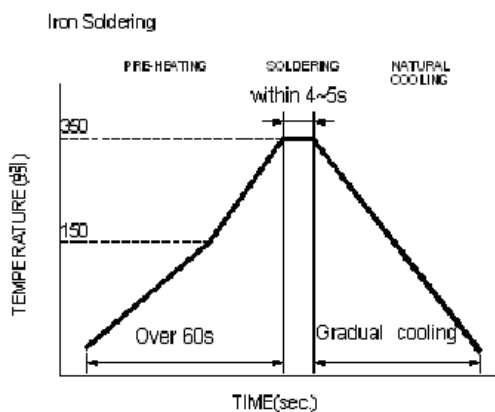
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5 sec.



Reflow times: 3 times max.

Fig.1

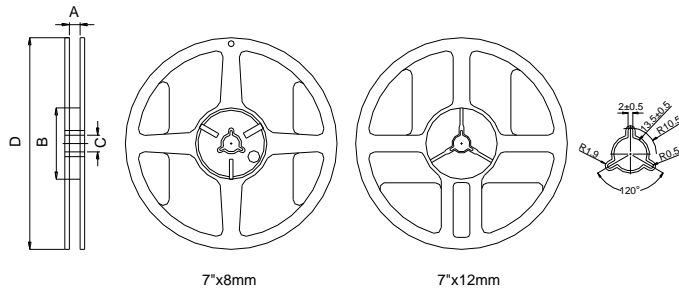


Iron Soldering times: 1 times max.

Fig.2

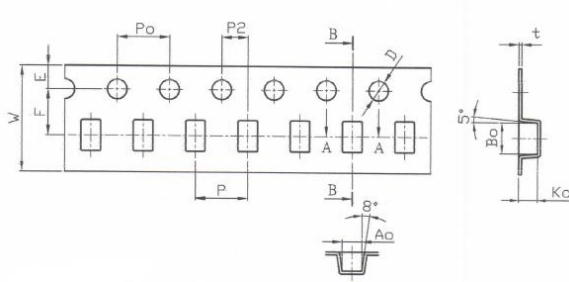
9. Packaging Information

9-1. Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2

9-2. Tape Dimension / 8mm

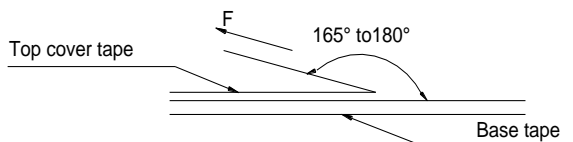


Series	W(mm)	P(mm)	E(mm)	F(mm)	P2(mm)	D(mm)	P0(mm)	A0(mm)	B0(mm)	K0(mm)	t(mm)
WCM2012L2NF	8.00±0.10	4.00±0.10	1.75±0.10	3.50±0.05	2.00±0.05	1.50+0.10/-0.00	4.00±0.10	1.42±0.10	2.25±0.10	1.04±0.10	0.22±0.05

9-3. Packaging Quantity

Chip size	Chip/Reel	Inner Box	Middle Box	Carton
WCM2012L2NF	4000	20000	100000	200000

9-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5-35	45-85	860-1060	300

Application Notice

- Storage Conditions(component level)
To maintain the solderability of terminal electrodes:
 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
 3. Recommended products should be used within 12 months form the time of delivery.
 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.