

Specification for Approval

Date: 2017/09/18

Customer:深圳台慶

	TAI-TECH P/N:	TMPC0302HV-Seri	es(G)						
	CUSTOMER P/N:								
	DESCRIPTION:								
	QUANTITY:	pcs	<u>.</u>						
REM	MARK:								
	Cus	stomer Approval Feedba	nck						

□西北臺慶科技股份有限公司

TAI-TECH Advanced Electronics Co., Ltd <u>Headquarter:</u>

NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI, TAO-YUAN HSIEN, TAIWAN, R.O.C.

TEL: +886-3-4641148 FAX: +886-3-4643565

http://www.tai-tech.com.tw E-mail: sales@tai-tech.com.tw

□Office:

深圳辦公室

11BC,Building B Fortune Plaza,NO.7002, Shennan Avenue, Futian District Shenzhen

TEL: +86-755-23972371 FAX: +86-755-23972340

□臺慶精密電子(昆山)有限公司

TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA

■慶邦電子元器件(泗洪)有限公司

TAIPAQ ELECTRONICS (SIHONG) CO., LTD JIN SHA JIANG ROAD, CONOMIC DEVELOPMENT ZONE SIHONG, JIANGSU, CHINA.

TEL: +86-527-88601191 FAX: +86-527-88601190

E-mail: sales@taipaq.cn

Sales Dep.

APPROVED	CHECKED
管哲頎	劉瑷瑄
Eric Kuan	Aries Liu

R&D Center

APPROVED	CHECKED	DRAWN
羅宜春	梁周虎	卜文娟

SMD Power Inductor

TMPC0302HV-Series(G)

	ECN HISTORY LIST						
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN		
1.0	17/09/18	新發行	羅宜春	梁周虎	ト文娟		
<i>[</i> #:			<u> </u>				
備、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、、							
注							

SMD Power Inductor

TMPC0302HV-Series(G)

1. Features

- 1. Carbonyl Powder.
- 2. Compact design.
- 3. High current , low DCR , high efficiency.
- 4. Very low acoustic noise and very low leakage flux noise.
- 5. High reliability.
- 6. 100% Lead(Pb)-Free and RoHS compliant.
- 7. High reliability -Reliability test complied to AEC-Q200.

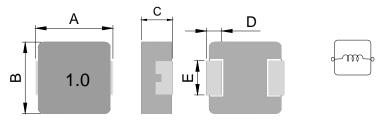




2. Applications

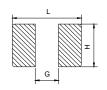
Note PC power system $\,^{,}$ incl. IMVP-6 DC/DC converter .

3. Dimensions



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
TMPC0302HV	3.5±0.2	3.2±0.2	1.8±0.2	0.7±0.2	1.2±0.2

Recommend PC Board Pattern



L(mm)	G(mm)	H(mm)
4.1	1.9	1.45

4. Part Numbering



A: Series

B: Dimension C: Type BxC

H:Carbonyl Powder, V:vehicle.

D: Inductance 1R0=1.00uH

E: Inductance Tolerance M=±20%,Y=±30%, 印字:黑色,單向印字,1.0 中間打點.

Note: 1. The above PCB layout reference only.
2. Recommend solder paste thickness at
0.12mm and above.

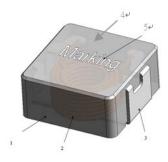
5. Specification

Part Number	Inductance L0 (uH)±20% @ 0 A	I rms (A) Typ.	I sat (A) Typ.	DCR(mΩ) Typ.@25℃	DCR(mΩ) Max.@25℃
TMPC0302HV-R10YG	0.10±30%	10.5	14	6.6	9.0
TMPC0302HV-R22YG	0.22±30%	9.0	11.2	11	14
TMPC0302HV-R33MG	0.33	8.0	10	17	21
TMPC0302HV-R47MG	0.47	7.0	9.0	19.7	23
TMPC0302HV-R60MG	0.60	6.0	7.5	24	28
TMPC0302HV-R68MG	0.68	5.5	7.0	25.5	29
TMPC0302HV-R82MG	0.82	4.8	6.0	27	32
TMPC0302HV-1R0MG	1.00	4.0	5.0	32	38
TMPC0302HV-1R2MG	1.20	3.9	4.5	39	47
TMPC0302HV-1R5MG	1.50	3.8	4.0	42	50
TMPC0302HV-2R2MG	2.20	3.5	3.7	65	75
TMPC0302HV-3R3MG	3.30	3.0	3.5	125	145
TMPC0302HV-4R7MG	4.70	2.6	3.0	172	200
TMPC0302HV-5R6MG	5.60	2.2	2.6	205	238
TMPC0302HV-6R8MG	6.80	1.9	2.2	260	300
TMPC0302HV-8R2MG	8.20	1.6	1.9	340	390
TMPC0302HV-100MG	10.0	1.4	1.6	366	422

Note:

- 1. Test frequency: L: 100KHz/1.0V;
- $3. \ \ Testing\ Instrument: L/Q: HP4284A, CH11025, CH3302, CH1320, CH1320S, LCR, METER / Rdc: CH16502, Agilent 33420A, MICRO, CHMMETER. Agilent 3420A, MICRO, CH16502, Agilent 3420A, MICRO, CH16502, Agilent 3420A, CH1650A, CH16$
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately Δt of 40 $^{\circ}$ C.
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.

6. Material List



NO	Items	Materials	
1	Core	Carbonyl Powder.	
2	Wire	Polyester Wire or equivalent.	
3	Clip	100% Pb free solder(Ni+SnPlating)	
4	paint	Epoxy resin	
5	Ink	Halogen-free ketone	

7. Reliability and Test Condition

Item	Performance	Test Condition		
Operating temperature	-55~+125°C (Including self - temperature rise)			
Storage temperature and Humidity range	110-+40°C,50~60%RH (Product without taping) 255~+125°C (on board)			
Electrical Performance Test				
Inductance	Defends a transfer of the street of the second of the line	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.		
DCR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.		
Saturation Current (Isat)	Approximately △L30%	Saturation DC Current (Isat) will cause L0 to drop \(\(\Lambda \)(\(\text{L}(\%) \)		
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise \triangle T($^{\circ}$ C). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer		
Reliability Test				
High Temperature Exposure(Storage) AEC-Q200 Temperature Cycling AEC-Q200		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Temperature : 125±2°C (Inductor) Duration : 1000hrs Min. Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1 : -55±2°C 30min Min.(Inductor) Step2 : 125±2°C transition time 1 min MAX. Step3 : 125±2°C 30min Min.		
Moisture Resistance	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Step4: Low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs, keep at 25°C for 2hrs then keep at -10°C for 3hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs. Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity: 85±3% R.H, Temperature: 85°C±2°C Duration: 1000hrs Min with 100% rated current. Measured at room temperature after placing for24±2hrs		
Biased Humidity (AEC-Q200)				
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Temperature: 125±2°C (Inductor) Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for24±2hrs		
External Visual	Appearance: No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.		
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement		
Resistance to Solvents	Appearance : No damage.	Add aqueous wash chemical - OKEM clean or equivalent.		
Mechanical Shock	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Type Peak value (g's) Normal (matter) Wave (g's) Wave (value) Normal (g's) Form change (Vi)ft/sec (value) Half-sine (value) Half-sine (value) Half-sine (value) Half-sine (value) Shocks in each direction along 3 perpendicular axes.		

Item	Performance	Test Condition			
Vibration		IPC/JEDEC J-STD-020DClassification Reflow Profiles Oscillation Frequency: 10~2K~10Hz for 20 minute Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time:12 hours(20 minutes, 12 cycles each of 3 orientations) ∘ Test condition:			
Resistance to Soldering Heat	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the	Temperature (°C) Time(s) Temperature ramp/immersion and emersion rate 260±5(solder 10±1 25mm(s ±6 mm/s 1			
Thermal shock (AEC-Q200)	specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1:-55±2°C 15±1min(Inductor) Step2:125±2°C within 20Sec. Step3:125±2°C 15±1min Number of cycles: 300 Measured at room femprature after placing fo24±2hrs			
ESD	Appearance : No damage.	10% Time (ns)			
Solderability	More than 95% of the terminal electrode should be covered with solder \circ	Steam Aging: 8 hours ± 15 min Preheat: 150 °C,60sec. Solder: Sn96.5% Ag3% Cu0. 5% Temperature: 245±5 °C ° Flux for lead free: Rosin. 9.5% ° Dip time: 4±1sec. Depth: completely cover the termination			
Electrical Characterization	Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation .			
Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.			
Board Flex	Appearance : No damage	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. Support Solder Chip Printed crout board before testing			
		Probe to exert bending force Radius 340 Printed circuit board under tost Displacement			
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. Tradius 0,5 mm DUT wide thickness shear force			

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

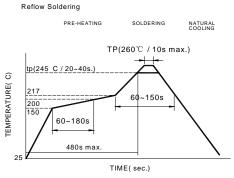
Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

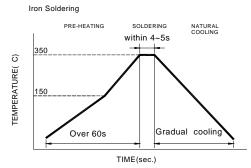
- Preheat circuit and products to 150 $^{\circ}\!\mathbb{C}$
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.



Reflow times: 3 times max.

Fig.1

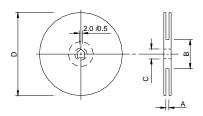


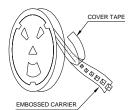
Iron Soldering times: 1 times max.

Fig.2

9. Packaging Information

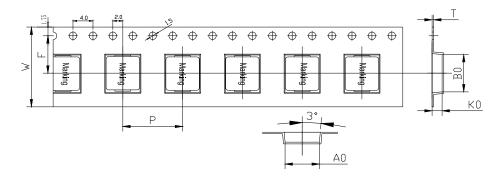
(1) Reel Dimension





Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x12mm	12.4+2/-0	100±2	13+0.5/-0.2	330

(2) Tape Dimension

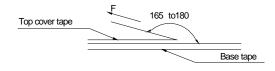


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)
ТМРС	0302	3.8±0.1	3.5±0.1	2.3±0.1	8.0±0.1	12±0.3	5.5±0.1	0.35±0.05

(3) Packaging Quantity

ТМРС	0302	
Chip / Reel	3000	
Inner box	6000	
Carton	24000	

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-C-2003 of 4.11 stadnard).

Room Temp. (℃)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 30 $^{\circ}\mathrm{C}$ $\,$ and 70% RH.
- 3. Recommended products should be used within 6 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

10. Typical Performance Curves

