

Form No.: QF-1274 Edition: 2

ISO9001 ISO14001 IATF16949 CHILISIN ELECTRONICS CORP.

RoHS & Halogen Free & REACH Compliance.

# **Preliminary Specification**

Customer :			
Customer P/N :			
Drawing No:			
Quantity :	Pcs.	Date :	
Chilisin P/N:	BTLB	002012NXVKBA10	

	SPECIFICATION ACCEPTED BY:				
COMPONENT ENGINEER					
ELECTRICAL ENGINEER					
MECHANICAL ENGINEER					
APPROVED					
REJECTED					
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奇力新電子(越南廠)有限公司 Chilisin Electronics (Vietnam) Limited No 143 - 145, Road No 10, VSIP Hai Phong, Lap Le Commune, Thuy Nguyen Dist, Haiphong City, Vietnam Tel : 84-316 255 688 Fax : 84-316 255 689 E-mail : sales@chilisin.com 高力新電子(湖南廠)有限公司 HuNan Chilisin Electronics Technology Co., Ltd No. 8, Shaziao Liangshuijing Town, Yuanling County, Huaihua City, Hunan Province 419601, China Tel : 86-745-867-5882 E-mail : cect@chilisin.com					
Drawn by	Checked by Approved b				
Bill	CF Derek				



#### APPLICATION

BPF for N77/N78 band application.

## FEATURES

#### 1-1 Compact Size

Miniaturized SMD packaged in low profile and lightweight.

#### 1-2 Low loss

Low insertion loss, high attenuation.

#### 1-3 High Soldering Heat Resistance

High quality termination allows both flow and re-flow soldering methods to be applied.

#### **1-4 Characteristics**

Eliminate noise over a wide frequency range. Idea for high frequency and space limited designs.

#### 1-5 Available in tape and reel packaging for automatic mounting

## PRODUCT IDENTIFICATION

#### <u>BTLB 00 2012 ###xx A1 0</u> ① ② ③ ④ ⑤ ⑥

- ① Product Code
- 2 Customer Code
- ③ Dimension Code
- ④ Series Type (### represents center frequency and xx represents material type)
- ⑤ Design Code
- <sup>©</sup> Version Code

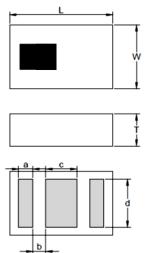
## ELECTRICAL REQUIREMENTS

	•	(	Simulation)	
Item	Frequency Range(MHz)	Min.	Тур.	Max.
Insertion Loss (dB)	3300~4200	-	1.2	1.4
Return Loss (dB)	3300~4200	10	20	-
	450~2200	40	43	-
	2300~2483	37	43	-
	2496~2690	32	40	-
Attenuation (dB)	5150~5850	22	27	-
	6600~8400	25	36	-
	9900~12600	20	41	-
	13200~16800	15	48	-

Operating Temperature Range : -40~85°C



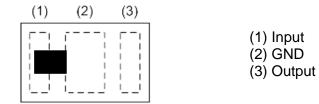
## PRODUCT DIMENSION



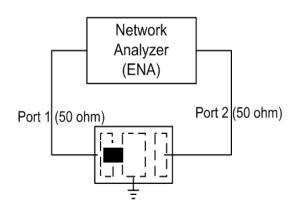
L	W	Т	а
2.00±0.15	1.25±0.10	0.65 max	0.275±0.10
b	С	d	
0.25±0.05	0.60±0.10	0.95±0.10	

NOTE : Dimensions in mm.





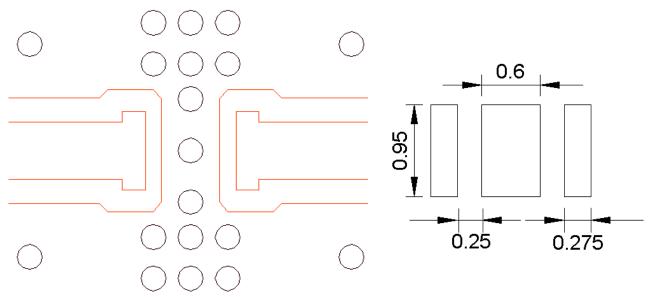
# MEASURING DIAGRAM



Test Instrument : Agilent E5071C Network Analyzer or equivalent.

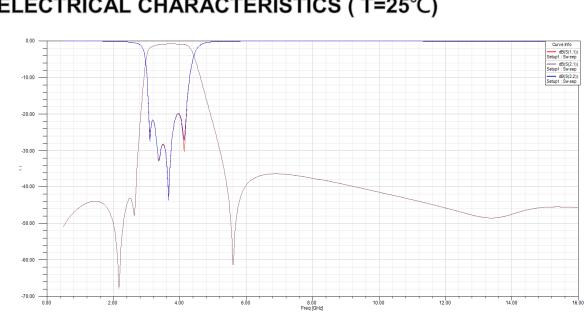


## ISO9001 ISO14001 IATF16949 CHILISIN ELECTRONICS CORP. **RECOMMENDED PCB LAYOUT AND LAND PATTERN**



Unit : mm

O Line width should be designed to match 50Ω characteristic impedance, depending on PCB material and thickness.



# ELECTRICAL CHARACTERISTICS (T=25°C)



## RELIABILITY TEST

## Mechanical Test

ltem	Test Condition	Specification
Vibration	10 Hz/min~55 Hz/min~10 Hz/min vibration frequency with 1.5 mm amplitude for two hours in x, y, z directions	No apparent damage
Drop shock	Dropped onto printed circuit board from 100cm height three times in x, y, z directions. The terminals shall be protected.	No apparent damage
Soldering heat resistance	Preheating temperature : 150±10°C Preheating time : 1 to 2 minutes Solder bath temperature : 260±5°C Bathing time : 10±1 seconds	Loss of metallization on the edges of each electrode shall not exceed 25%.
Bending test onto printed circuit board	Solder specimen LTCC components on the test printed circuit board (L: 100 x W: 40 x T: 1.6mm) in appended recommended PCB pattern. Apply the load in direction of the arrow until bending reaches 2 mm. 60sec holding time.	No apparent damage
Solderability	*Solder bath temperature:245±5°C *Immersion time:3±1 seconds. Solder:Sn3Ag0.5Cu for lead-free	At least 95% of a surface of each terminal electrode must be covered by fresh solder.
Adhesive strength	Standard is as follows 0605~1005 >0.1KgF 1109~2016 >0.5KgF 2520~>1KgF	No apparent damage

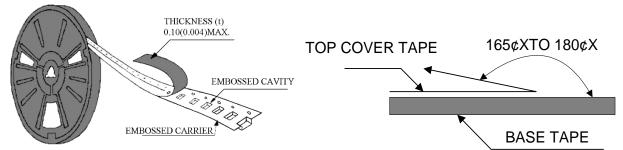
## Environment Test

Thermal shock	-40 $^\circ\!\!\!C$ ~85 $^\circ\!\!\!C$ for 100 cycles each cycle being 30 min	No apparent damage Fulfill the electrical spec. after test
Humidity resistance	85±2℃,80~90% R.H. for 1000 hours	No apparent damage Fulfill the electrical spec. after test
High temperature resistance	85±2°C for 1000 hours	No apparent damage Fulfill the electrical spec. after test
Low temperature resistance	-40±3℃ for 1000 hours	No apparent damage Fulfill the electrical spec. after test



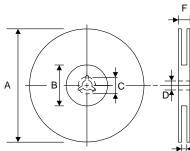
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## Peel-off force



The force for peeling off cover tape is 10 grams in the arrow direction.

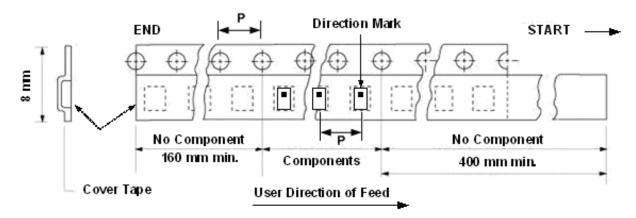
## Dimension (Unit: mm)



TYPE	Α	В	С	D	Е	F
8 mm	178±1	60+0.5 -0	-	13±0.2	9±0.5	12±0.5
12 mm	178±0.3	60±0.2	19.3±0.1	13.5±0.1	13.6±0.1	-

## **Taping quantity**

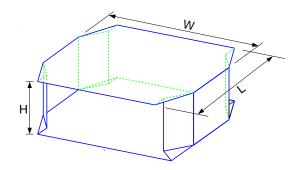
SERIES	5824 5724	5320 5220	4532	4516	3225	3216 2520	2012 1608	1005
PCS/Reel	5000	3000	1000	2000	2500	3000	4000	10000



P= 4 mm



## TAPE PACKING CASE



No. of Reels	W	L	н
2	18±0.5	18±0.5	2.4±0.2
3	18±0.5	18±0.5	3.6±0.2
4	18±0.5	18±0.5	4.8±0.2
5	18±0.5	18±0.5	6.0±0.2

Unit:cm

## **MSL RATING**

Level 1

## **OPERATION TEMPERATURE**

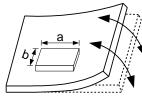
-40°C~85°C

## STORAGE CONDITION

The temperature should be within -40~85 $^{\circ}$ C and humidity should be less than 75% RH. The product should be used within 6 months from the time of delivery.

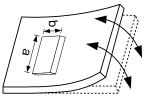
## ATTENTION REGARDING PCB BENDING

(a) PCB shall be designed so that products are not subjected to the mechanical stress for board wrapage. Product shall be located in the sideway direction to the mechanical stress.



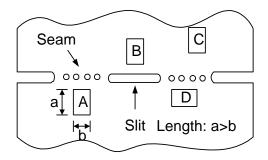
(Poor example)

Length: a>b



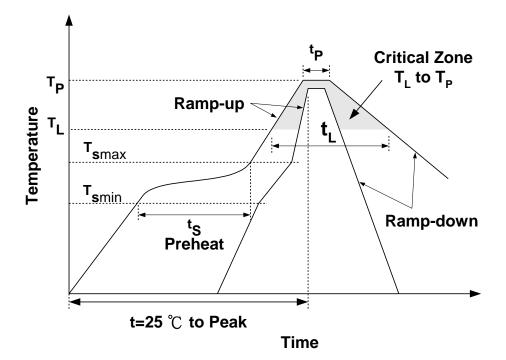
(Good example)

(b) Products (A,B,C,D) shall be located carefully so that products are not subjected to the mechanical stress due to warping the board. Because they may be subjected to the mechanical stress in order of A>C>B≒D.





#### ISO9001 ISO14001 IATF16949 CHILISIN ELECTRONICS CORP. RECOMMENDED REFLOW SOLDERING PROFILE



Profile Feature		Sn-Pb	Pb-Free
	ts	60~120 seconds	60~180 seconds
Preheat	T <sub>smin</sub>	<b>100</b> ℃	<b>150</b> ℃
	T <sub>smax</sub>	<b>150</b> ℃	<b>200</b> °C
Average ramp-up	rate (T <sub>smax</sub> to T <sub>P</sub> )	3°C/second max.	3℃/second max.
Time main above	Temperature (T∟)	<b>183</b> ℃	<b>217</b> ℃
Time main above	Time (t∟)	60~150 seconds	60~150 seconds
Peak temperature	(Тр)	<b>230</b> ℃	<b>250~260</b> ℃
Time within 5° $\mathbb C$ of actual peak temperature (t <sub>P</sub> )		10 seconds	10 seconds
Ramp-down rate		6°C/sec max.	6°C/sec max.
Time 25 $^\circ\!\mathbb{C}$ to peak	temperature	6 minutes max.	8 minutes max.

## NOTES

The contents of this data sheet are subject to change without notice. Please confirm the specifications and delivery conditions when placing your order.