

Form No.: QF-1274 Edition: 2

ISO9001 ISO14001 IATF16949 CHILISIN ELECTRONICS CORP.

RoHS & Halogen Free & REACH Compliance.

SPECIFICATION FOR APPROVAL

Customer :			
Customer P/N :			
Drawing No:			
Quantity:	Pcs. Date :		
Chilisin P/N:	BTLB001608LXVKBD20		

SPECIFICATION ACCEPTED BY:						
COMPONENT ENGINEER						
ELECTRICAL ENGINEER						
MECHANICAL ENGINEER						
APPROVED						
REJECTED						
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Drawn by	Checked by Approve					



APPLICATION

BPF for 5 GHz band application.

FEATURES

1-1 Compact Size

Miniaturized SMD packaged in low profile and lightweight.

1-2 Low loss

Low insertion loss, high attenuation.

1-3 High Soldering Heat Resistance

High quality termination allows both flow and re-flow soldering methods to be applied.

1-4 Characteristics

Eliminate noise over a wide frequency range. Idea for high frequency and space limited designs.

1-5 Available in tape and reel packaging for automatic mounting

PRODUCT IDENTIFICATION

BTLB 00 1608 ###xx D2 0 () (2) (3) (4) (5) (6)

- ① Product Code
- 2 Customer Code
- ③ Dimension Code
- ④ Series Type (### represents center frequency and xx represents material type)
- ⑤ Design Code
- [©] Version Code

ELECTRICAL REQUIREMENTS

Part No.	Pass Band (MHZ)	Insertion Loss (dB)		Insertion Loss (dB)		Returi (d	n Loss B)		Atten (dB/dE	uation 3/MHz)
	Freq.	Тур.	Max.	Тур.	Min.	Тур.	Min.	Freq.		
вт						39.9	38	30~2700		
LB0	LBO						35.5	16	3453~3547	
0160		1 at 25°C			34.4	33	3677~3883			
згхл	[®] 4900~5950 MHZ 0.	0.68	0.68 1.1 at -40~85°C	17.7	12	21.6	10	6900~7093		
'KBD				at -40~85°C			30.2	32	7333~7750	
20						37.3	34	10600~11650		
				28.1	20	15540~17700				

Operating Temperature Range : -40~85°C Power Capacity : 3W max.



PRODUCT DIMENSION



L	W	Т	а
1.60±0.10	0.80±0.10	0.65 max.	0.25±0.10
b	С	d	е
0.23±0.10	0.40±0.10	0.55±0.10	0.60±0.10

NOTE : Dimensions in mm.

TERMINAL CONFIGURATION



MEASURING DIAGRAM



Test Instrument : Agilent E5071C Network Analyzer.



ISO9001 ISO14001 IATF16949 CHILISIN ELECTRONICS CORP. RECOMMENDED PCB LAYOUT AND LAND PATTERN



 \bigcirc Line width should be designed to match 50 Ω characteristic impedance, depending on PCB material and thickness.

ELECTRICAL REQUIREMENTS





RELIABILITY TEST

Mechanical Test

ltem	Test Condition	Specification		
Vibration	10 Hz/min~55 Hz/min~10 Hz/min vibration frequency with 1.5 mm amplitude for two hours in x, y, z directions	No apparent damage		
Drop shock	Dropped onto printed circuit board from 100cm height three times in x, y, z directions. The terminals shall be protected.	No apparent damage		
Soldering heat resistance	dering heat istancePreheating temperature : 150±10°C Preheating time : 1 to 2 minutes Solder bath temperature : 260±5°C Bathing time : 10±1 secondsLoss of metallization on the e of each electrode shall not ex 25%.			
Bending test onto printed circuit board	Solder specimen LTCC components on the test printed circuit board (L: 100 x W: 40 x T: 1.6mm) in appended recommended PCB pattern. Apply the load in direction of the arrow until bending reaches 2 mm. 60sec holding time.	No apparent damage		
Solderability	*Solder bath temperature:245±5°C *Immersion time:3±1 seconds. Solder:Sn3Ag0.5Cu for lead-free	At least 95% of a surface of each terminal electrode must be covered by fresh solder.		
Adhesive strength	Standard is as follows 0605~1005 >0.1KgF 1109~2016 >0.5KgF 2520~>1KgF	No apparent damage		

Environment Test

Thermal shock	-40 $^\circ\!\!\mathbb{C}$ ~85 $^\circ\!\!\mathbb{C}$ for 100 cycles each cycle being 30 min	No apparent damage Fulfill the electrical spec. after test
Humidity resistance	85±2℃,80~90% R.H. for 1000 hours	No apparent damage Fulfill the electrical spec. after test
High temperature resistance	85±2°C for 1000 hours	No apparent damage Fulfill the electrical spec. after test
Low temperature resistance	-40±3 $^{\circ}$ C for 1000 hours	No apparent damage Fulfill the electrical spec. after test



Peel-off force



The force for peeling off cover tape is 10 grams in the arrow direction.

Dimension (Unit: mm)



TYPE	Α	В	С	D	Е	F
8 mm	178±1	60+0.5 -0	-	13±0.2	9±0.5	12±0.5
12 mm	178±0.3	60±0.2	19.3±0.1	13.5±0.1	13.6±0.1	-

Taping quantity

SERIES	5824 5724	5320 5220	4532	4516	3225	3216 2520	2012 1608 1109	1005 0605
PCS/Reel	5000	3000	1000	2000	2500	3000	4000	10000



P= 4 mm



TAPE PACKING CASE



No. of Reels	W	L	н
2	18±0.5	18±0.5	2.4±0.2
3	18±0.5	18±0.5	3.6±0.2
4	18±0.5	18±0.5	4.8±0.2
5	18±0.5	18±0.5	6.0±0.2

Unit:cm

MSL RATING

Level 1

OPERATION TEMPERATURE

-40°C~85°C

STORAGE CONDITION

The temperature should be within -40~85 $^{\circ}$ C and humidity should be less than 75% RH. The product should be used within 6 months from the time of delivery.

ATTENTION REGARDING PCB BENDING

(a) PCB shall be designed so that products are not subjected to the mechanical stress for board wrapage. Product shall be located in the sideway direction to the mechanical stress.



(Poor example)





(b) Products (A,B,C,D) shall be located carefully so that products are not subjected to the mechanical stress due to warping the board. Because they may be subjected to the mechanical stress in order of A>C>B≒D.





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Profile Feature		Sn-Pb	Pb-Free	
	ts	60~120 seconds	60~180 seconds	
Preheat	T _{smin}	100 ℃	150 ℃	
	T _{smax}	150 ℃	200 ℃	
Average ramp-up rate (T _{smax} to T _P)		3℃/second max.	3℃/second max.	
Time main above	Temperature (T∟)	183 ℃	217 ℃	
	Time (t∟)	60~150 seconds	60~150 seconds	
Peak temperature (T _P)		230 ℃	250~260 ℃	
Time within 5℃ of actual peak temperature (t⊳)		10 seconds	10 seconds	
Ramp-down rate		6°C/sec max.	6°C/sec max.	
Time 25 $^\circ\!\!\mathbb{C}$ to peak temperature		6 minutes max.	8 minutes max.	

NOTES

The contents of this data sheet are subject to change without notice. Please confirm the specifications and delivery conditions when placing your order.