



### 60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

## **Product Summary**

BV <sub>DSS</sub>	Rds(on)	I <sub>D</sub> Tc = +25°C	
60V	$16m\Omega @ V_{GS} = 10V$	37.1A	
000	$24m\Omega$ @ V <sub>GS</sub> = 4.5V	30.3A	

## **Description and Applications**

This MOSFET has been designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Power Management
- DC-DC Converters
- Motor Control

## **Features**

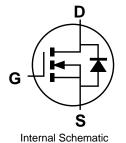
- Rated to +175°C Ideal for High Ambient Temperature Environments
- Thermally Efficient Package Cooler Running Applications
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
  - The DMTH6016LPSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

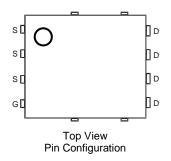
https://www.diodes.com/quality/product-definitions/

### **Mechanical Data**

- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)







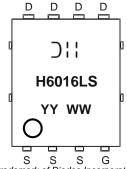
## **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMTH6016LPSQ-13	PowerDI5060-8	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### Marking Information



☐ He Manufacturer's Marking
H6016LS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 21 = 2021)
WW = Week Code (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	60	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V $T_C = +25^{\circ}C$ $T_C = +100^{\circ}C$			lo	37.1 26.2	А
Continuous Drain Current (Note 5) $V_{GS} = 10V$ Steady  TA = +25°C State  TA = +100°C		lo	10.6 7.5	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	145	Α
Maximum Continuous Body Diode Forward Current (Note 6)			Is	31	Α
Avalanche Current, L = 0.1mH			I <sub>AS</sub>	15.3	Α
Avalanche Energy, L = 0.1mH			Eas	11.7	mJ

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	3	W
Thermal Resistance, Junction to Ambient (Note 5)  Steady State		Reja	49	°C/W
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		PD	37.5	W
Thermal Resistance, Junction to Case (Note 6)	Rejc	4	°C/W	
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +175	°C	

# Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

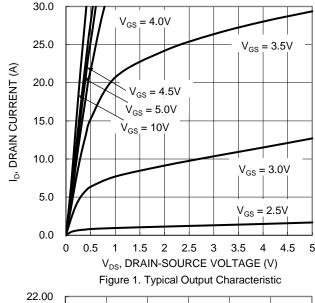
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)		•			•	•	
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100	nA	V <sub>G</sub> S = ±20V, V <sub>D</sub> S = 0V	
ON CHARACTERISTICS (Note 7)	•					·	
Gate Threshold Voltage	Vgs(th)	1	_	2.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	0	_	12.4	16	mΩ	Vgs = 10V, ID = 20A	
Static Drain-Source On-Resistance	RDS(ON)	_	18.2	24	11177	$V_{GS} = 4.5V, I_D = 18A$	
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = 1A	
DYNAMIC CHARACTERISTICS (Note 8)	•					·	
Input Capacitance	Ciss	_	864	_		V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss	_	282		pF		
Reverse Transfer Capacitance	Crss	_	27	_			
Gate Resistance	Rg	_	1.3	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	8.4	_			
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	17	_		V 00V I 40A	
Gate-Source Charge	Qgs	_	3.1		nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 10A	
Gate-Drain Charge	$Q_{gd}$	_	4.3	_			
Turn-On Delay Time	tD(ON)	_	3.4	_			
Turn-On Rise Time	tR	_	5.2	_		$V_{GS} = 10V, V_{DS} = 30V,$ $R_{G} = 6\Omega, I_{D} = 10A$	
Turn-Off Delay Time	tD(OFF)	_	13	_	ns		
Turn-Off Fall Time	tF	_	7	_			
Reverse Recovery Time	t <sub>RR</sub>	_	22	_	ns	1 404 31/31 4004/55	
Reverse Recovery Charge	QRR	_	11	_	nC	IF = 10A, di/dt = 100A/µs	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

6. Thermal resistance from junction to soldering point (on the exposed drain pad). 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.







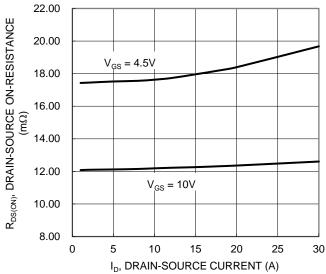


Figure 3. Typical On-Resistance vs Drain Current and Gate Voltage

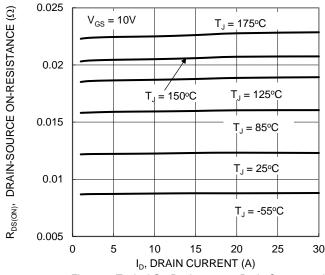
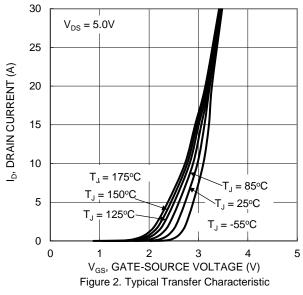
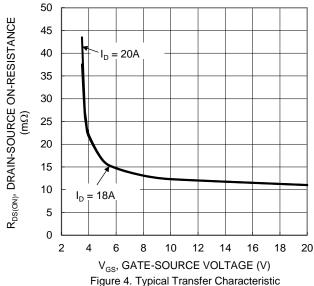


Figure 5. Typical On-Resistance vs Drain Current and Temperature





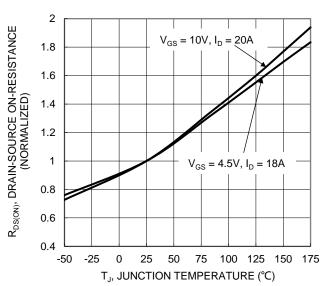
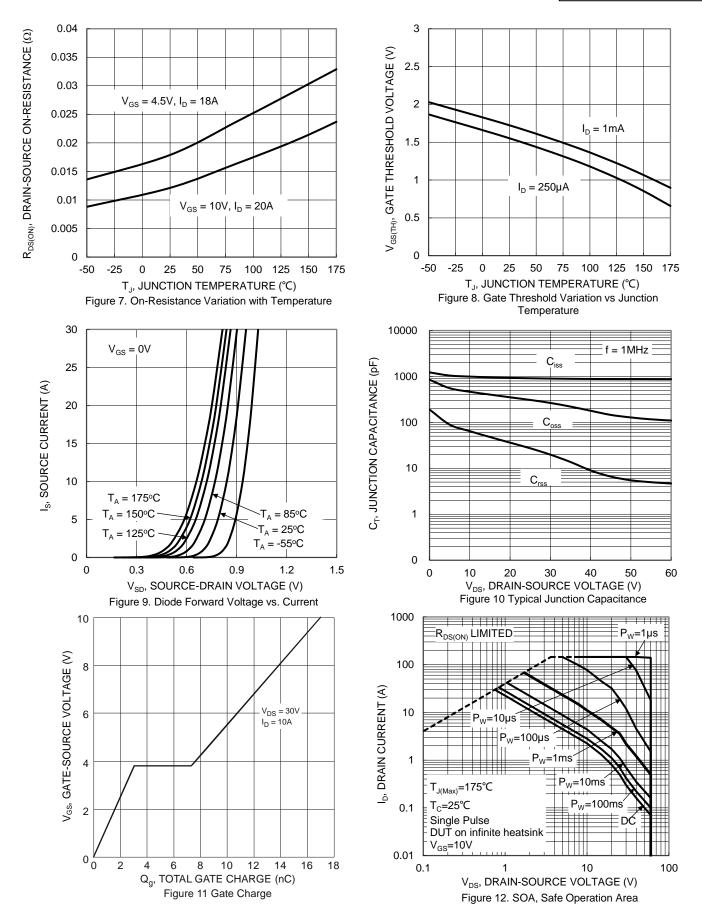


Figure 6. On-Resistance Variation with Temperature





July 2021

© Diodes Incorporated



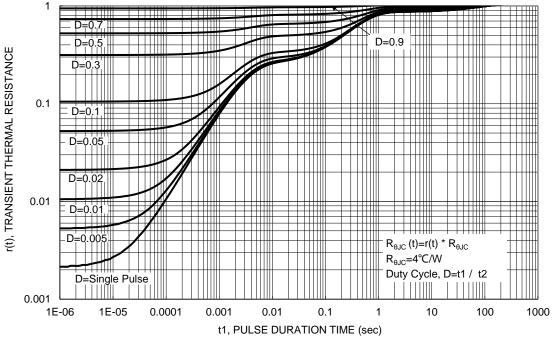


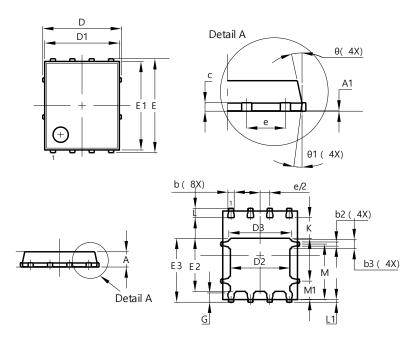
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8

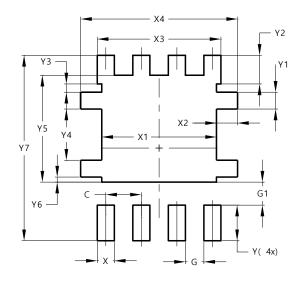


PowerDI5060-8						
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0.00	0.05	1			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
D	į	5.15 BSC	;			
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
E	(	6.15 BSC				
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	1.27 BSC					
G	0.51	0.71	0.61			
K	0.51	-	-			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
M	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12º	11º			
Θ1	6º	8º	7º			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Υ	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



#### **IMPORTANT NOTICE**

- 1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com