

SGM13001A Low Noise Amplifier for GNSS

GENERAL DESCRIPTION

The SGM13001A high gain, low noise amplifier (LNA) is dedicated to GPS, GLONASS Galileo and Beidou standards. This product has an extremely low noise figure of 0.9dB, 18.2dB gain and excellent linearity.

The SGM13001A works under a 1.6V to 3.1V single power supply while consumes 6.5mA current, in power down (PD) mode, the power consumption will be reduced to less than 1μ A.

The SGM13001A is available in a Green UTDFN-1.1×0.7-6L package, RoHS compliant and halogen free. When no external DC is applied, there is no need for external DC blocking capacitors, thus saving PCB area and cost.

FEATURES

- High Gain: 18.2dB
- Low Noise Figure 0.9dB at 1575.42MHz
- Low Operation Current: 6.5mA and PD Current Less than 1µA
- Operating Frequency Range: 1550MHz to 1615MHz
- Single Supply Voltage Range: 1.6V to 3.1V
- Low Cost BOM
- Lead-Free and RoHS Compliant
- Available in a Green UTDFN-1.1x0.7-6L Package

APPLICATIONS

Automotive Navigation
Personal Navigation Device (PND)
Cell Phone with GPS
MID/PAD with GPS

BLOCK DIAGRAM

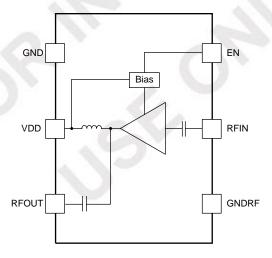


Figure 1. SGM13001A Block Diagram

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM13001A	UTDFN-1.1×0.7-6L	-40°C to +85°C	SGM13001AYUEC6G/TR	ZY	Tape and Reel, 10000

MARKING INFORMATION

NOTE: Fixed character for 7Y.



Green (RoHS & HSF): PS Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your PSMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, V _{DD}	0.3V to 3.6V
Other Pin to GND	0.3V to V _{DD} + 0.3V
RF Input Power, PIN	10dBm
Junction Temperature	+150°C
Storage Temperature Range	55°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	
HBM	2000V
MM	150V
CDM	500V

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range -40°C to +85°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

ESD SENSITIVITY CAUTION

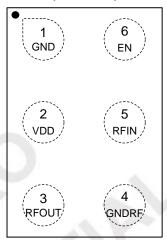
This integrated circuit can be damaged if ESD protections are not considered carefully. PSMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

PS Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION

(TOP VIEW)



UTDFN-1.1×0.7-6L

PIN DESCRIPTION

PIN	NAME	FUNCTION
1	GND	Analog Ground.
2	VDD	Power Supply.
3	RFOUT	LNA Output.
4	GNDRF	RF Ground.
5	RFIN	LNA Input from Antenna.
6	EN	Active High Enable Input for the Device. Pull high enable, pull low into power down mode.

ELECTRICAL CHARACTERISTICS

 $(V_{DD} = 1.6V \text{ to } 3.1V, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, f = 1550\text{MHz} \text{ to } 1615\text{MHz}, \text{ typical values are at } V_{DD} = 2.8V, T_A = +25^{\circ}\text{C}, f = 1575.42\text{MHz}, \text{ unless otherwise noted.})$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC Specifications						'
Supply Voltage	V_{DD}				3.1	V
Cupply Current	I _{DD}	EN = High		6.5		mA
Supply Current	I _{SD}	EN = Low		0.01	1	μA
EN Input High	V _{IH}		1.35	1.8	V_{DD}	V
EN Input Low V _{IL}			0	0	0.45	_ v
AC Specifications	<u> </u>					
RF Frequency Range	f_0	None		1575.42		MHz
Power Gain	S21	16		18.2	19.8	dB
Noise Figure	NF			0.90		dB
Input Return Loss	S11			-4		dB
Output Return Loss	S22			-16		dB
Reverse Isolation	S12	Sweep Power -40dBm, 1575.42MHz		-31		dB
Desense	ΔNF	Jammed signal @ 1463MHz and 1712MHz, - 20dBm		0.25		dB
Stability	Kf	Frequency range from 500MHz to 5GHz	1			
Input Power 1dB Compression Point	P1dB	1575MHz		-10		dBm
Input In-Band IP3	IIP3_inb	$f_1 = 1574.5MHz$, $f_2 = 1575.5MHz$, -30dBm		1		dBm
Input Out-Band IP3	IIP3_outb	$f_1 = 1712.7 \text{MHz}, -20 \text{dBm}, f_2 = 1850 \text{MHz}, -65 \text{dBm}, \\ P_3 = (2 \times P_1 + P_2 + Gain 1575 \text{MHz} - IM3)/2$		6		dBm

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{DD} = 1.6V \text{ to } 3.1V, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, f = 1550\text{MHz} \text{ to } 1615\text{MHz}, \text{ typical values are at } V_{DD} = 1.8V, T_A = +25^{\circ}\text{C}, f = 1575.42\text{MHz}, \text{ unless otherwise noted.})$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC Specifications	I					
Supply Voltage	V_{DD}		1.6		3.1	V
Supply Current	I _{DD}	EN = High		6.3		mA
Supply Current	I _{SD}	EN = Low	0	0.01	1	μA
EN Input High	V _{IH}		1.35	1.8	V_{DD}	V
EN Input Low V _{IL}		0 0		0	0.45	_ v
AC Specifications						
RF Frequency Range	f_0	None		1575.42		MHz
Power Gain	S21		16.2	17.7	19.2	dB
Noise Figure NF		1000		0.90		dB
Input Return Loss	Loss S11			-4		dB
Output Return Loss	S22			-15		dB
Reverse Isolation	S12	Sweep Power -40dBm, 1575.42MHz		-30		dB
		Jammed signal @ 1463MHz and 1712MHz, - 20dBm		0.25		dB
Stability	Kf	Frequency range from 500MHz to 5GHz	1			
Input Power 1dB Compression Point	P1dB		K	-14		dBm
Input In-Band IP3	IIP3_inb	f ₁ = 1574.5MHz, f ₂ = 1575.5MHz, -30dBm		-1		dBm
Input Out-Band IP3 IIP3_outb $f_1 = 1712.7MHz, -20dBm, f_2 = 1850MHz, -650 IP3 = (2 x P1 + P2 + Gain1575MHz - IM3)/2$		$f_1 = 1712.7 \text{MHz}$, -20dBm, $f_2 = 1850 \text{MHz}$, -65dBm, IP3 = $(2 \times \text{P1} + \text{P2} + \text{Gain1575MHz} - \text{IM3})/2$		4		dBm

TYPICAL APPLICATION CIRCUIT

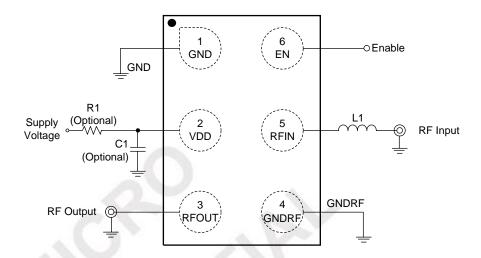


Figure 2. SGM13001 A Typical Application Circuit

Table 1. SGM13001A Function Table

Component	Vendor	Туре	Part Number & value
L1	Murata	Wired inductor, high Q	LQW15AN9N1, 9.1nH

EVALUATION BOARD LAYOUT

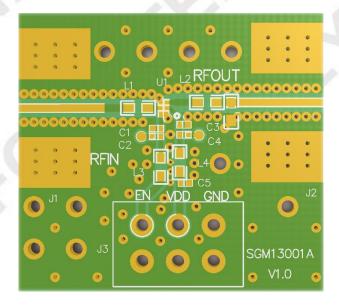


Figure 3. SGM13001A Evaluation Board Layout



For the latest specifications or product information:

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