

## ASDM30N75KQ

#### **30V N-CHANNEL MOSFET**

#### **Features**

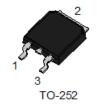
- 30V,75A
- $R_{DS(ON)}$ =4.8m $\Omega$  (Typ.) @ V<sub>GS</sub>=10V
- $R_{DS(ON)}$ =6.5 $m\Omega$  (Typ.) @  $V_{GS}$  =4.5V
- Advanced Trench Technology
- Provide Excellent R<sub>DS(ON)</sub> and Low Gate Charge

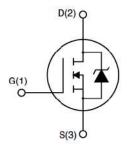
#### **Application**

- Load Switch
- PWM Application



<b>V</b> <sub>DS</sub>	30	V
<i>R</i> DS(on),TYP@ <i>V</i> GS=10 V	4.8	mΩ
lo	75	Α





### **Absolute Maximum Ratings** (Tc=25℃ unless otherwise specified)

Symbol	Parameter		Max.	Units
V <sub>DSS</sub>	Drain-Source Voltage		30	V
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
I_	I <sub>D</sub> Continuous Drain Current	T <sub>C</sub> = 25°C	75	Α
ID		T <sub>C</sub> = 100°C	50	Α
I <sub>DM</sub>	Pulsed Drain Current note1		300	Α
Eas	Single Pulsed Avalanche Energy note2		88	mJ
PD	Power Dissipation	T <sub>C</sub> = 25 °C	75	W
Rejc	Thermal Resistance, Junction to Case		2	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		65	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	$^{\circ}$



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### **Electrical Characteristics** ( $T_C$ =25 $^{\circ}$ C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units	
Off Charac	teristic			ı	I.		
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>D</sub> =250µA	30	_	-	V	
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V, V <sub>GS</sub> = 0V, T <sub>J</sub> =25℃	-	-	1	uA	
		V <sub>DS</sub> =24V, V <sub>GS</sub> = 0V, T <sub>J</sub> =125°C	-	-	10		
Igss	Gate to Body Leakage Current	V <sub>DS</sub> =0V,V <sub>GS</sub> = ±20V	-	-	±100	nA	
On Charac	teristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250µA	1.0	1.6	2.5	V	
1	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	4.8	6		
R <sub>DS(on)</sub>	note3	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	6.5	12	mΩ	
<b>g</b> FS	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =10A	-	20	-	S	
Dynamic C	Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	-	1560	-	pF	
Coss	Output Capacitance		-	220	-	pF	
Crss	Reverse Transfer Capacitance	f = 1.0MHz	-	178	-	pF	
Qg	Total Gate Charge	\/ -45\/ L -20A	-	11.1	-	nC	
Qgs	Gate-Source Charge	V <sub>DS</sub> =15V, I <sub>D</sub> =20A,	-	1.85	-	nC	
$Q_{gd}$	Gate-Drain("Miller") Charge	V <sub>GS</sub> =4.5V	-	6.8	-	nC	
Switching	Characteristics						
t <sub>d(on)</sub>	Turn-on Delay Time		-	7.5	-	ns	
tr	Turn-on Rise Time	V <sub>DS</sub> =15V,	-	14.5	-	ns	
t <sub>d(off)</sub>	Turn-off Delay Time	$I_D$ =15A, R <sub>G</sub> =3.3Ω,	-	35.2	-	ns	
<b>t</b> f	Turn-off Fall Time	V <sub>GS</sub> =10V	-	9.6	-	ns	
Drain-Sou	rce Diode Characteristics and Maxim	um Ratings					
Is	Is Maximum Continuous Drain to Source Diode Forward Current		-	-	75	А	
Ism	Maximum Pulsed Drain to Source Diode Forward Current		_	_	300	Α	
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> =30A	-	-	1.2	V	
trr	Body Diode Reverse Recovery Time		_	32	-	ns	
Qrr	Body Diode Reverse Recovery Charge	I <sub>S</sub> =30A,dI/dt=100A/µs	-	12	-	nC	

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition: TJ=25°C,VDD=25V,VGS=10V, L=0.1mH, IAS=42A, RG=25  $\Omega$
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%

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## **Typical Performance Characteristics**

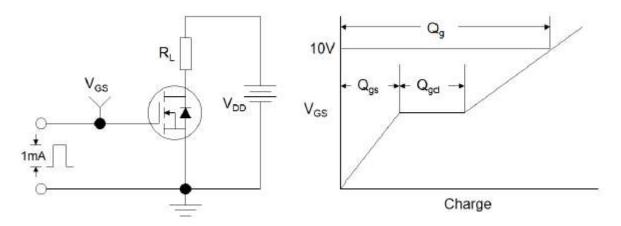


Figure1:Gate Charge Test Circuit & Waveform

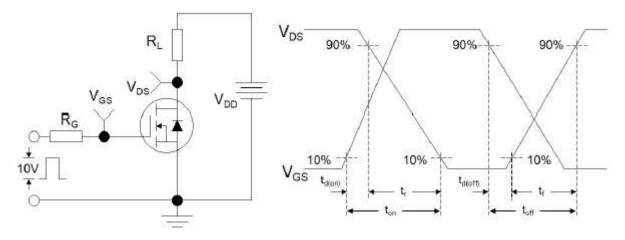


Figure 2: Resistive Switching Test Circuit & Waveforms

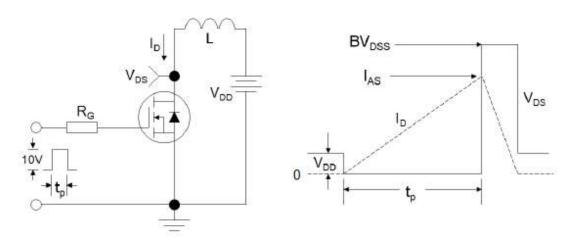
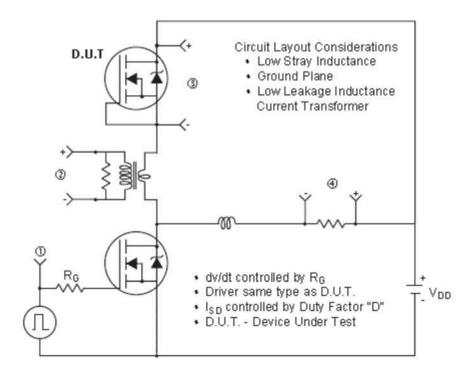


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms





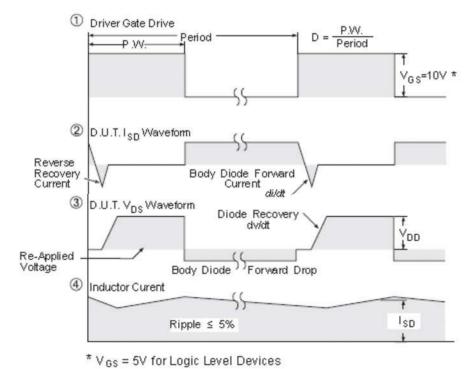


Figure 4:Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)





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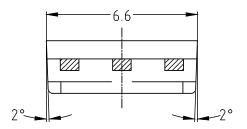
## **Ordering and Marking Information**

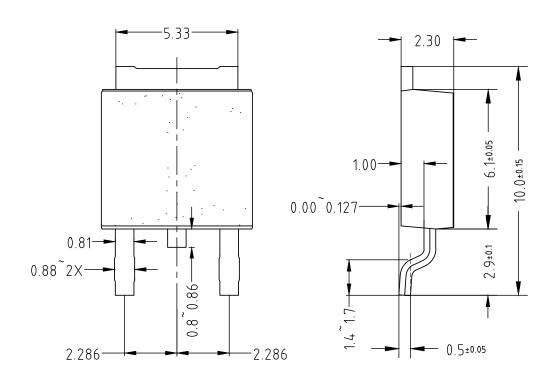
Ordering Device No.	Marking	Package	Packing	Quantity
ASDM30N75KQ-R	30N75	TO-252	Tape/ Reel	2500/Reel

PACKAGE	MARKING
TO-252	AS □□□→ Lot Number  30N75 □□□□→ Date Code



# TO-252







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