



DMN33D8LT

Product Summary

BV _{DSS}	Rds(on)	ID TA = +25°C
30V	5Ω @ V _{GS} = 4V	200mA
307	7Ω @ V _{GS} = 2.5V	115mA

Description

This new generation MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

Features

- N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- ESD Protected Gate 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

N-CHANNEL ENHANCEMENT MODE MOSFET

- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Qsuffix) part. A listing can be found at <u>https://www.diodes.com/products/automotive/automotiveproducts/.</u>
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMN33D8LTQ</u>)

Mechanical Data

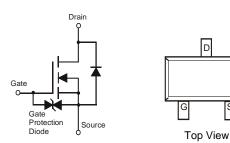
- Case: SOT523
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed Over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)





SOT523

Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN33D8LT-7	SOT523	3,000/Tape & Reel
DMN33D8LT-13	SOT523	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

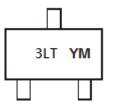
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



3LT = Product Type Marking Code YM = Date Code Marking Y = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Year	2014		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	В		_	J	K	L	М	Ν	0	Р	R	S
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	30	V
Gain-Source Voltage		Vgss	±20	V
Drain Current (Note 5)	Continuous	ID	115	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	240	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	521	°C/W
Total Power Dissipation (Note 6)	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	420	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

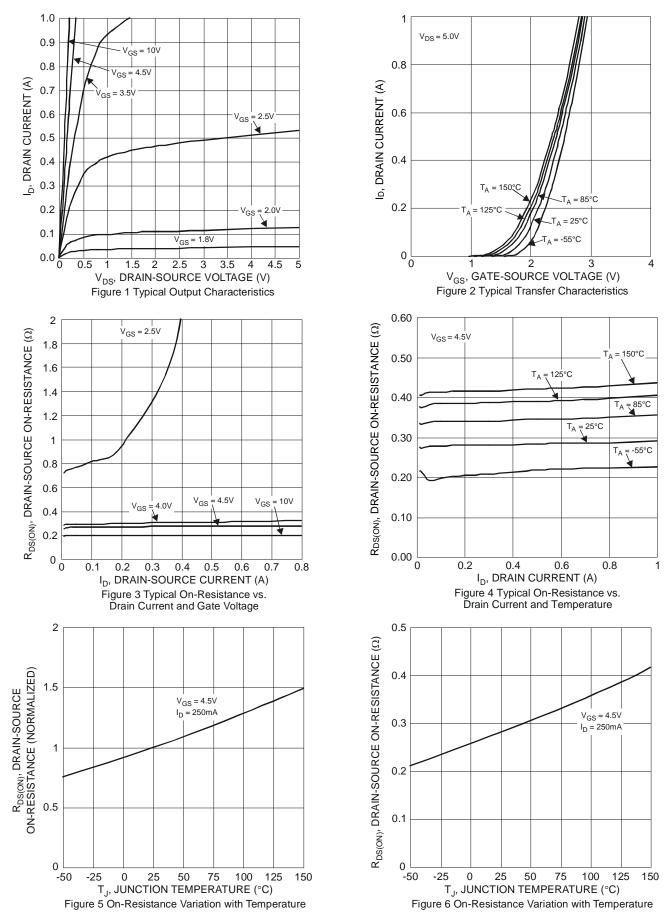


Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)			,				
Drain-Source Breakdown Voltage	BV _{DSS}	30		—	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS			1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Body Leakage	lgss	_		±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)				•		·	
Gate Threshold Voltage	Vgs(th)	0.8		1.5	V	V _{DS} = 3V, I _D = 100µA	
Static Drain-Source On-Resistance	Deserve	_		5	Ω	V _{GS} = 4V, I _D = 10mA	
Static Drain-Source On-Resistance	Rds(on)	_		7	Ω	$V_{GS} = 2.5V, I_D = 5mA$	
Diode Forward Voltage	Vsd	_		1.2	V	V _{GS} = 0V, I _S = 115mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	48			V _{DS} = 5V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	11		pF		
Reverse Transfer Capacitance	Crss	_	8	_			
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	0.55	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	1.23		nC		
Gate-Source Charge	Q _{gs}	_	0.14	_	nc	V _{DS} = 10V, I _D = 250mA	
Gate-Drain Charge	Q _{gd}	_	0.14				
Turn-On Delay Time	tD(ON)	_	2.9				
Turn-On Rise Time	t _R	_	2.6		1	V _{DD} = 30V, I _D = 0.2A, V _{GEN} = 10V,	
Turn-Off Delay Time	t _{D(OFF)}		18.2		ns	$R_{GEN} = 25\Omega$	
Turn-Off Fall Time	tF		13.6		1		

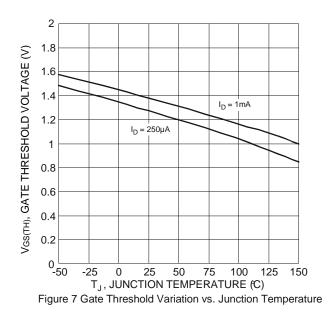
Notes:7. Short duration pulse test used to minimize self-heating effect.8. Guaranteed by design. Not subject to product testing.

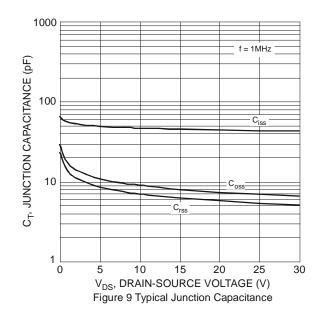


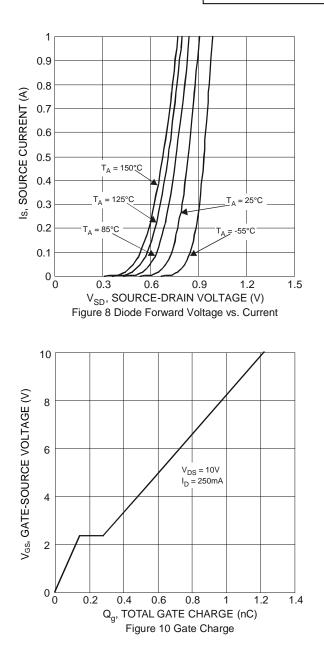


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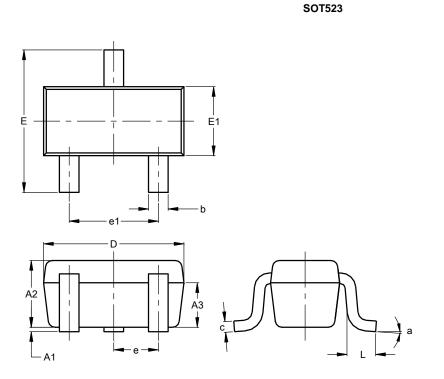






Package Outline Dimensions

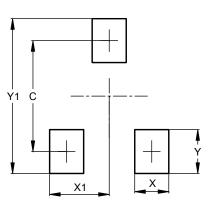
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT523							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
С	0.10	0.20	0.12				
D	1.50	1.70	1.60				
ш	1.45	1.75	1.60				
E1	0.75	0.85	0.80				
е	0.50 BSC						
e1	0.90	1.10	1.00				
L	0.20	0.40	0.33				
а	0°		8°				
Α	II Dimen	sions ir	ו mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80

SOT523



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