

ESD56281NXX
<http://www.sh-willsemi.com>
1-Line, Uni-directional, Transient Voltage Suppressor
Descriptions

The ESD56281NXX is a transient voltage suppressor designed to protect power interfaces. It is suitable to replace multiple discrete components in portable electronics.

The ESD56281NXX is specifically designed to protect USB port and power lines.

The ESD56281NXX is available in DFN1006-2L package. Standard products are Pb-free and Halogen-free.

Features

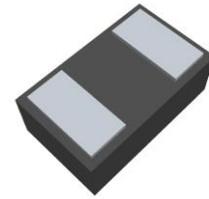
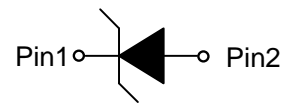
- Reverse stand-off voltage: 4.85V ~ 6.3V
- Surge protection according to IEC61000-4-5 see [Table 4](#)
- ESD protection according to IEC61000-4-2 ±30kV (contact and air discharge)
- Low clamping voltage
- Solid-state silicon technology

Applications

- Cell phone handsets and accessories
- Power supply protection
- Power management

Order information
Table 1.

Device	Package	Shipping	Marking
ESD56281N04-2/TR	DFN1006-2L	10000/Tape&Reel	<u>Y</u> *
ESD56281N05-2/TR	DFN1006-2L	10000/Tape&Reel	<u>Z</u> *
ESD56281N06-2/TR	DFN1006-2L	10000/Tape&Reel	<u>Q</u> *


DFN1006-2L (Bottom View)

Circuit diagram

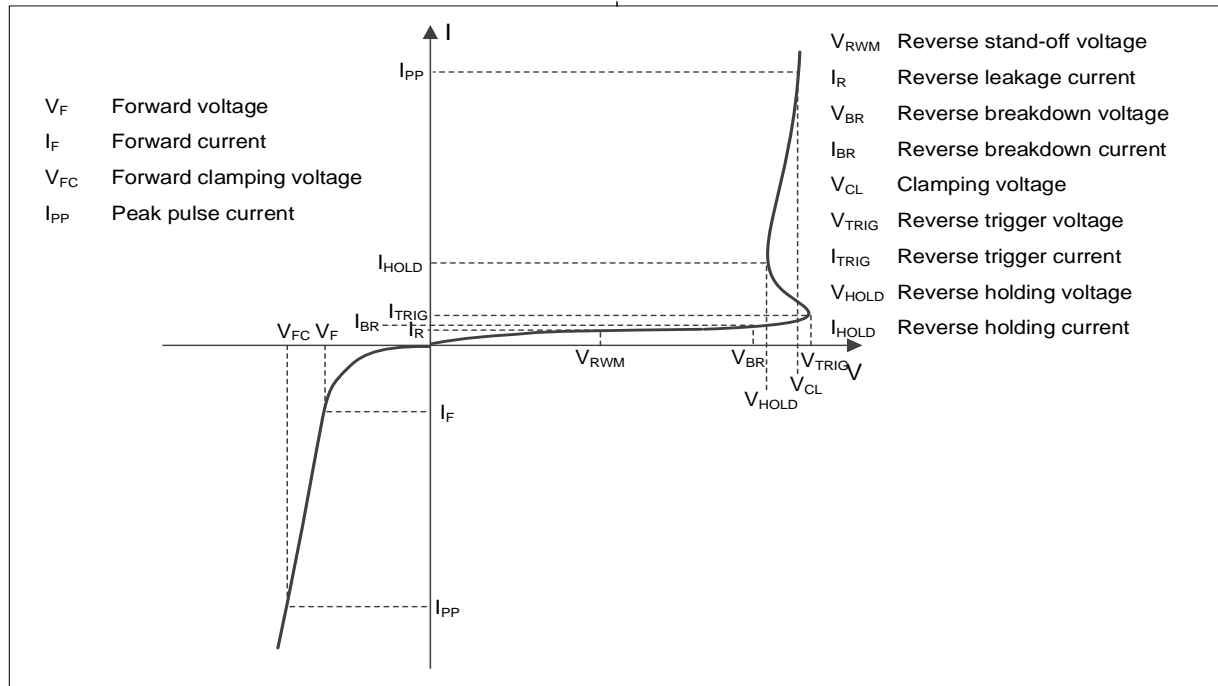

X = Device code (Y, Z, Q)

* = Month code (A~Z)

Marking (Top View)

Absolute maximum ratings
Table 2.

Parameter	Symbol	Rating	Unit
Peak pulse power (tp=8/20μs)	Ppk	350	W
ESD according to IEC61000-4-2 air discharge	V _{ESD}	±30	kV
ESD according to IEC61000-4-2 contact discharge		±30	
Junction temperature	T _J	125	°C
Operating temperature	T _{OP}	-40~85	°C
Lead temperature	T _L	260	°C
Storage temperature	T _{STG}	-55~150	°C

Electrical characteristics (T_A = 25°C, unless otherwise noted)

Definitions of electrical characteristics

Electrical characteristics ($T_A = 25^\circ\text{C}$, unless otherwise noted)
Table 3.

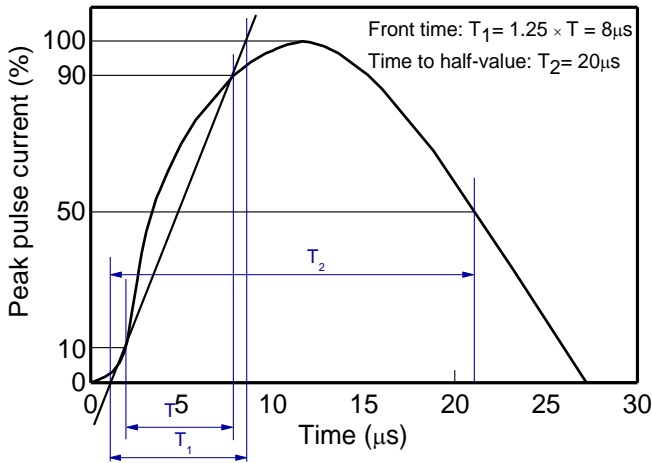
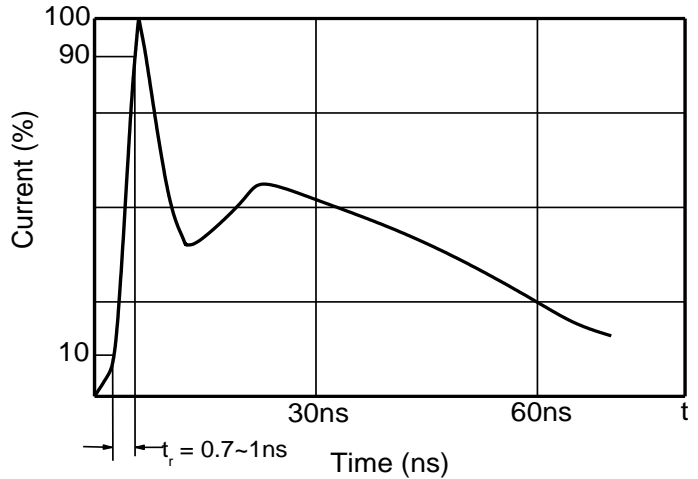
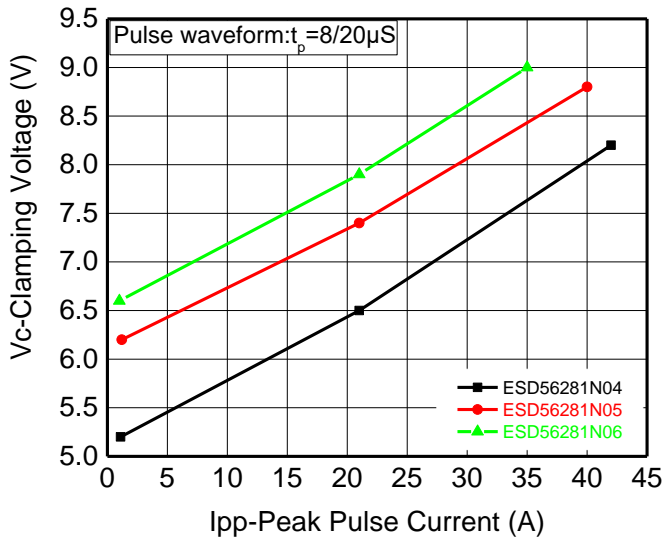
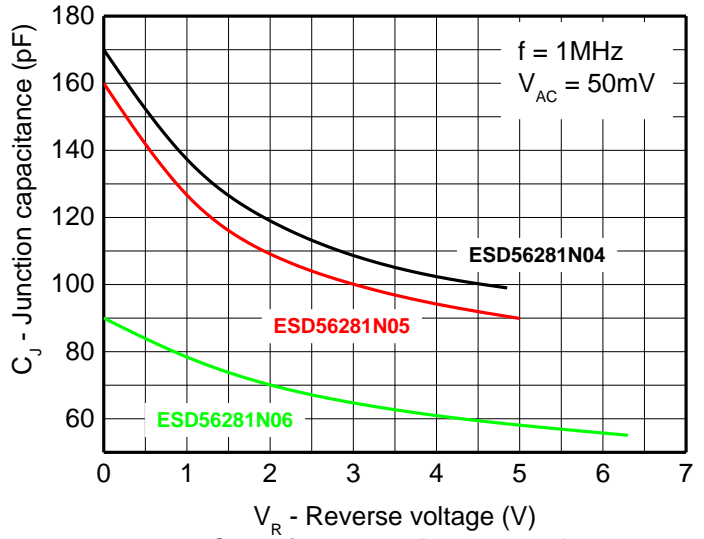
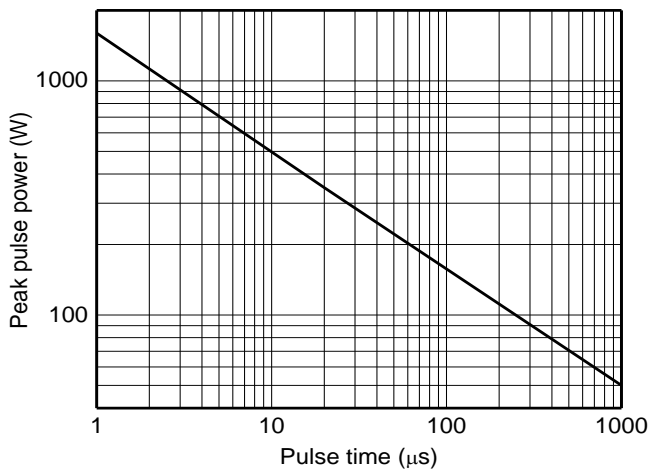
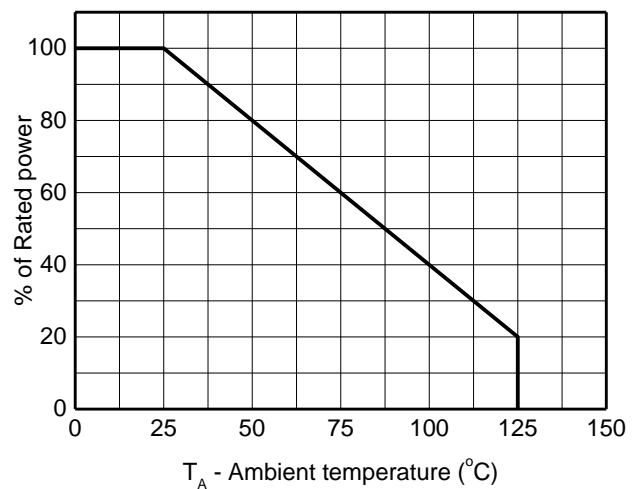
Type number	Reverse Stand off Voltage V_{RWM} (V)	Breakdown voltage V_{BR} (V) $I_{BR} = 1\text{mA}$			Reverse leakage current I_{RM} (nA) at V_{RWM}		Forward voltage V_F (V) $I_F = 20\text{mA}$		Junction capacitance $F=1\text{MHz}$, $V_R=0\text{V}$ (pF)	
	Max.	Min.	Typ.	Max.	Typ.	Max.	Min.	Max.	Typ.	Max.
ESD56281N04	4.85	5.0	5.5	6.0	1	100	0.5	1.1	170	180
ESD56281N05	5.5	5.6	6.4	7.5	1	100	0.5	1.1	160	170
ESD56281N06	6.3	6.5	7.5	9.0	1	100	0.5	1.1	90	100

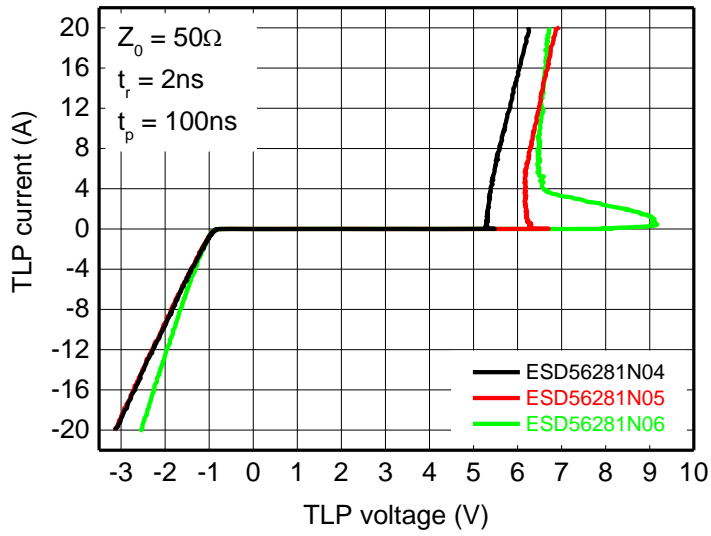
Table 4.

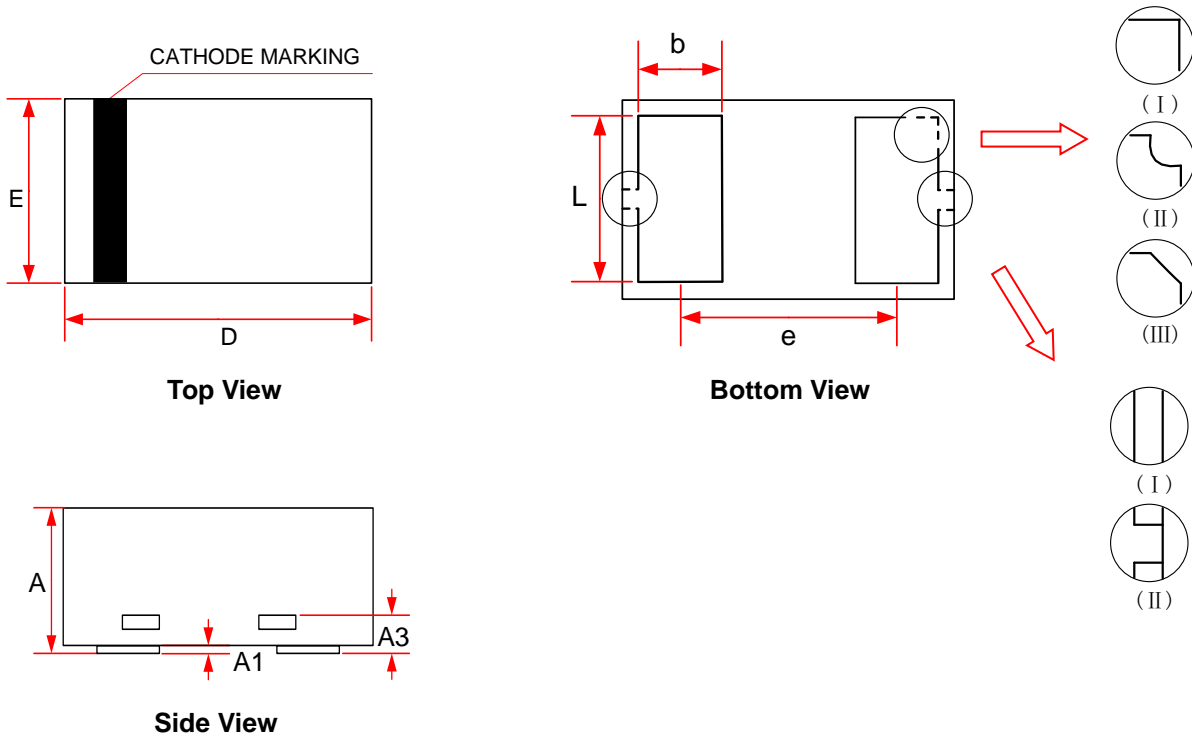
Type number	Rated peak pulse current I_{PP} (A) ¹⁾³⁾	Clamping voltage Typ. V_{CL} (V) at I_{PP} (A) ¹⁾³⁾	Clamping voltage Typ. V_{CL} (V) at $I_{PP} = 16\text{A}$, $t_p = 100\text{ns}$ ²⁾³⁾	Clamping voltage Typ. V_{CL} (V) at $V_{ESD} = 8\text{kV}$ ²⁾³⁾
ESD56281N04	42	8.2	6.0	7.5
ESD56281N05	40	8.8	6.6	8.0
ESD56281N06	35	9.0	6.6	8.0

Notes:

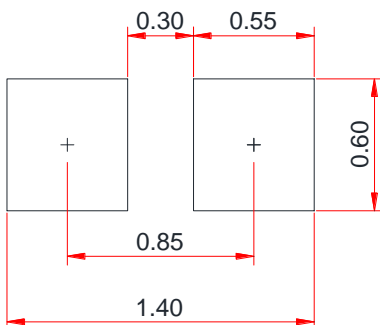
- 1) Non-repetitive current pulse, according to IEC61000-4-5. (8/20 μs current waveform)
- 2) Non-repetitive current pulse, according to IEC61000-4-2.
- 3) Measured from pin 1 to pin 2.

Typical characteristics ($T_A = 25^\circ\text{C}$, unless otherwise noted)

8/20 μs waveform per IEC61000-4-5

Contact discharge current waveform per IEC61000-4-2

Clamping voltage vs. Peak pulse current

Capacitance vs. Reverse voltage

Non-repetitive peak pulse power vs. Pulse time

Power derating vs. Ambient temperature

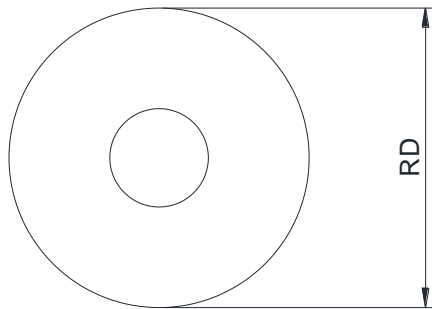
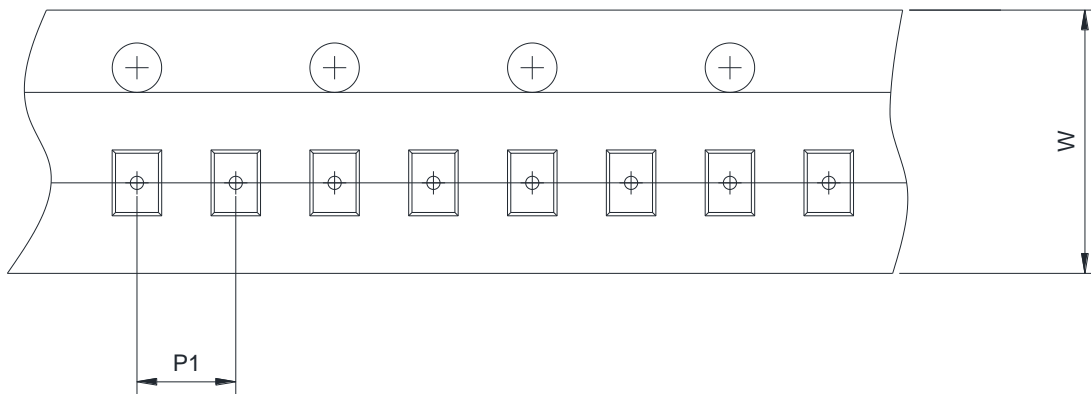
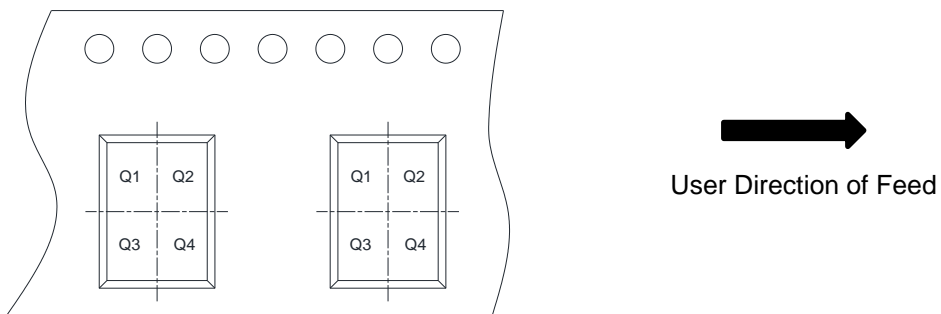
Typical characteristics ($T_A = 25^\circ\text{C}$, unless otherwise noted)

TLP Measurement

PACKAGE OUTLINE DIMENSIONS
DFN1006-2L


Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.340	0.450	0.530
A1	0.000	0.020	0.050
A3	0.125 Ref.		
D	0.950	1.000	1.075
E	0.550	0.600	0.675
b	0.200	0.250	0.300
L	0.450	0.500	0.550
e	0.650 BSC		

Recommended PCB Layout (Unit: mm)

Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

TAPE AND REEL INFORMATION
Reel Dimensions

Tape Dimensions

Quadrant Assignments For PIN1 Orientation In Tape


RD	Reel Dimension	<input checked="" type="checkbox"/> 7inch	<input type="checkbox"/> 13inch	
W	Overall width of the carrier tape	<input checked="" type="checkbox"/> 8mm	<input type="checkbox"/> 12mm	
P1	Pitch between successive cavity centers	<input checked="" type="checkbox"/> 2mm	<input type="checkbox"/> 4mm	<input type="checkbox"/> 8mm
Pin1	Pin1 Quadrant	<input checked="" type="checkbox"/> Q1	<input checked="" type="checkbox"/> Q2	<input type="checkbox"/> Q3 <input type="checkbox"/> Q4