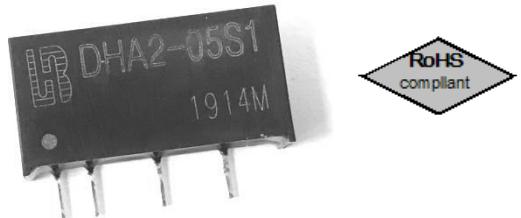




Features

- 7pin SIP Package with Industry-Standard Footprint
- Input / Output Isolation Voltage: 1.5kVDC
- High Efficiency
- Lead Free Design, RoHS Compliant
- Operating temperature: -40°C to +85°C
- Meet Safety Standard / Approval: IEC / EN60950-1



Applications

These converters are well suitable for battery operated equipment, measurement equipment, telecom, wireless network, Industry control system, everywhere where isolated, tightly regulated voltages and compact size are required.

Technical Specification

All specifications are typical at nominal input, full load and 25°C unless otherwise stated.

| Model Number | Input Voltage Range(V) | Output Voltage (V) | Output Current (mA) ⁽¹⁾ | Input Current (mA) | | Eff .(%) ⁽²⁾ Typ. | Capacitive Load, max. ⁽³⁾ (uF) |
|--------------|------------------------|--------------------|------------------------------------|--------------------|---------|------------------------------|---|
| | | | | Typ. | No Load | | |
| DHA2-05S1 | 4.75-5.25 Nominal:5 | 5 | 400 | 40 | 571 | 70 | 47 |
| DHA2-05S2 | | 12 | 167 | | 527 | 76 | 22 |
| DHA1R5-05S3 | | 15 | 100 | | 400 | 75 | 10 |
| DHA2-12S1 | 11.4-12.6 Nominal:12 | 5 | 400 | 20 | 228 | 73 | 47 |
| DHA2-12S2 | | 12 | 167 | | 214 | 78 | 22 |
| DHA1R5-12S3 | | 15 | 100 | | 164 | 76 | 10 |
| DHA2-15S1 | 14.3-15.8 Nominal:15 | 5 | 400 | 15 | 183 | 73 | 47 |
| DHA2-15S2 | | 12 | 167 | | 171 | 78 | 22 |
| DHA1R5-15S3 | | 15 | 100 | | 132 | 76 | 10 |
| DHA2-24S1 | 22.8-25.2 Nominal:24 | 5 | 400 | 10 | 114 | 73 | 47 |
| DHA2-24S2 | | 12 | 167 | | 106 | 79 | 22 |
| DHA1R5-24S3 | | 15 | 100 | | 80 | 78 | 10 |



BOTHHAND ENTERPRISE INC.

Isolated DC-DC Converters
DHA2 Series -- 2Watt

Input Specifications

| | |
|-------------------|------------|
| 5V nominal input | 4.75-5.25V |
| 12V nominal input | 11.4-12.6V |
| 15V nominal input | 14.3-15.8V |
| 24V nominal input | 22.8-25.2V |

| | |
|--------------|-----------|
| Input filter | Capacitor |
|--------------|-----------|

Environmental Specifications

| | |
|-------------------------------|-----------------|
| Operating ambient temperature | -40°C to +85°C |
| Maximum case temperature | +105°C |
| Storage temperature range | -55°C to +125°C |
| Relative humidity | 95%RH Max. |

Output Specifications

| | |
|------------------------------------|---|
| Output power | 2Watts Max. |
| Voltage accuracy | Nominal Vin and full load |
| | 5Vdc 4.75-5.15V |
| | 12Vdc 11.64-12.24V |
| | 15Vdc 14.7-15.3V |
| Minimum load | 0A |
| Line regulation | For Vin change of -5% +5% $\pm 0.25\%$ Max. |
| Load Regulation | 10%~100% load $\pm 1\%$ Max. |
| Ripple and Noise (20MHz Bandwidth) | 60mVp-p Max. |
| Maximum capacitive load | See table |
| Output short circuit protection | Automatic recovery |
| Temperature coefficient | $\pm 0.03\%/\text{°C}$ Typ. |

General Specifications

| | | |
|------------------------------|-----------------------------|---------------------|
| Efficiency | Nominal input and full load | See table |
| Isolation voltage | Input to output | 1500VDC (60 second) |
| Isolation resistance | 500VDC | 1000MΩ Min. |
| Isolation capacitance | | 30pF Typ. |
| Switching frequency | | 300kHz Max. |
| Reliability, calculated MTBF | | 2×10^6 Hrs |

Physical Specifications

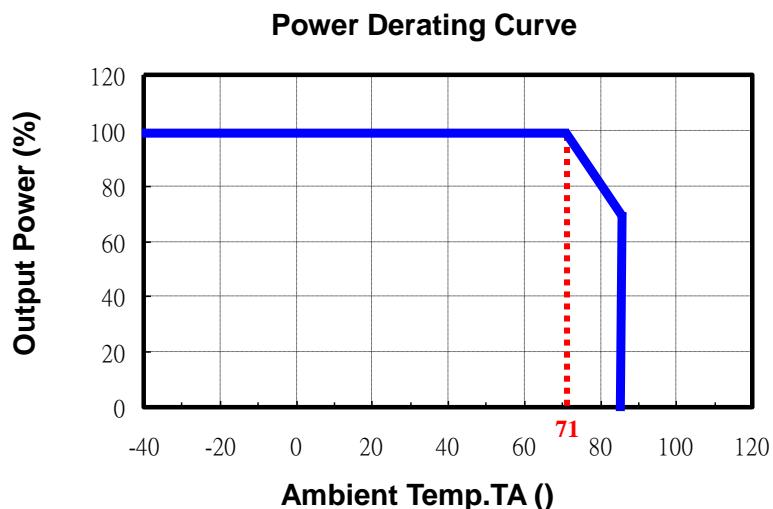
| | |
|------------------|--------------------|
| Case material | Plastic (UL94 V-0) |
| Potting material | PU (UL94 V-0) |
| Dimensions | 19.6×10.1×7.0 mm |
| Weight | 2.5g Typ. |

Note

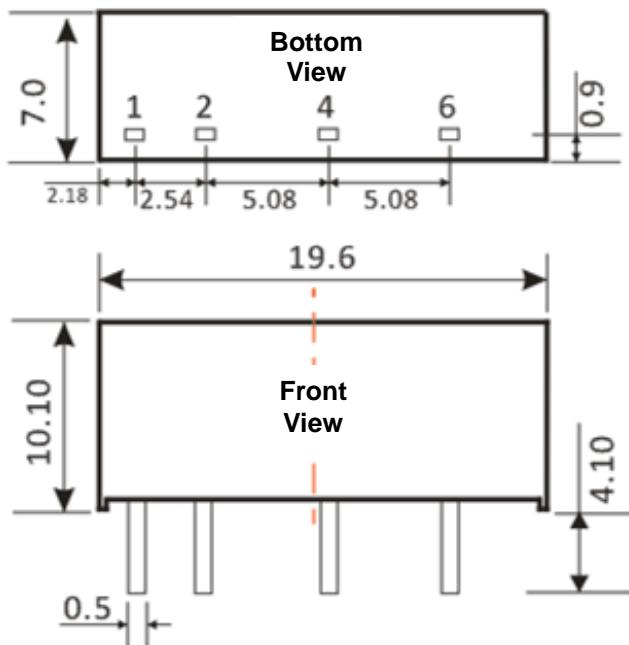
1. Io below this value will not damage these converters, however, they may not meet all listed specifications.
2. Typical value, tested at nominal input and full load.
3. For each output.
4. Specifications subject to change without notice.
5. In case of long input lines or hot plug-in requirements, we recommended to use an external low ESR capacitor (22uF) near to the converter's input pins.



Power Derating Curve



Mechanical Dimensions

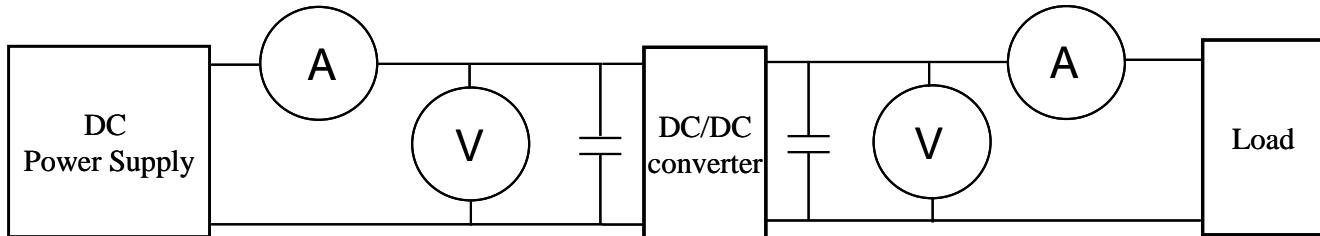


| Pin Assignment | |
|----------------|--------|
| Pin | Single |
| 1 | +Vin |
| 2 | -Vin |
| 4 | -Vout |
| 6 | +Vout |



Test Configurations

All specifications are typical at nominal input, full load and 25°C unless otherwise stated.



◎DC Power Supply: It offers a wide voltage and current range precisely.

◎Current meter (A): Accuracy → 200µA ~ 200mA 4 ranges + (0.2% rdg + 2 digits)

2000mA ~ 20A 2ranges + (0.3% rdg + 2 digits).

◎Voltage meter (V): Accuracy → ±(0.03% rdg + 4 digits).

◎Load: At full load.

◎Wires: The resistance of the wires must be small.

1. Input voltage range: Narrow input voltage range (±10%)、wide input voltage range (2:1 and 4:1)。

EX: Narrow input voltage range (±10%)

| | | |
|-------------------|---|-------------|
| 5V nominal input | → | 4.5~5.5V |
| 12V nominal input | → | 10.10~13.2V |
| 24V nominal input | → | 21.6~26.4V |

Wide input voltage range 2:1

| | | |
|-------------------|---|--------|
| 5V nominal input | → | 4.5~9V |
| 12V nominal input | → | 9~18V |
| 24V nominal input | → | 18~36V |
| 48V nominal input | → | 36~75V |

Wide input voltage range 4:1 (W)

| | | |
|-------------------|---|--------|
| 24V nominal input | → | 9~36V |
| 48V nominal input | → | 18~75V |

2. Input power :

$$P_{in} = V_{in} \times I_{in}$$

V_{in} : Input voltage

I_{in} : Input current

3. Output power :

$$P_{out} = V_{out} \times I_{out}$$

V_{out} : Output voltage

I_{out} : Output current

4. Efficiency :

$$\text{Efficiency} = \frac{P_{out}}{P_{in}} \times 100\%$$

P_{out}: Output power

P_{in}: Input power

5. Voltage accuracy:

$$\left| \frac{V_{out} - V_{out(\text{nominal})}}{V_{out}} \right| \times 100\%$$

V_{out} : Output voltage

V_{out(nominal)} : Nominal output voltage



6. Line regulation:

Narrow input voltage range ($\pm 10\%$) and unregulated output voltage series.

$$\text{Line regulation} = \frac{\Delta V_{\text{out}}}{\Delta V_{\text{in}}}$$

$$\Delta V_{\text{out}} = \frac{V_{\text{out}}(+10\%) - V_{\text{out}}(-10\%)}{V_{\text{out}}} \times 100\%$$

$V_{\text{out}}(+10\%)$: Output voltage at $V_{\text{in}} = 1.1 \times V_{\text{in(nominal)}} \& \text{full load}$

$V_{\text{out}}(-10\%)$: Output voltage at $V_{\text{in}} = 0.9 \times V_{\text{in(nominal)}} \& \text{full load}$

V_{out} : Output voltage at $V_{\text{in}} = V_{\text{in(nominal)}} \& \text{full load}$

$$\Delta V_{\text{in}} = \frac{V_{\text{in}}(+10\%) - V_{\text{in}}(-10\%)}{V_{\text{in(nominal)}}} \times 100\%$$

$V_{\text{in}}(+10\%)$: Input voltage = $1.1 \times V_{\text{in(nominal)}}$

$V_{\text{in}}(-10\%)$: Input voltage = $0.9 \times V_{\text{in(nominal)}}$

$V_{\text{in(nominal)}}$: Nominal Input voltage

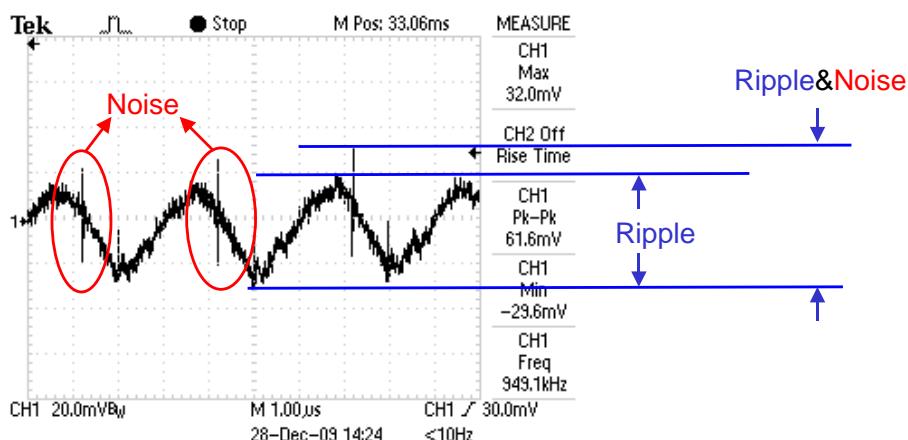
7. Load regulation :

$$\frac{|V_{\text{out(FL)}} - V_{\text{out(NL)}}|}{V_{\text{out(FL)}}} \times 100\%$$

$V_{\text{out(FL)}}$: Output voltage at full load

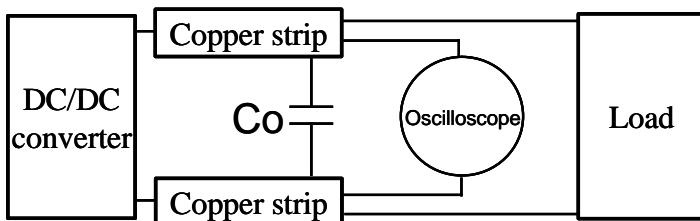
$V_{\text{out(NL)}}$: Output voltage at 25% full load or 10% full load

8. Ripple and Noise: as shown below. The bandwidth is 0-20MHz.



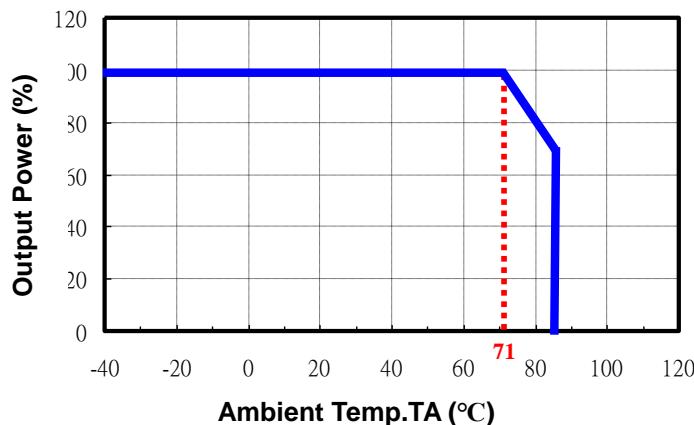


Output Ripple&Noise measurement test circuit: as shown below.



Co: usually 0.47uF.

9. Temperature derating curve: The DC-DC converter will operate over a wider temperature range if less power is drawn from the output and the device is already running. The temperature derating curve shows the operating power-temperature range. As shown below.



10. Switching frequency: The nominal operating frequency of the DC-DC converters.

11. Input to output isolation: The dielectric breakdown strength test between input and output circuits. This is the isolation voltage the device is capable of withstanding for a specified time, usually 1 second or 1 minute.

12. Input source impedance: The power module should be connected to low ac-impedance input source.

Highly inductive source impedances can affect the stability of the power module. In applications where power is supplied over long lines and output loading is high, it may be necessary to use a capacitor at the input to ensure startup. Capacitor mounted close to the power module helps ensure stability of the unit, it is recommended to use a good quality low Equivalent Series Resistance (ESR < 0.1Ω at 100KHz) capacitor of a 22uF for the power module.

