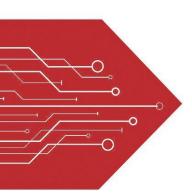
MSKSEMI















ESD

TVS

TSS

MOV

GDT

PLED

Broduct data speet









- 150 Watts peak pulse power (tp = $8/20\mu s$)
- Transient protection for high speed data lines to IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns)
- Working voltages : 5V
- Protects two bidirectional line
- Low operating and clamping voltages
- Solid-state silicon avalanche technology



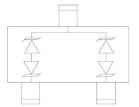
- Notebooks, Desktops, Servers and Video Graphics Cards
- USB Power & Data Line Protection
- Monitors and Flat Panel Displays
- I²C Bus Protection
- Portable Instrumentation
- Set Top Box

Electrical Characteristics@ Ta=25°C unless otherwise

	VDRM	IDRM	VBR	lr	Vc	lpp	Vc	Ipp	СО
P/N	V	μA	٧	mA	V	Α	V	Α	pF
		MAX	MIN		MAX		MAX		TYP
ESD5V0S2BT-MS	5	1	5.5	1	9.8	1	15	12	50

Maximum Rating @ Ta=25°C unless otherwise specified

Symbol	Parameter	Ratings	Units
Ррк	Peak Pulse Power (tp = 8/20μs)	150	Watts
TL	Lead Soldering Temperature	260(10sec.)	${\mathbb C}$
TJ	Operating Temperature	-55 to +125	$^{\circ}$
Тѕтс	Storage Temperature	-55 to +150	${\mathbb C}$





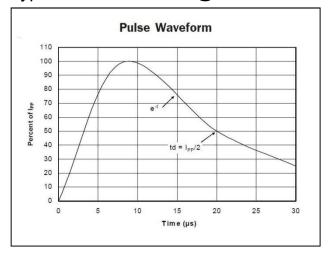
SOT-23

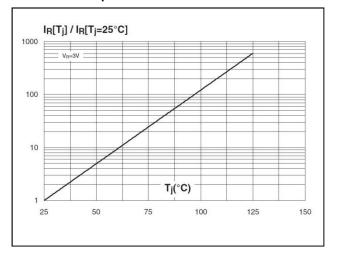


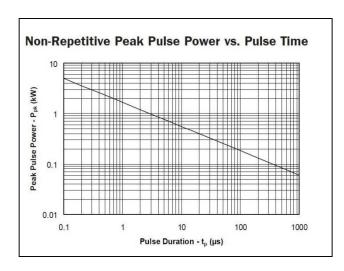


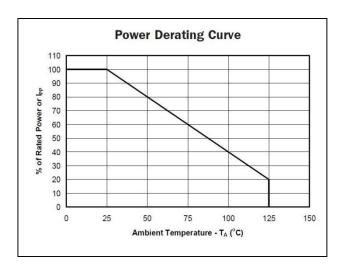


Typical Characteristics@ Ta=25°C unless otherwise specified



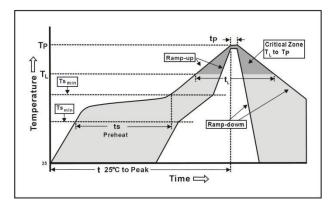






Soldering Parameters

Reflow Co	ndition	Fb — Free assembly		
	-Temperature Min (T _{s(Min)})	150°C		
Pre Heat	- Temperature Max (T _{s(Max)})	200°C		
	-Time (Min to max) (t _s)	60 – 180 secs		
Average ra (T _L) to pea	amp up rate (Liquidus) Temp k	3°C/second Max		
T _{s (Max)} to T	- Ramp-up Rate	3°C/second Max		
Reflow	-Temperature (T _L) (Liquidus)	217°C		
	-Temperature (t _L)	60 – 150 seconds		
Peak Tem	perature (T _p)	250+0/-5 °C		
Time within 5°C of actual peak Temperature (t _p)		20 – 40 seconds		
Ramp-dov	vm Rate	6°C/second Max		
Time 25°C	to peak Temperature (T _p)	8 minutes Max.		
Do not exc	ceed	260°C		



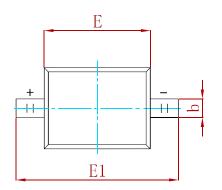


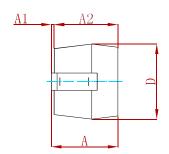


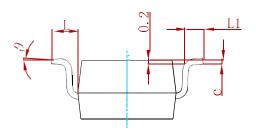




PACKAGE MECHANICAL DATA

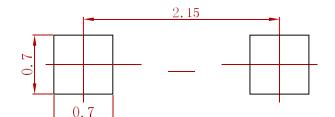






Comple ed	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α		1.000		0.039	
A 1	0.000	0.100	0.000	0.004	
A2	0.800	0.900	0.031	0.035	
b	0.250	0.350	0.010	0.014	
С	0.080	0.150	0.003	0.006	
D	1.200	1.400	0.047	0.055	
E	1.600	1.800	0.063	0.071	
E1	2.550	2.750	0.100	0.108	
L	0.47	REF.	0.019	REF.	
L1	0.250	0.400	0.010	0.016	
θ	0°	8°	0°	8°	

Suggested Pad Layout



Note:

- 1. Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
ESD5V0S2BT-MS	SOT-23	3000



Semiconductor

Compiance

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