

Lonten N-channel 70V, 70A, 8.5mΩ Power MOSFET

Description

These N-Channel enhancement mode power field effect transistors are using **trench** DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

- $70V,70A,R_{DS(on).max}=8.5m\Omega@V_{GS}=10V$
- ♦ Improved dv/dt capability
- Fast switching
- ♦ 100% EAS Guaranteed
- Green device available

Applications

- Motor Drives
- UPS
- ♦ DC-DC Converter

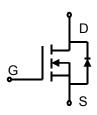
Product Summary

 $\begin{array}{ll} V_{DSS} & 70V \\ R_{DS(on).max} \textcircled{0} \ V_{GS} = 10V & 8.5 m\Omega \\ I_D & 70A \end{array}$

Pin Configuration



TO-252





N-Channel MOSFET

Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	70	V
Continuous drain current (T _C = 25°C)	1	70	Α
$(T_{C} = 100^{\circ}C)$	ID	44	A
Pulsed drain current ¹⁾	I _{DM}	280	Α
Gate-Source voltage	V _{GSS}	±20	V
Avalanche energy ²⁾	Eas	144	mJ
Power Dissipation	P _D	78	W
Storage Temperature Range	T _{STG}	-55 to +150	°C
Operating Junction Temperature Range	TJ	-55 to +150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{0JC}	1.6	°C/W
Thermal Resistance, Junction-to-Ambient ³⁾	R _{0JA}	125	°C/W

Package Marking and Ordering Information

Device	Device Package	Marking	Units/Reel	
LNG07R085H	TO- 252	LNG07R085H	2500	



Electrical Characteristics	T _J = 25°C unle	T _J = 25°C unless otherwise noted						
Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit		
Static characteristics				•				
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =250uA	70			V		
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	2.0	3.0	4.0	V		
	I _{DSS}	V _{DS} =70 V, V _{GS} =0 V, T _J = 25°C			1	μA		
Drain-source leakage current		V _{DS} =56 V, V _{GS} =0 V, T _J = 125°C			30	μΑ		
Gate leakage current, Forward	I _{GSSF}	V _{GS} =20 V, V _{DS} =0 V			100	nA		
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-20 V, V _{DS} =0 V			-100	nA		
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =30 A,T _J = 25°C		7.2	8.5	mΩ		
Forward transconductance	g fs	V _{DS} =5 V , I _D =30A		63		S		
Dynamic characteristics								
Input capacitance	C _{iss}	V 05.V.V 0.V		3570				
Output capacitance	Coss	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		248		pF		
Reverse transfer capacitance	C _{rss}	f= 1MHz		197				
Turn-on delay time	t _{d(on)}			17.8				
Rise time	tr	V _{DD} = 30V,V _{GS} =10V, I _D =30 A		27.6		ns		
Turn-off delay time	t _{d(off)}			102				
Fall time	t _f			28.6				
Gate resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		3.25		Ω		
Gate charge characteristics								
Gate to source charge	Q _{gs}			21.2				
Gate to drain charge	Q _{gd}	V _{DS} =30 V, I _D =30A,		17.9		nC		
Gate charge total	Qg	V _{GS} = 10 V		65.4				
Gate plateau voltage	V _{plateau}			5		V		
Drain-Source diode characteristic	s and Maxi	mum Ratings				1		
Continuous Source Current	Is				65	Α		
Pulsed Source Current ⁴⁾	Isм				260	Α		
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =30A, T _J =25℃			1.2	V		
Reverse Recovery Time	t _{rr}	I _S =25A, di/dt=100A/us,		28.4		ns		
Reverse Recovery Charge	Qrr	T _J =25℃		21.3		nC		

Notes:

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2: V_DD=35V, V_GS=10V, L=0.5mH, I_AS=24A, R_G=25\Omega, Starting T_J=25 $^{\circ}\!\!\mathrm{C}$.
- 3: The value of R_{thJA} is measured by placing the device in a still air box which is one cubic foot.
- 4. Pulse Test: Pulse Width $\leq 300 \, \mu \, \text{s}$, Duty Cycle $\leq 2\%$.

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Electrical Characteristics Diagrams

Figure 1. Typ. Output Characteristics

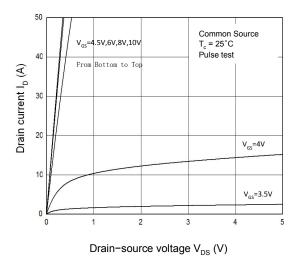


Figure 3. On-Resistance vs.Drain Current

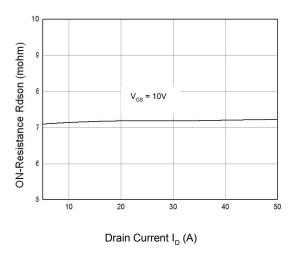


Figure 5.Body-Diode Characteristics

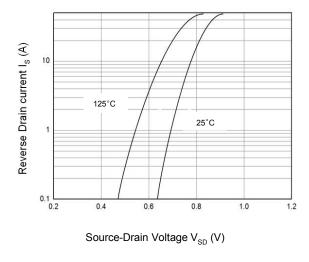


Figure 2. Transfer Characteristics

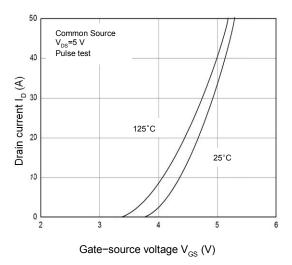


Figure 4.On-Resistance vs.Temperature

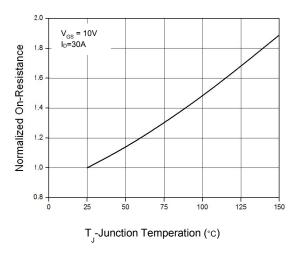


Figure 6.Capacitance Characteristics

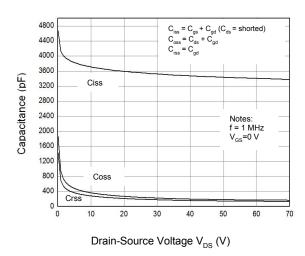




Figure 7.Gate Charge Characteristics

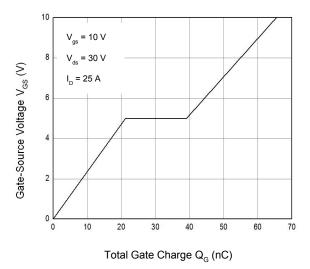


Figure 8.Drain Current Derating

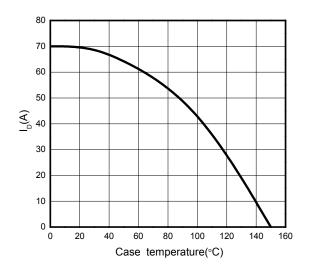


Figure 9.Power Dissipation vs.Temperature

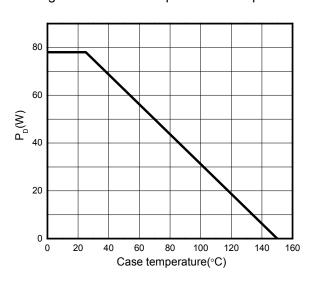


Figure 10: Safe Operating Area

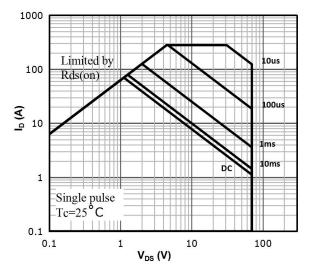
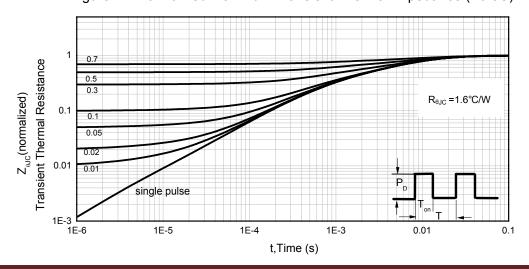


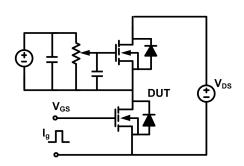
Figure 11. Normalized Maximum Transient Thermal Impedance (RthJC)

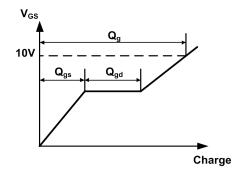




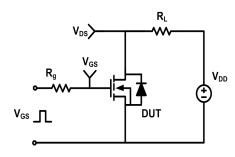
Test Circuit & Waveforms

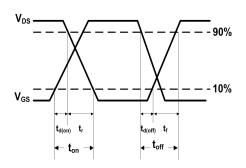
Gate Charge Test Circuit & Waveform



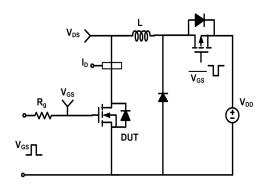


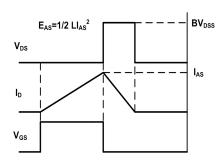
Resistive Switching Test Circuit & Waveform



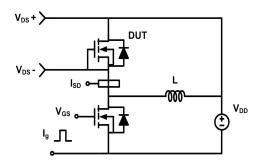


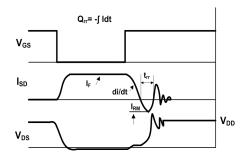
Unclamped Inductive Switching (UIS) Test Circuit & Waveform





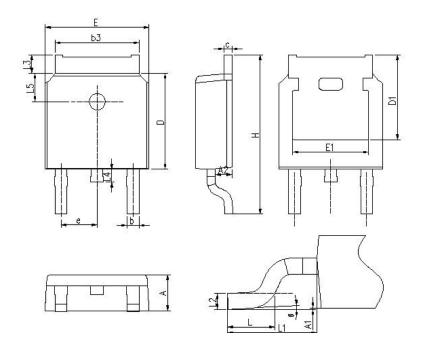
Diode Recovery Test Circuit & Waveform







Mechanical Dimensions for TO-252



DIMENSIONS IN MILLITMETERS		DIMENSIONS IN INCHES		
SYMBOL	MIN	MAX	MIN	MAX
A	2. 18	2.4	0.086	0.094
A1	-	0.2	-	0.008
A2	0.9	1. 17	0. 035	0.046
b	0.65	0.9	0. 026	0.035
b3	4. 95	5. 5	0. 195	0. 217
С	0. 43	0.89	0.017	0.035
D	5. 97	6. 22	0. 235	0. 245
D1	5. 21	_	0. 205	_
E	6. 35	6.8	0. 250	0. 268
E1	4. 32	_	0.170 -	
е	2. 286BSC		0. 09BSC	
Н	9.4	10.5	0.370	0.413
L	0.38	1. 78	0.015	0.070
L1	2. 90BSC		0. 114BSC	
L2	0. 51BSC		0. 020BSC	
L3	0.88	1. 28	0. 035	0.050
L4	-	1. 02	-	0.040
L5	1. 65	1. 95	0.065	0.077
θ	0°	10°	0°	10°



Version Information

LNG07R085H

Revision:2020-11-19,Rev 0.1

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