TOSHIBA Photocoupler IRED & Photo-Triac

TLP160G

Triac Drive Programmable Controllers AC-Output Module Solid State Relay

The TOSHIBA mini flat coupler TLP160G is a small outline coupler, suitable for surface mount assembly.

The TLP160G consists of a photo triac, optically coupled to an infrared emitting diode.

- Peak off-state voltage: 400 V (min)
- Trigger LED current: 10 mA (max)
- On-state current: 70 mA (max)
- Isolation voltage: 2500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A
 File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

Note 1: When a VDE approved type is needed, please designate the **Option(V4)**.

Unit: mm 3 1 4 6 3.6 ± 0.2 11-4C3

Weight: 0.09 g (typ.)

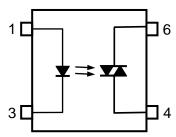
Trigger LED Current

	Classification (Note 1)	Trigger LED	Marking of		
		V _T ≠3V,	Classification		
		Min//	Max	Classification	
	(IFT5)	-\\	5.0	T5	
	(IFT7)	~ ^ -	7.0	T5, T7	
	Standard	\\	10.0	T5, T7, blank	

Note 1: (IFT5); TLP160G (IFT5)

Note: Application type name for certification test, please use standard product type name, i.e. TLP160G(IFT5): TLP160G

Pin Configurations (top view)



- 1. Anode
- 3. Cathode
- 4. Triac Terminal
- 6. Triac Terminal

Start of commercial production 1988-04

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit	
	Forward current	lF	50	mA		
	Forward current derating (Ta ≥ 5	ΔI _F / °C	-0.7	mA / °C		
	Peak forward current (100µs pul	IFP	1	Á		
LED	Reverse voltage		V _R	5	V (
	Diode power dissipation		PD	100	mW	
	Diode power dissipation derating	∆P _D /°C	-1.4	mW/°C		
	Junction temperature	Tj	125	Ç		
	Off– state output terminal voltag	V_{DRM}	400) V		
	On-state RMS current	Ta=25°C	IT(DMO)	70) MA	
		Ta=70°C	IT(RMS)	40) IIIA	
_	On-state current derating (Ta ≥ 2	ΔIT / °C	-0.67	mA / °C		
Jetector	Peak on-state current (100µs pu	I _{TP}	(7/2)	A		
Det	Peak non-repetitive surge currer (Pw=10ms)	ITSM	1.2	A		
	Output power dissipation		P ₀	200	mW	
	Output power dissipation derating	g (Ta ≥ 25°C)	ΔP ₀ /°C	-2.0	mW / °C	
	Junction temperature	Ti	115	Ç		
Storage temperature range			T _{stg}	-55 to 125	(°c)	
Operating temperature range			Topr	-40 to 100	°C	
Lead soldering temperature (10 s)			T _{sol}	260) °C	
Isolatio	on voltage (AC, 60 s, R.H. ≤ 60 %))) BVs	2500	Vrms		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal device; Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	_	_	120	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	ITP	_	_	1	Α
Operating temperature	T _{opr}	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



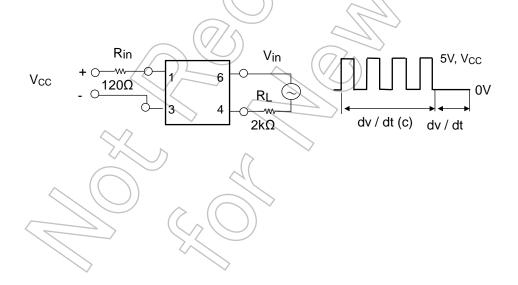
Electrical Characteristics (Ta = 25°C)

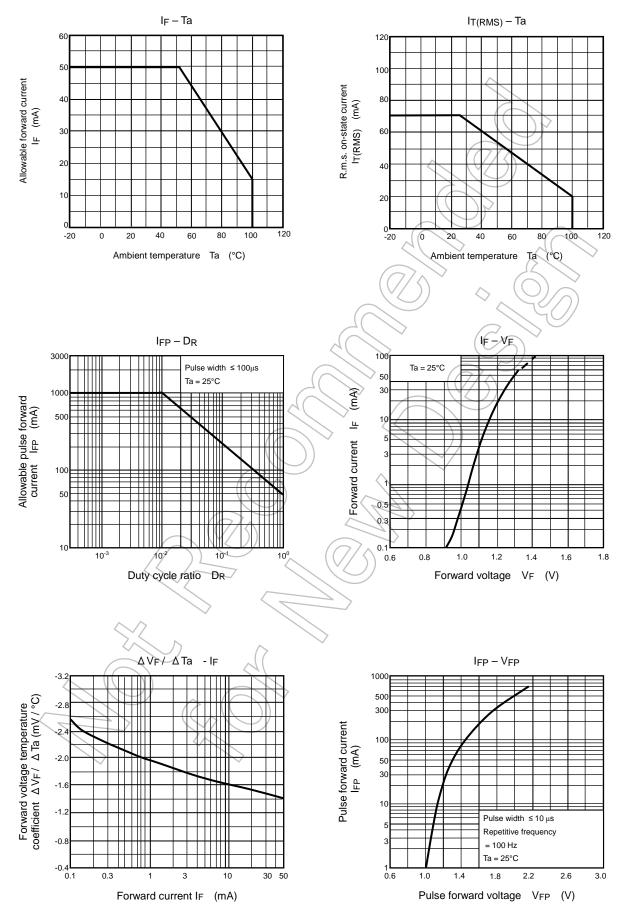
	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	Ст	VF = 0 V, f = 1 MHz	<u> </u>	30	_	pF
Detector	Peak off-state current	IDRM	V _{DRM} = 400 V		10	1000	nA
	Peak on-state voltage	Vтм	I _{TM} = 70 mA		1.7	2.8	V
	Holding current	lн	(2))/<	0.6	_	mA
	Critical rate of rise of off–state voltage	dv / dt	V _{in} = 120 Vrms, Ta = 85 °C (Fig.1)	200	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	I _T = 15 mA, V _{in} = 30 Vrms (Fig.1)	_	0.2	_	V / µs

Coupled Electrical Characteristics (Ta = 25°C)

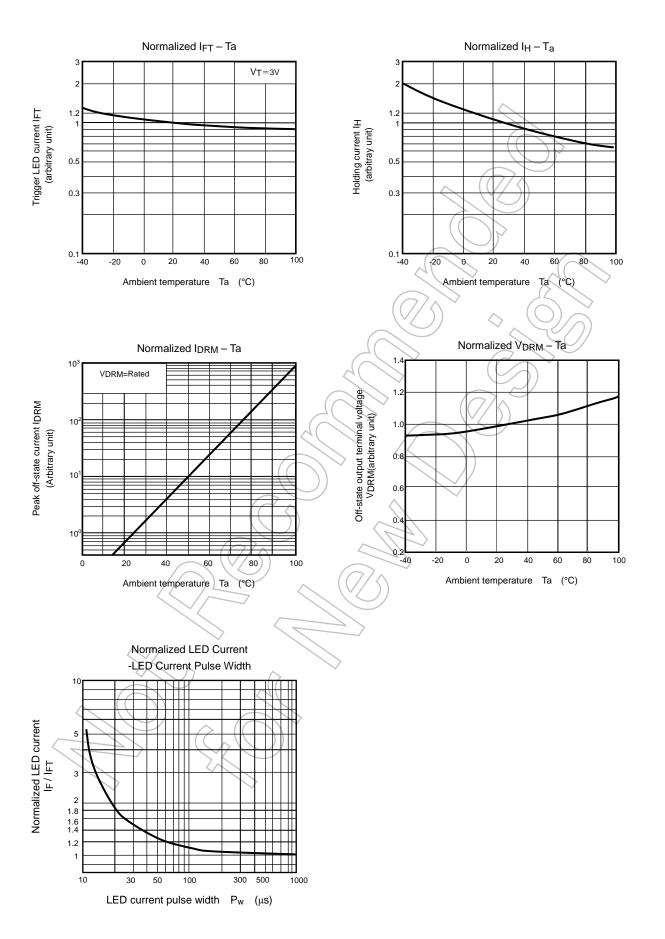
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	V _T = 3V		5	10	mA
Capacitance input to output	Cs	Vs = 0 V, f = 1 MHz		0.8	_	pF
Isolation resistance	Rs	Vs = 500 V, R.H. ≤ 60 %	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVs	AC, 60 s	2500	_	_	Vrms
Turn-on time	ton	$V_D = 6 \rightarrow 4 \text{ V, R}_L = 100 \Omega$ $I_F = \text{Rated } I_{FT} \times 1.5$	/ _	30	100	μs

Fig.1: dv / dt Test Circuit





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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