

Photocouplers Photorelay

TLP220D

1. Applications

- · Mechanical relay replacements
- · Security Systems
- · Measuring Instruments
- · Factory Automation (FA)
- · Amusement Equipment
- · Smart Meters
- · Electricity Meters

2. General

The TLP220D photorelay consists of a photo MOSFET optically coupled to an infrared LED. It is housed in a 4-pin DIP package. It provides an isolation voltage of 5000 Vrms, making it suitable for applications that require reinforced insulation.

3. Features

- (1) Normally open (1-Form-A)
- (2) OFF-state output terminal voltage: 200 V (min)
- (3) Trigger LED current: 2 mA (max)
- (4) ON-state current: 250 mA (max)
- (5) ON-state resistance: 8Ω (max)
- (6) Isolation voltage: 5000 Vrms (min)
- (7) Safety standards

UL-recognized: UL 1577, File No.E67349

cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349

VDE-approved: EN 60747-5-5 (Note 1)

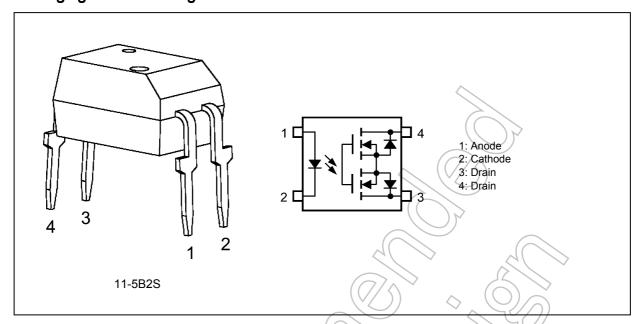
CQC-approved: GB4943.1, GB8898 Japan Factory

Note 1: When a VDE approved type is needed, please designate the Option (D4).

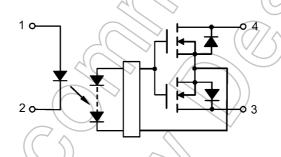




4. Packaging and Pin Configuration



5. Internal Circuit



6. Mechanical Parameters

Characteristics	7.62-mm Pitch TLP220D	10.16-mm Pitch TLP220DF	Unit
Creepage distances	7.0 (min)	8.0 (min)	mm
Clearance distances	7.0 (min)	8.0 (min)	
Internal isolation thickness	0.4 (min)	0.4 (min)	



7. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

	Characterist	Symbol	Note	Rating	Unit	
LED	Input forward current		I _F		30	mA
	Input forward current derating	$(T_a \ge 25 ^{\circ}C)$	$\Delta I_F/\Delta T_a$		-0.3	mA/°C
	Input forward current (pulsed)	(100 μs pulse, 100 pps)	I _{FP}	\wedge	1	Α
	Input reverse voltage		V_R		5	V
	Input power dissipation		P_{D}		50	mW
	Input power dissipation derating	$(T_a \ge 50 \text{ °C})$	$\Delta P_D/\Delta T_a$		-0.5	mW/°C
	Junction temperature		T _j ((77^	125	°C
Detector	OFF-state output terminal voltage		VOFF		200	V
	ON-state current		ION		250	mA
	ON-state current derating	$(T_a \ge 25 ^{\circ}C)$	ΔΙ _{ΟΝ} /ΔΤ _a	7	-2.5	mA/°C
	ON-state current (pulsed)	(t = 100 ms, duty = 1/10)	I _{ONP}		750	mA
	Output power dissipation	d	Po		500	mW
	Output power dissipation derating	(T _a ≥ 25 °C)	$\Delta P_{O}/\Delta T_{a}$		-5.0	mW/°C
	Junction temperature	((//-	T_j		125	°C
Common	Storage temperature		T _{stg}		-55 to 125	°C
	Operating temperature		T _{opr}		-40 to 85	°C
	Lead soldering temperature	(10 s)	T _{sol}		260	°C
	Isolation voltage	(AC, 60 s, R.H. ≤ 60 %)	BV _S	(Note 1)	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

8. Recommended Operating Conditions (Note)

Characteristics	Symbol	Note	Min	Тур.	Max	Unit
Supply voltage	V_{DD}				160	V
Input forward current	I _F		3	5	15	mA
ON-state current	I _{ON}				250	
Operating temperature	T _{opr}		-20	_	65	°C

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this datasheet should also be considered.

9. Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

	Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
LED	Input forward voltage	V _F		I _F = 10 mA	1.45	1.63	1.75	V
	Input reverse current	I _R		V _R = 5 V			10	μА
	Input capacitance	Ct		V = 0 V, f = 1 MHz	_	40		pF
Detector	OFF-state current	I _{OFF}		V _{OFF} = 200 V	_	_	1	μА
	Output capacitance	C _{OFF}		V = 0 V, f = 1 MHz		90		pF



10. Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}		I _{ON} = 250 mA	_	0.3	2	mA
Return LED current	I _{FC}		I _{OFF} = 10 μA	0.1			mA
ON-state resistance	R _{ON}	(Note 1)	I_{ON} = 250 mA, I_F = 5 mA, Continuous	<u> </u>	5	8	Ω

Note 1: Thermally saturated state.

11. Isolation Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	C _S	(Note 1)	V _S = 0 V, f = 1 MHz		0.8		pF
Isolation resistance	R _S	(Note 1)	V _S = 500 V, R.H. ≤ 60 %	1012	1014	_	Ω
Isolation voltage	BVS	(Note 1)	AC, 60 s	5000			Vrms

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

12. Switching Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Typ	Max	Unit
Turn-on time	t _{ON}		See Fig. 12.1.		0.5	1	ms
Turn-off time	t _{OFF}		$R_L = 200 \Omega$, $V_{DD} = 20 V$, $I_F = 5 mA$	/=))	0.2	1	

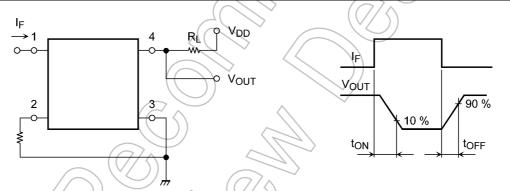


Fig. 12.1 Switching Time Test Circuit and Waveform

80

0.5

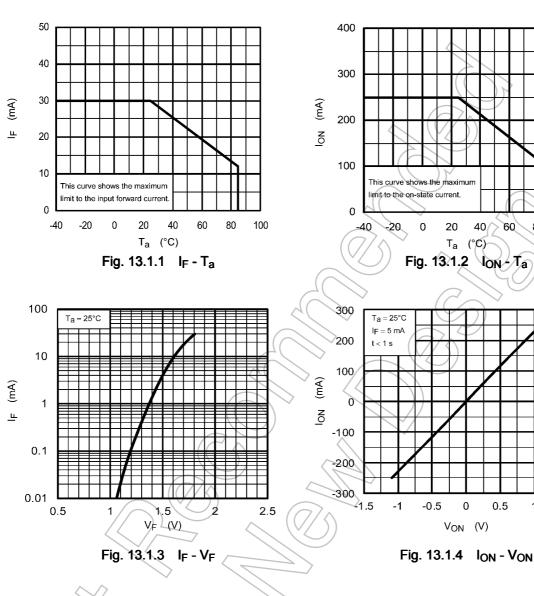
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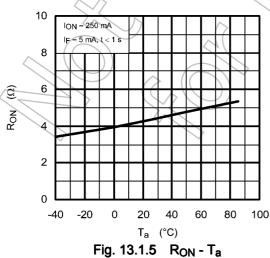
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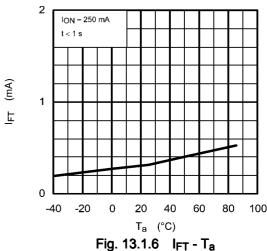


13. Characteristics Curves

13.1. Characteristics Curves (Note)

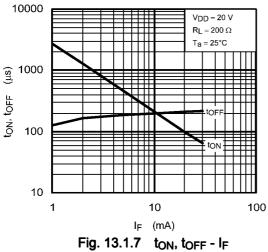


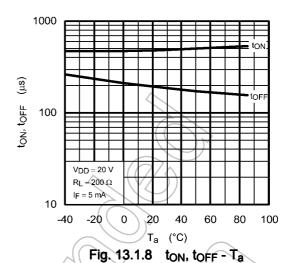


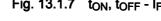


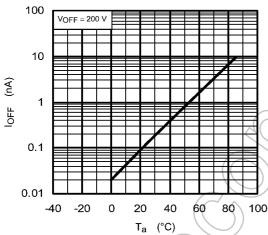
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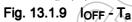


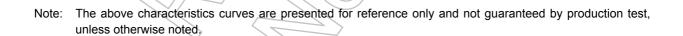










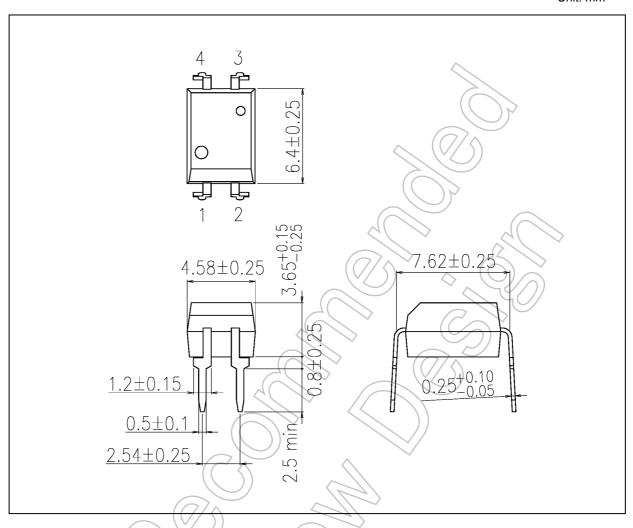




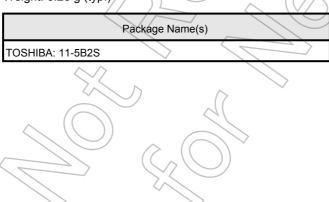


Package Dimensions

Unit: mm



Weight: 0.26 g (typ.)



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