## 1. Features and Benefits

- Wide operating voltage range: from 2.7 V to 24 V
- Maximum application flexibility by reprogramming of all sensor functions
- Customer End-of-Line programming via VDD pin in the application module for system design optimization
- Lateral sensing by IMC technology
- Wide programmable magnetic Latch/Switch range
- Programmable magnetic hysteresis, active pole, output polarity
- Programmable permanent magnet Temperature Coefficient compensation from 0 to -2000ppm/ ${ }^{\circ} \mathrm{C}$
- Integrated self-diagnostic functions activating dedicated Safe Mode
- Reverse supply voltage protection
- Under-Voltage Lockout protection
- Thermal protection
- Integrated capacitor for PCB less designs
- HW component Qualified according to ISO26262-8:13 for use in safety critical systems


## 2. Application Examples

- Automotive, Consumer and Industrial
- Wiper motor
- Brake light switch
- Window lifter
- Door lock
- Seatbelt buckle
- Seat positioning
- Sunroof/Tailgate opener
- Transmission applications
- Electrical power steering
- Speed sensing (motorcycles)

3. Ordering Information

| Product Code | Temperature Code | Package Code | Option Code | Packing Form <br> Code |
| :---: | :---: | :---: | :---: | :---: |
| MLX92242 | L | UA | AAA-000 | BU |
| MLX92242 | L | SE | AAA-000 | RE |
| MLX92242 | L | UA | AAA-100 | BU |
| MLX92242 | L | SE | AAA-200 | RE |
| MLX92242 | L | UA | AAA-300 | BU |

## Legend:

Temperature Code:
Package Code:
Option Code:

Packing Form: Ordering example:
$\mathrm{L}\left(-40^{\circ} \mathrm{C}\right.$ to $\left.150^{\circ} \mathrm{C}\right)$
UA = TO92-3L | SE = TSOT-3L
000 => Perpendicular sensitive
100 => Integrated capacitor (UA package only)
200 => IMC (SE package only)
$300=>$ Integrated capacitor and IMC (UA package only)
BU = Bulk | RE = Reel | CA=Papertape in Ammopack | CR=Papertape on Reel
MLX92242LUA-AAA-x00-BU

## 4. Functional Diagram



## 5. General Description

The Melexis MLX92242 is based on the Melexis Hall-effect switch latest platform, designed in mixed signal submicron CMOS technology. The device integrates a voltage regulator, Hall sensor with advanced offset cancellation system and a current sink-configured output driver and integrated capacitor all in a single package. Lateral sensing option is available by using the Melexis IMC technology.

Based on the proven in use platform, the magnetic core is using an improved offset cancellation system allowing faster and more accurate processing while being temperature insensitive and stress independent. In addition a pre-programmable temperature coefficient is implemented to compensate the natural behavior of certain types of magnets becoming weaker with rise in temperature.

The included voltage regulator operates from 2.7 to 24 V , hence covering a wide range of applications. With the built-in reverse voltage protection, a serial resistor or diode on the supply line is not required so that even remote sensors can be specified for low voltage operation down to 2.7 V while being reverse voltage tolerant.
In an event of a drop below the minimum supply voltage during operation, the under-voltage lock-out protection will automatically freeze the device, preventing the electrical perturbation to affect the magnetic measurement circuitry. The output current state is therefore only updated based on a proper and accurate magnetic measurement result.
The two-wire interface not only saves one wire, but also allows implementation of diagnostic functions as reverse polarity connection and malfunction detection.
The on-chip thermal protection also switches off the output if the junction temperature increases above an abnormally high threshold. It will automatically recover once the temperature decreases below a safe value.

The MLX92242 is delivered in a Green and RoHS compliant Plastic Single-in-Line (TO-92 flat) for through-hole mount, or PCB-less design with integrated capacitor or in 3-pin Thin Small Outline Transistor (TSOT) for surface mount process.

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## 6. Glossary of Terms

Tesla Units for the magnetic flux density, $1 \mathrm{mT}=10$ Gauss
TC Temperature Coefficient in ppm/ ${ }^{\circ} \mathrm{C}$
NC Not Connected
POR Power on Reset
IMC integrated magnetic concentrator (lateral sensing)

## 7. Absolute Maximum Ratings

| Parameter | Symbol | Value | Units |
| :---: | :---: | :---: | :---: |
| Supply Voltage ${ }^{(1,2)}$ | $V_{D D}$ | +28 | V |
| Supply Voltage (Load Dump) ${ }^{(1,4)}$ | $V_{D D}$ | +32 | V |
| Supply Current ${ }^{(1,2,3)}$ | $I_{\text {D }}$ | +20 | mA |
| Supply Current ${ }^{(1,3,4)}$ | IDD | +50 | mA |
| Reverse Supply Voltage ${ }^{(1,2)}$ | $V_{\text {direv }}$ | -24 | V |
| Reverse Supply Voltage ${ }^{(1,4)}$ | $V_{\text {dorev }}$ | -30 | V |
| Reverse Supply Current ${ }^{(1,2,5)}$ | I dorev | -20 | mA |
| Reverse Supply Current ${ }^{(1,4,5)}$ | I dorev | -50 | mA |
| Maximum Junction Temperature ${ }^{(6)}$ | $\mathrm{T}_{\mathrm{J}}$ | +165 | ${ }^{\circ} \mathrm{C}$ |
| ESD Sensitivity - HBM ${ }^{(7)}$ | - | 8 | kV |
| ESD Sensitivity - System level ${ }^{(8)}$ | - | 15 | kV |
| ESD Sensitivity - CDM ${ }^{(9)}$ | - | 1000 | V |
| Magnetic Flux Density | B | Unlimited | mT |

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

[^0]
## 8. General Electrical Specifications

DC Operating Parameters $\mathrm{V}_{D D}=2.7 \mathrm{~V}$ to $24 \mathrm{~V}, \mathrm{~T}_{J}=-40^{\circ} \mathrm{C}$ to $165^{\circ} \mathrm{C}$ (unless otherwise specified)

| Parameter | Symbol | Test Conditions | Min | Typ ${ }^{(1)}$ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OFF Supply Current (selectable by a dedicated bit) | Ioff | $\mathrm{V}_{\mathrm{DD}}=3.5$ to 24 V | 2 | - | 5 | mA |
|  |  |  | 5 | - | 6.9 | mA |
| ON Supply Current | Ion | $\mathrm{V}_{\mathrm{DD}}=3.5$ to 24 V | 12 | - | 17 | mA |
| Reverse Supply current | $\mathrm{I}_{\text {d }}$ dev | $V_{D D}=-16 \mathrm{~V}$ | -1 | - | - | mA |
| Safe Mode Supply Current | $\mathrm{I}_{\text {SAFE }}$ |  | - | - | 1 | mA |
| Supply Current Rise/Fall Time ${ }^{(2)}$ | $\mathrm{t}_{\mathrm{R}} / \mathrm{t}_{\mathrm{F}}$ | $\mathrm{V}_{\mathrm{DD}}=12 \mathrm{~V}, \mathrm{C}_{\text {LOAD }}=50 \mathrm{pF}$ to GND | 0.1 | 0.3 | 1 | $\mu \mathrm{s}$ |
| Power-On Time ${ }^{(3,4)}$ | $\mathrm{t}_{\text {ON }}$ | $\mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}, \mathrm{dV} \mathrm{~V}_{\mathrm{DD}} / \mathrm{dt}>2 \mathrm{~V} / \mathrm{us},$ <br> activated output with $>1 \mathrm{mT}$ overdrive | - | 40 | 70 | $\mu \mathrm{s}$ |
| Chopping Frequency | $\mathrm{f}_{\text {CHOP }}$ |  | - | 350 | - | kHz |
| Delay Time ${ }^{(2,5)}$ | $t_{\text {D }}$ | Average over 1000 successive switching events @10kHz, Latch, $\mathrm{B}_{\mathrm{op}}$ set to 5 mT , triangle wave magnetic field with $\mathrm{B}> \pm 20 \mathrm{mT}$ | - | 7.5 | - | $\mu \mathrm{s}$ |
| Output Jitter (p-p) ${ }^{(2,6)}$ | $\mathrm{t}_{\text {JITTER }}$ | Over 1000 successive switching events @10kHz, Latch, Bop set to 5 mT , triangle wave magnetic field with $\mathrm{B}> \pm 20 \mathrm{mT}$ | - | $\pm 3.5$ | - | $\mu \mathrm{s}$ |
| Maximum Switching Frequency ${ }^{(2,7)}$ | $\mathrm{f}_{\text {sw }}$ | Latch, $\mathrm{B}_{\mathrm{Op}}$ set to 5 mT , triangle wave magnetic field with $\mathrm{B}> \pm 20 \mathrm{mT}$ | - | 50 | - | kHz |
| Under-voltage Lockout Threshold | $\mathrm{V}_{\text {UVL }}$ |  | - | - | 2.7 | V |
| Under-voltage Lockout Reaction time ${ }^{(2)}$ | $t_{\text {UvL }}$ |  | - | 1 | - | $\mu \mathrm{s}$ |
| Max Programming Supply Voltage | $V_{\text {DDprog }}$ |  | - | 22 | 28 | V |
| Integrated bypass capacitor | $\mathrm{C}_{\text {BP }}$ | Only for options MLX92242LUA-AAA- $1 x x$ and $-3 x x$ | - | 68 | - | nF |
| Thermal Protection Activation | $\mathrm{T}_{\text {PROT }}$ |  | - | $190^{(8)}$ | - | ${ }^{\circ} \mathrm{C}$ |
| Thermal Protection Release | $\mathrm{T}_{\text {REL }}$ |  | - | $180^{(8)}$ | - | ${ }^{\circ} \mathrm{C}$ |
| UA Package Thermal Resistance | $\mathrm{R}_{\text {THJA }}$ |  | - | 200 | - | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| SE Package Thermal Resistance | $\mathrm{R}_{\text {THJA }}$ |  | - | 300 | - | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

[^1]
## 9. Magnetic Specifications

### 9.1. MLX92242LUA / LSE-AAA-000 and MLX92242LUA-AAA-100

DC Operating Parameters $V_{D D}=3.5 \mathrm{~V}$ to $24 \mathrm{~V}, \mathrm{~T}_{J}=-40^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ (unless otherwise specified)


[^2]
### 9.2. MLX92242LSE-AAA-200 and MLX92242LUA-AAA-300

DC Operating Parameters $\mathrm{V}_{\mathrm{DD}}=3.5 \mathrm{~V}$ to $24 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=-40^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ (unless otherwise specified)

| Parameter | Symbol | Test Conditions | Min | Typ ${ }^{(1)}$ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Point Programming Range ${ }^{(2)}$ | $\mathrm{B}_{\text {op }}$ | $\mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | -40 | - | 40 | mT |
| Factory pre-programmed Operating Point, Latch | $\mathrm{B}_{\text {op }}$ | $V_{D D}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C},$ <br> programming target 12 mT | 9 | 12 | 15 | mT |
| Factory pre-programmed Release Point, Latch | $B_{\text {RP }}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}, \\ & \text { programming target } 12 \mathrm{mT} \end{aligned}$ | -15 | -12 | -9 | mT |
| $\mathrm{B}_{\text {OP }}$ Magnitude Programming ${ }^{(3)}$ |  |  | - | 8 | - | bit |
| $B_{\text {op }}$ Polarity Selection |  |  | - | 1 | - | bit |
| $\mathrm{B}_{\mathrm{OP}} / \mathrm{B}_{\mathrm{RP}}$ Temperature Coefficient Programming Range ${ }^{(4,5)}$ | TC ${ }^{(6)}$ | $\begin{aligned} & V_{D D}=5 \mathrm{~V}, \text { Latch with } \\ & B_{O P}=12 \mathrm{mT}, B_{R P}=-12 \mathrm{mT} \end{aligned}$ | -2000 | - | 0 | ppm/ ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{B}_{\mathrm{OP}} / \mathrm{B}_{\mathrm{RP}}$ Temperature Coefficient Programming |  |  | - | 5 | - | bit |
| Switch/Latch Function Selection |  |  | - | 1 | - | bit |
| Proportional/Absolute Hysteresis Selection |  |  | - | 1 | - | bit |
| Hysteresis Magnitude Programming ${ }^{(7)}$ |  |  | - | 5 | - | bit |
| Proportional Hysteresis Ratio Programming Range $\mathrm{HYS}_{\text {RATIO }}=\mathrm{B}_{\text {HYSPR }} / \mathrm{B}_{\mathrm{OP}}{ }^{(4)}$ | HYS RATIO | Programming step 0.025 | 0.025 | - | 0.800 | - |
| Absolute Hysteresis Programming Range ${ }^{(4)}$ | $\mathrm{B}_{\text {HYSABS }}$ | Programming step 0.1mT | 0 | - | 3.1 | mT |
| Absolute Hysteresis tolerance ${ }^{(8)}$ | $\mathrm{B}_{\text {HYstoL }}$ | $\begin{aligned} & \mathrm{B}_{\text {HYSABS }}=1.5 \mathrm{mT} \text { to } 3.1 \mathrm{mT}, \\ & \mathrm{~B}_{\mathrm{OP}}=3 \mathrm{mT}, \mathrm{~V}_{\mathrm{DD}}=3.5 \text { to } 14 \mathrm{~V} \end{aligned}$ | -1.2 | - | 1.2 | mT |
| $\mathrm{B}_{\mathrm{OP}}, \mathrm{B}_{\mathrm{RP}}$ and $\mathrm{B}_{\text {HYS }}$ life time drift |  | HTOL 1000h at $\mathrm{T}_{\mathrm{j}}=165^{\circ} \mathrm{C}$ | $(4 \%+0.2$ <br> mT ) |  | 4\%+0.2mT |  |
| Output Polarity Selection |  |  | - | 1 | - | bit |
| $V_{\text {DD }}$ Programming Lock |  |  | - | 1 | - | bit |

[^3]

South active pole (IMC version)


South active pole


## South active pole



South active pole (IMC version)


North active pole (IMC version)


North active pole


North active pole


North active pole (IMC version)

## 10. Magnetic Behavior

### 10.1. Latch sensor



South Active Pole


North Active Pole

### 10.2. Unipolar Switch sensor



Direct South Active Pole


Inverted South Active Pole


Direct North Active Pole


Inverted North Active Pole

## 11. Application Information

### 11.1. Typical Automotive Application Circuit



### 11.2. Automotive and Harsh, Noisy Environments Application Circuit



## Notes:

1. For proper operation, a 10 to 100 nF bypass capacitor should be placed as close as possible to the $\mathrm{V}_{\mathrm{DD}}$ and ground (GND) pin. For MLX92242LUA-AAA- 1 xx and -3 xx C1 is not required. 2. The device can tolerate positive voltages up to $+28(+32) \mathrm{V}$ and negative voltages down to $-24(-30) \mathrm{V}$.
If bigger transients over the supply line are expected the usage of D1 and DZ1 ( $24 . . .27 \mathrm{~V}$ ) is recommended.
The series resistor R1 is used to limit the current through DZ1 and to improve the EMC performance.

### 11.3. Power Dissipation Derating Curve



### 11.4. Voltage Derating Curve



## 12. Standard information regarding manufacturability of Melexis products with different soldering processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

## Reflow Soldering SMD's (Surface Mount Devices)

- IPC/JEDEC J-STD-020

Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)

- EIA/JEDEC JESD22-A113

Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing
(reflow profiles according to table 2)

## Wave Soldering SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EN60749-20

Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat

- EIA/JEDEC JESD22-B106 and EN60749-15

Resistance to soldering temperature for through-hole mounted devices

## Iron Soldering THD's (Through Hole Devices)

- EN60749-15

Resistance to soldering temperature for through-hole mounted devices

## Solderability SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EIA/JEDEC JESD22-B102 and EN60749-21

Solderability
For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

Melexis recommends reviewing on our web site the General Guidelines soldering recommendation (http://www.melexis.com/Quality soldering.aspx) as well as trim\&form recommendations (http://www.melexis.com/Assets/Trim-and-form-recommendations-5565.aspx).

Melexis is contributing to global environmental conservation by promoting lead free solutions. For more information on qualifications of RoHS compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website:
http://www.melexis.com/quality.aspx

## 13. ESD Precautions

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD).
Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

## 14. Package Information

### 14.1. SE (TSOT-3L) Package Information



Notes:

1. All dimensions are in millimeters
2. Outermost plastic extreme width does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.15 mm per side.
3. Outermost plastic extreme length does not include mold flash or protrusions Mold flash and protrusions shall not exceed 0.25 mm per side.
4. The lead width dimension does not include dambar protrusion. Allowable dambar protrusion shall be 0.07 mm total in excess of the lead width dimension at maximum material condition.
5. Dimension is the length of terminal for soldering to a substrate.
6. Formed lead shall be planar with respect to one another with 0.076 mm at seating plane.

Marking:
TOP:
Option -0xx: 42WW
Option -2xx: 43WW
WW: Assembly week
BOTTOM:
YLLL
Y: Assembly Year
LLL: Last 3 digits from lot\#


|  | A | A1 | A2 | D | E | E1 | L | b | c | e | e1 | $\boldsymbol{\alpha}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\min$ | - | 0.025 | 0.85 | 2.80 | 2.60 | 1.50 | 0.30 | 0.30 | 0.10 | 0.95 | 1.90 | $0^{\circ}$ |
| $\max$ | 1.00 | 0.10 | 0.90 | 3.00 | 3.00 | 1.70 | 0.50 | 0.45 | 0.20 | BSC | BSC | $8^{\circ}$ |

Notes:

1. Dimension " D " and "E1" do not include mold flash or protrusions. Mold flash or protrusion shall not exceed 0.15 mm on " $D$ " and 0.25 mm on " E " per side.
2. Dimension " $b$ " does not include dambar protrusion.

| SE Pin № | Name | Type | Function |
| :--- | :--- | :--- | :--- |
| 1 | VDD | Supply | Supply Voltage pin |
| 2 | TEST | I/O | For Melexis use only |
| 3 | GND | Ground | Ground pin |



### 14.2. UA (TO92-3L) Package Information



## Hall plate location



|  | A | D | E | F | J | L | L1 | S | b1 | b2 | c | e | e1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\min$ | 2.80 | 3.90 | 1.40 | 0.00 | 2.51 | 14.0 | 0.90 | 0.63 | 0.35 | 0.43 | 0.35 | 2.51 | 1.24 |
| $\max$ | 3.20 | 4.30 | 1.60 | 0.15 | 2.72 | 15.0 | 1.10 | 0.84 | 0.44 | 0.52 | 0.44 | 2.57 | 1.30 |
|  | $\theta 1$ | $\theta 2$ | $\theta 3$ | $\theta 4$ |  |  |  |  |  |  |  |  |  |
| $\min$ | $7^{\circ}$ | $7^{\circ}$ | $45^{\circ}$ | $7^{\circ}$ |  |  |  |  |  |  |  |  |  |
| $\max$ | REF | REF | REF | REF |  |  |  |  |  |  |  |  |  |

Notes:

1. All dimensions are in millimeters.
2. Mold flashes and protrusion are not included.
3. Gate burrs shall not exceed 0.127 mm on the top side.

| UA Pin № | Name | Type | Function |
| :---: | :--- | :--- | :--- |
| 1 | VDD | Supply | Supply Voltage pin |
| 2 | GND | Ground | Ground pin |
| 3 | TEST | I/O | For Melexis use only |



### 14.3. UA (TO92-3L) with integrated capacitor Package Information



## Hall plate location



|  | A | D | E | F | J | L | L1 | S | b1 | b2 | c | e | e1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\min$ | 2.80 | 3.90 | 1.40 | 0.00 | 2.51 | 14.0 | 0.90 | 0.63 | 0.35 | 0.43 | 0.35 | 2.51 | 1.24 |
| $\max$ | 3.20 | 4.30 | 1.60 | 0.15 | 2.72 | 15.0 | 1.10 | 0.84 | 0.44 | 0.52 | 0.44 | 2.57 | 1.30 |
|  | $\theta 1$ | $\theta 2$ | $\theta 3$ | $\theta 4$ |  |  |  |  |  |  |  |  |  |
| $\min$ | $7^{\circ}$ | $7^{\circ}$ | $45^{\circ}$ | $7^{\circ}$ |  |  |  |  |  |  |  |  |  |
| $\max$ | REF | REF | REF | REF |  |  |  |  |  |  |  |  |  |

Notes:

1. All dimensions are in millimeters
2. Mold flashes and protrusion are not included.
3. Gate burrs shall not exceed 0.127 mm on the top side.


## 15. Contact

For the latest version of this document, go to our website at www.melexis.com.

For additional information, please contact our Direct Sales team and get help for your specific needs:

| Europe, Africa | Telephone: +3213670495 |
| :--- | :--- |
|  | Email : sales_europe@melexis.com |
| Americas | Telephone: +16032232362 |
|  | Email : sales_usa@melexis.com |
| Asia | Email : sales_asia@melexis.com |

## 16. Disclaimer

The information furnished by Melexis herein ("Information") is believed to be correct and accurate. Melexis disclaims (i) any and all lia bility in connection with or arising out of the furnishing, performance or use of the technical data or use of the product(s) as described herein ("Product") (ii) any and all liability, including without limitation, special, consequential or incidental damages, and (iii) any and all warranties, express, statutory, implied, or by description, including warranties of fitness for particular purpose, non-infringement and merchantability. No obligation or liability shall arise or flow out of Melexis' rendering of technical or other services.
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Users or any third party must further determine the suitability of the Product for its application, including the level of reliability required and determine whether it is fit for a particular purpose
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This document as well as the Product(s) may be subject to export control regulations. Please be aware that export might require a prior authorization from competent authorities.
The Product(s) are intended for use in normal commercial applications. Unless otherwise agreed upon in writing, the Product(s) are not designed, authorized or warranted to be suitable in applications requiring extended temperature range and/or unusual environmental requirements. High reliability applications, such as medical life-support or life-sustaining equipment are specifically not recommended by Melexis.
The Product(s) may not be used for the following applications subject to export control regulations: the development, production, processing, operation, maintenance, storage, recognition or proliferation of 1) chemical, biological or nuclear weapons, or for the development, production, maintenance or storage of missiles for such weapons: 2) civil firearms, including spare parts or ammunition for such arms; 3) defense related products, or other material for military use or for law enforcement; 4) any applications that, alone or in combination with other goods, substances or organisms could cause serious harm to persons or goods and that can be used as a means of violence in an armed conflict or any similar violent situation.
The Products sold by Melexis are subject to the terms and conditions as specified in the Terms of Sale, which can be found at https://www.melexis.com/en/legal/terms-andconditions.

This document supersedes and replaces all prior information regarding the Product(s) and/or previous versions of this document

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ISO/TS 16949 and ISO14001 Certified


[^0]:    ${ }^{1}$ The maximum junction temperature should not be exceeded
    ${ }^{2}$ For maximum 1 hour
    ${ }^{3}$ Including current through protection device
    ${ }^{4}$ For maximum 500 ms
    ${ }^{5}$ Through protection device
    ${ }^{6}$ For 1000 hours
    ${ }^{7}$ Human Model according AEC-Q100-002 standard
    ${ }^{8}$ Indirect discharge according VW TL82466 standard, typical value, only for option MLX92242LUA-AAA-1xx and -3xx
    ${ }^{9}$ Charged Device Model according AEC-Q100-011 standard

[^1]:    ${ }^{1}$ Typical values are defined at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ and $\mathrm{V}_{\mathrm{DD}}=12 \mathrm{~V}$.
    ${ }^{2}$ Guaranteed by design and verified by characterization, not production tested.
    ${ }^{3}$ The Power-On Time represents the time from reaching $\mathrm{V}_{\mathrm{DD}}=2.7 \mathrm{~V}$ to the first refresh of the supply current state
    ${ }^{4}$ Power-On Slew Rate is not critical for the proper device start-up.
    ${ }^{5}$ The Delay Time is the time from magnetic threshold reached to the start of the output switching.
    ${ }^{6}$ Output jitter is the unpredictable deviation of the Delay time.
    ${ }^{7}$ Maximum switching frequency corresponds to the maximum frequency of the applied magnetic field which is detected without loss of pulses.
    ${ }^{8} T_{\text {PROT }}$ and $T_{\text {REL }}$ are the corresponding junction temperature values.

[^2]:    ${ }^{1}$ Typical values are defined at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ and $\mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}$
    ${ }^{2}$ Guaranteed by correlation with production test at $\mathrm{B}=28 \mathrm{mT}$ and verified by characterization
    ${ }^{3}$ The programming step is typically from $0.4 \%$ to $0.8 \%$ of the programmed $B_{\text {op }}$ value for $\left|B_{\text {op }}\right| \geq 2 \mathrm{mT}$ and 0.016 mT for $\left|\mathrm{B}_{\text {op }}\right| \leq 2 \mathrm{mT}$
    ${ }^{4}$ The minimum and maximum limits are typical values
    ${ }^{5}$ The factory pre-programmed TC value is $-2000 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$
    ${ }^{6}$ The $B_{o p} / B_{R P}$ Temperature Coefficient is calculated using the following formula:

    $$
    T C=\frac{\left(B_{O P T 2}-B_{R P T 2}\right)-\left(B_{O P T 1}-B_{R P T 1}\right)}{\left(B_{O P T 1}-B_{R P T 1}\right) *\left(T_{2}-T_{1}\right)} * 10^{6}, \frac{\mathrm{ppm}}{{ }^{\circ} \mathrm{C}} ; T_{1}=25^{\circ} \mathrm{C} ; T_{2}=150^{\circ} \mathrm{C}
    $$

    ${ }^{7}$ Programming of very low hysteresis magnitude ( $<1 \mathrm{mT}$ ) could lead to output toggling due to noise and mechanical looseness in the magnetic system. As $T_{j}=T_{A}+V_{D D}{ }^{*} I_{D D} * R_{T H A}$ the change in the junction temperature due to $I_{D D}$ switching between $I_{O N}$ and $I_{\text {OFF }}$ in combination with the device TC could cause $B_{\text {op }}$ or $B_{R P}$ shift. If the chosen magnetic hysteresis is close or below the above shift and inverted output polarity is selected, an output toggling could appear
    ${ }^{8}$ Including life time drift. Guaranteed by correlation with production test at $B=3 \mathrm{mT}, \mathrm{T}_{j}=+25^{\circ} \mathrm{C}$ and verified by characterization

[^3]:    ${ }^{1}$ Typical values are defined at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ and $\mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}$
    ${ }^{2}$ Guaranteed by correlation with production test at $\mathrm{B}=12 \mathrm{mT}$ and verified by characterization
    ${ }^{3}$ The programming step is typically from $0.4 \%$ to $0.8 \%$ of the programmed $B_{\text {op }}$ value for $\left|B_{\text {op }}\right| \geq 2 m T$ and 0.016 mT for $\left|\mathrm{B}_{\text {op }}\right| \leq 2 \mathrm{mT}$
    ${ }^{4}$ The minimum and maximum limits are typical values
    ${ }^{5}$ The factory pre-programmed TC value is $0 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$
    ${ }^{6}$ The $B_{o p} / B_{R P}$ Temperature Coefficient is calculated using the following formula:

    $$
    T C=\frac{\left(B_{O P T 2}-B_{R P T 2}\right)-\left(B_{O P T 1}-B_{R P T 1}\right)}{\left(B_{O P T 1}-B_{R P T 1}\right) *\left(T_{2}-T_{1}\right)} * 10^{6}, \frac{\mathrm{ppm}}{{ }^{\circ} \mathrm{C}} ; T_{1}=25^{\circ} \mathrm{C} ; T_{2}=150^{\circ} \mathrm{C}
    $$

    ${ }^{7}$ Programming of very low hysteresis magnitude $(<1.5 \mathrm{mT})$ could lead to output toggling due to noise and mechanical looseness in the magnetic system. As $T_{J}=T_{A}+V_{D D}{ }^{*} I_{D D} * R_{T H A A}$ the change in the junction temperature due to $I_{D D}$ switching between $I_{O N}$ and $I_{\text {OFF }}$ in combination with the device $T C$ could cause $\mathrm{B}_{\text {op }}$ or $\mathrm{B}_{\text {RP }}$ shift. If the chosen magnetic hysteresis is close or below the above shift and inverted output polarity is selected, an output toggling could appear
    ${ }^{8}$ Guaranteed by correlation with production test at $\mathrm{B}=3 \mathrm{mT}, \mathrm{T}_{j}=+25^{\circ} \mathrm{C}$ and verified by characterization

